



All dimensions are in mm; tolerances according to ISO 2768 m-H

Interface

According to IEC 61169-16

Contents and Documentation

This kit is delivered with

- **Standard Definitions Card**
Printed Standard Definitions that can be used on nearly all Vector Network Analyzers
- **Test Results Documentation**
- **Lanyard**
- **Hard Shell Case**
- **Protection Caps**

Material and plating

Connector parts

Center conductor
Outer conductor
Body
Dielectric
Substrate

Material

CuBe
Stainless steel
Aluminum
PPE
Al₂O₃

Plating

Gold, min. 1.27 μm, over nickel
Passivated
black anodized

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Electrical data

Frequency range DC to 18 GHz

Open

Error from nominal phase¹
 ≤ 2.0°, DC to 6 GHz
 ≤ 3.0°, 6 GHz to 9 GHz
 ≤ 4.0°, 9 GHz to 18 GHz

Short

Error from nominal phase²
 ≤ 1.5°, DC to 6 GHz
 ≤ 2.0°, 6 GHz to 9 GHz
 ≤ 2.5°, 9 GHz to 18 GHz

Load

Return loss
 ≥ 42 dB, DC to 6 GHz
 ≥ 36 dB, 6 GHz to 9 GHz
 ≥ 30 dB, 9 GHz to 18 GHz

DC-Resistance 50 Ω ± 0.5 Ω
 Power handling (at 25 °C, sea level) ≤ 1.0 W, derate by 0.01 W/K

¹ The nominal phase is defined by the Offset Delay, the Offset Loss and the Fringing Capacitances

² The nominal phase is defined by the Offset Delay, the Offset Loss and the Short Inductance

Mechanical data

Mating cycles ≥ 500
 Maximum torque 1.70 Nm
 Recommended torque 1.10 Nm
 Gauge 5.22 mm to 5.26 mm

General standard definitions

For proper operation the vector network analyzer (VNA) needs a model describing the electrical behaviour of this calibration standard. The different models, units, and terms used will depend on the VNA type and they will have to be entered into the VNA. All values are based on typical geometry and plating.

Open

Offset Z₀ / Impedance / Z₀ 50 Ω
 Offset Delay 40.028 ps
 Length (electrical) / Offset Length 12.00 mm
 Offset Loss 0.80 GΩ/s
 Loss 0.0056 dB/√GHz
 Fringing Capacitances
 C₀ = 37.1000 x 10⁻¹⁵ F / 37.1000 fF
 C₁ = 1200.00 x 10⁻²⁷ F/Hz / 1.20000 fF /GHz
 C₂ = -30.0000 x 10⁻³⁶ F/Hz² / -0.03000 fF /GHz²
 C₃ = 0.00000 x 10⁻⁴⁵ F/Hz³ / 0.00000 fF /GHz³