

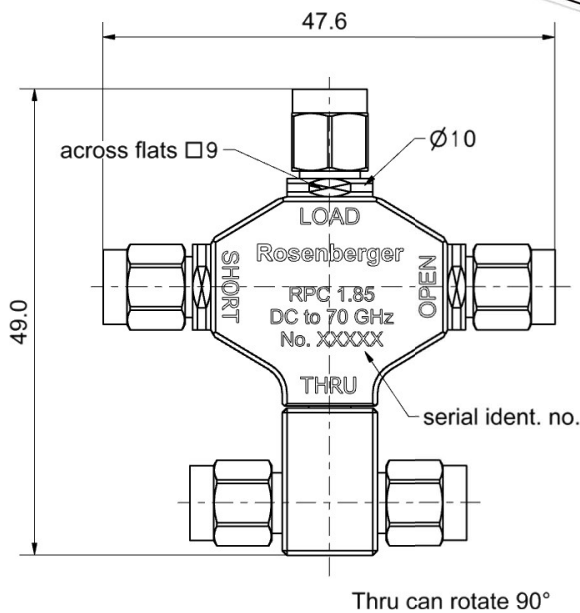
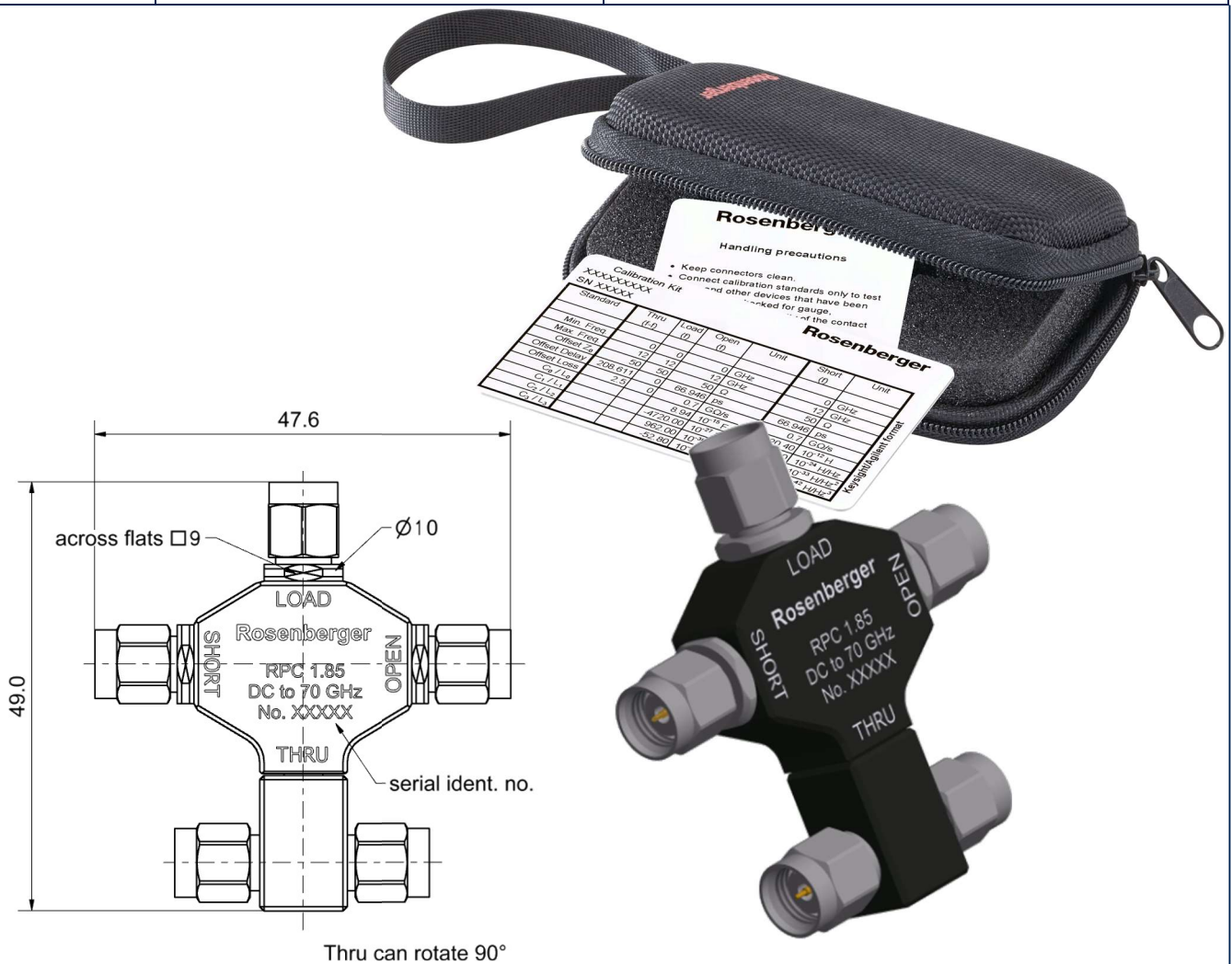
Technical Data Sheet

Rosenberger

RPC-1.85

Calibration Kit
Plug

08S30R-MSOTS3



All dimensions are in mm; tolerances according to ISO 2768 m-H

Interface

According to IEC 61169-32
Mechanically compatible with RPC-2.40

Contents and Documentation

This kit is delivered with

- **Standard Definitions Card**
Printed Standard Definitions that can be used on nearly all Vector Network Analyzers
- **Test Results Documentation**
- **Hard Shell Case**
- **Protection Caps**

Material and plating

Connector parts

Center conductor
Outer conductor
Coupling nut
Body
Dielectric
Substrate

Material

CuBe
Stainless steel
Stainless steel
Aluminum
PS
Al₂O₃

Plating

Gold, min. 1.27 µm, over nickel
Passivated
Passivated
black anodized

Electrical data

Frequency range DC to 70.0 GHz

Thru

Return loss ≥ 28 dB, DC to 4 GHz
 ≥ 21 dB, 4 GHz to 26.5 GHz
 ≥ 20 dB, 26.5 GHz to 50 GHz
 ≥ 17 dB, 50 GHz to 70 GHz

Open

Error from nominal phase¹ $\leq 2.0^\circ$, DC to 4 GHz
 $\leq 5.0^\circ$, 4 GHz to 26.5 GHz
 $\leq 7.0^\circ$, 26.5 GHz to 50 GHz
 $\leq 10.0^\circ$, 50 GHz to 70 GHz

Short

Error from nominal phase² $\leq 2.0^\circ$, DC to 4 GHz
 $\leq 5.0^\circ$, 4 GHz to 26.5 GHz
 $\leq 7.0^\circ$, 26.5 GHz to 50 GHz
 $\leq 10.0^\circ$, 50 GHz to 70 GHz

Load

Return loss ≥ 35.0 dB, DC to 4 GHz
 ≥ 25.0 dB, 4 GHz to 26.5 GHz
 ≥ 22.0 dB, 26.5 GHz to 50 GHz
 ≥ 20.0 dB, 50 GHz to 70 GHz

DC Resistance $50 \Omega \pm 0.5 \Omega$

Power handling (at 25 °C, sea level) ≤ 0.5 W, derate by 0.005 W/K

¹ The nominal phase is defined by the Offset Delay, the Offset Loss and the Fringing Capacitances

² The nominal phase is defined by the Offset Delay, the Offset Loss and the Short Inductance

Mechanical data

Mating cycles ≥ 500
 Maximum torque 1.65 Nm
 Recommended torque 0.90 Nm
 Gauge 0.00 mm to 0.05 mm

General standard definitions

For proper operation the vector network analyzer (VNA) needs a model describing the electrical behaviour of this calibration standard. The different models, units, and terms used will depend on the VNA type and they will have to be entered into the VNA. All values are based on typical geometry and plating.

Thru

Offset Z_0 / Impedance / Z_0 50 Ω
 Offset Delay 84.492 ps
 Length (electrical) / Offset Length 25.33 mm
 Offset Loss 4.00 G Ω /s
 Loss 0.0294 dB/ $\sqrt{\text{GHz}}$
 Line Loss @ 1GHz 0.0012 dB/mm

Load

Offset Z_0 / Impedance / Z_0 50 Ω
 Offset Delay 0.0000 ps
 Length (electrical) / Offset Length 0.000 mm
 Offset Loss 0.00 G Ω /s
 Loss 0.0000 dB/ $\sqrt{\text{GHz}}$