

LOC	DIST	REVISIONS					
		P	LTN	DESCRIPTION	DATE	DMN	APVD
GP	00	C		REV PER ECO 14-10695	7-16-14	CT	DH

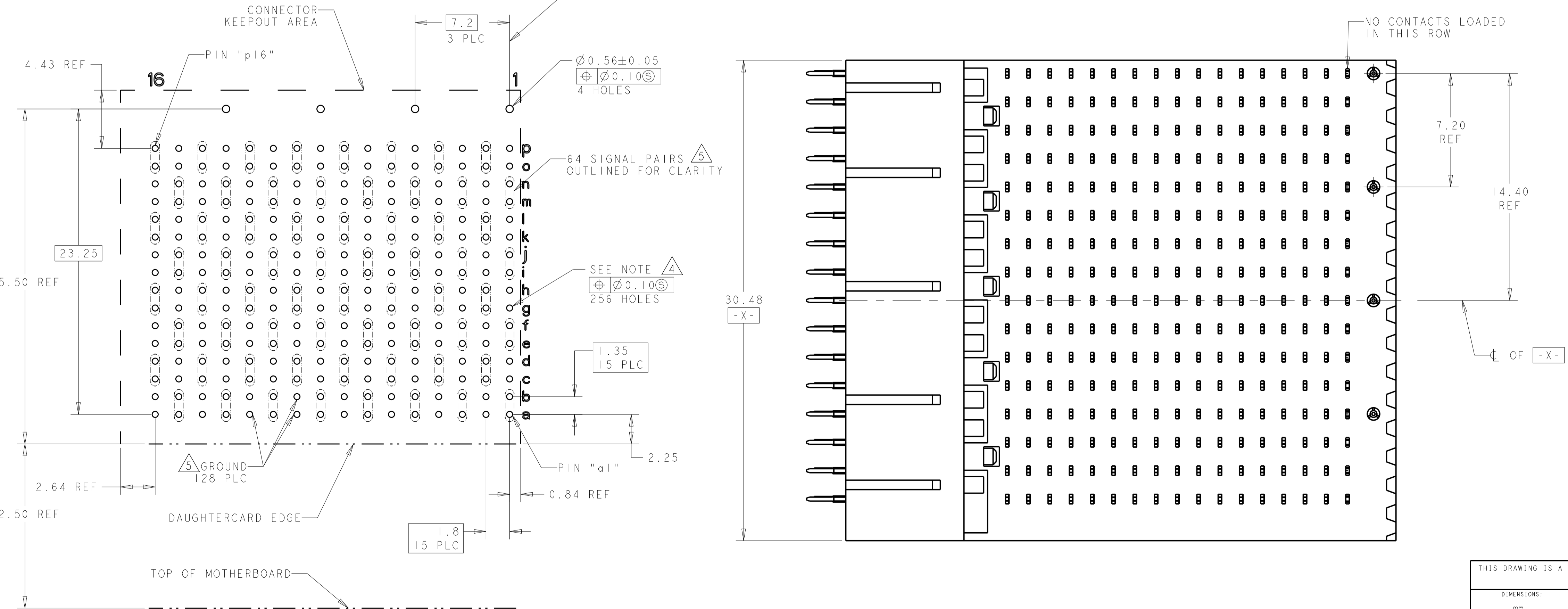
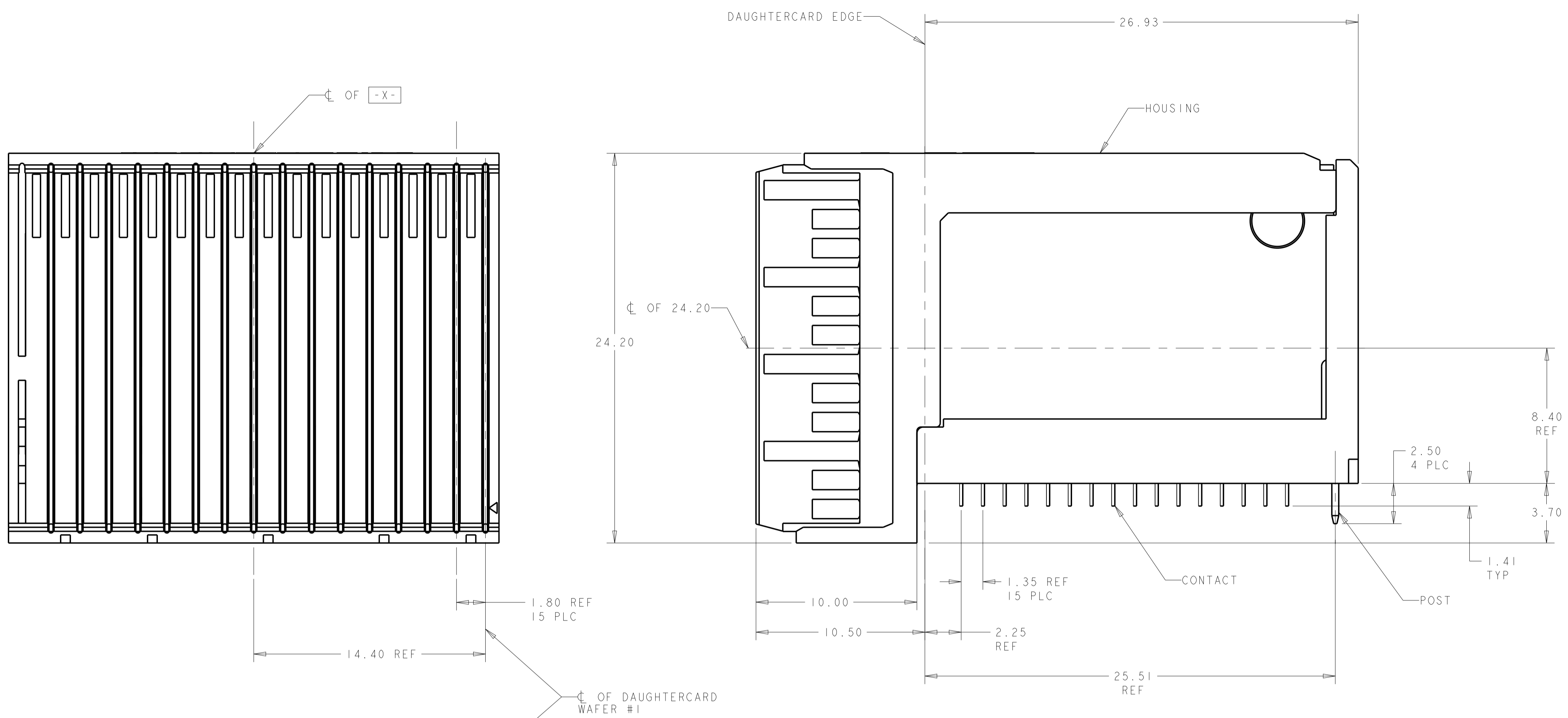
- 1 HOUSING: LCP, UL94V0, COLOR: BLACK.
CONTACT: PHOSPHOR BRONZE.
POST: BRASS WIRE
- 2 CONTACT: 1.27µm MIN. TIN-LEAD ON PCB TAIL
OVER 1.27µm MIN. NICKEL OVER ALL.
POST: 1.27µm MIN NICKEL PLATED.
- 3 CONTACT: 1.27µm MIN. TIN ON PCB TAIL
OVER 1.27µm MIN. NICKEL OVER ALL.
POST: 1.27µm MIN NICKEL PLATED.
- 4 MANUFACTURING TOLERANCE FOR:
Ø0.46±0.05 FINISHED HOLE WITH Sn PB PLATING:
DRILLED HOLE = Ø0.55±0.02
COPPER PLATING = 0.025-0.050
Sn PB PLATING = 0.0038-0.0124
Ø0.475±0.05 FINISHED HOLE WITHOUT Sn PB PLATING:
DRILLED HOLE = Ø0.55±0.02
COPPER PLATING = 0.025-0.050
- 5 SEE TABLE 1 FOR INTERCONNECTIONS TO
BACKPLANE CONNECTOR.
- 6 TE PART NO AND DATE CODE MARKED ON THIS ASSEMBLY.

TABLE 1
INTERCONNECTIONS WITH BACKPLANE CONNECTOR 1410128

TYPICAL INTERCONNECTIONS FOR EACH ODD-NUMBERED COLUMN (WAFER): 1, 3, 5, 7, 9, 11, 13, 15		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	ax bx	ax bx
SIGNAL PAIR	ex fx	dx ex
SIGNAL PAIR	ix jx	gx hx
SIGNAL PAIR	mx nx	jx kx
GROUPS	cx, dx, gx, hx, kx, lx, ox, px (ALL COMMONED)	cx, fx, ix, lx

TYPICAL INTERCONNECTIONS FOR EACH EVEN-NUMBERED COLUMN (WAFER): 2, 4, 6, 8, 10, 12, 14, 16		
CONTACT USAGE	DAUGHTERCARD CONNECTOR PIN	BACKPLANE CONNECTOR PIN
SIGNAL PAIR	cx dx	bx cx
SIGNAL PAIR	gx hx	ex fx
SIGNAL PAIR	kx lx	hx ix
SIGNAL PAIR	ox px	kx lx
GROUPS	ax, bx, ex, fx, ix, jx, mx, nx (ALL COMMONED)	ax, dx, gx, jx

NOTE: "x" DESIGNATES THE COLUMN NUMBER.



REMARKS	PLATING	PART NO
6	3	1410125-2
6	2	1410125-1

THIS DRAWING IS A CONTROLLED DOCUMENT. DWG: 03SEP2002, CHK: 03SEP2002, J. CONSOLI, G. GRIFFITH, 13MAR2006.

TE Connectivity

RIGHT-ANGLE PLUG ASSEMBLY, RIGHT END, 25.4mm, MultiGig RT2, DIFFERENTIAL, DAUGHTERCARD CONNECTOR

SCALE 6:1 SHEET 1 OF 1 REV C