

Programmable, Isolated Voltage-to-Current Converter

1**B**22

FEATURES

Internal Isolated Loop Supply Drives 1000 Ω Load Pin Programmable Inputs: 0 V to +5 V or 0 V to +10 V Pin Programmable Outputs: 4 to 20 mA or 0 to 20 mA High CMV Isolation: 1500 V RMS Normal-Mode Output Protection: 240 V RMS High Accuracy Low Offset Tempco: ±300 nA/°C Low Gain Tempco: ±50 ppm/°C Low Nonlinearity: ±0.02%

High CMR: 90 dB min Small Package: $1.0" \times 2.10" \times 0.35"$ Meets IEEE STD 472: CMV Transient Protection (SWC)

APPLICATIONS

Multichannel Process Control D/A Converter—Current Loop Interface Analog Transmitters and Controllers Remote Data Acquisition Systems

GENERAL DESCRIPTION

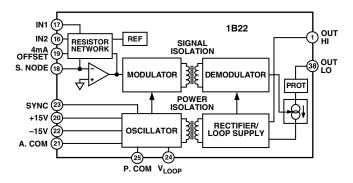
The 1B22 is an isolated voltage-to-current converter that incorporates transformer isolation to achieve high performance and automated surface mount manufacturing for low cost and increased reliability. Designed for industrial applications, it is especially suited for harsh environments with extremely high common-mode interference. With programmable inputs and outputs, the 1B22 provides an unbeatable combination of versatility and performance in a compact plastic package.

Functionally, the V/I converter consists of four basic sections: input conditioning, modulator/demodulator, isolated loop supply and current source (Figure 1). The 1B22 is pin programmable for 0 V to +5 V or 0 V to +10 V inputs and 0 to 20 mA or 4 to 20 mA outputs using an internal resistor network. It can also be set by an external resistor to accept 0 V to +1 V to 0 V to +10 V inputs. Transformer coupling provides 1500 V rms galvanic isolation between the inputs and the current loop. Nonlinearity is an excellent $\pm 0.05\%$ max.

Loop power is generated internally through a dc/dc converter and is also isolated from the input side (1500 V rms). Loop compliance voltage is dependent on the voltage supplied to the 1B22, and with $V_{LOOP} = 28$ V, it is sufficient to drive a 1000 Ω load.

The 1B22 is fully specified over -25° C to $+85^{\circ}$ C and operates over the industrial (-40° C to $+85^{\circ}$ C) temperature range.

FUNCTIONAL BLOCK DIAGRAM



DESIGN FEATURES AND USER BENEFITS

Isolated Loop Power: Internal loop supply completely isolates the loop from the input terminals (1500 V rms) and provides the capability to drive 0 Ω to 1000 Ω loads. This eliminates the need for an external dc/dc converter.

Ease of Use: The 1B22 offers complete isolated voltage-tocurrent conversion with minimum external parts required to get a conditioned current signal. No external buffers or drivers are required.

High CMV Isolation: The 1B22 features high input to output galvanic isolation to eliminate ground loops and offer protection against damage from transients and fault voltages. The isolation barrier will withstand continuous CMV of 1500 V rms and meets the IEEE Standard for Common-Mode Voltage Transient Protection (STD 472-SWC).

Small Size: The 1B22 package size $(1.0" \times 2.1"$ DIP) makes it an excellent choice in multichannel systems for maximum channel density. The 0.35" height also facilitates applications with limited board clearance.

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$1B22 - SPECIFICATIONS (typical @ +25°C and V_S = \pm 15 V, V_{L00P} = +24 V, unless otherwise noted)$

Model	1B22AN	- OUTLINE DIMENSIONS
INPUT SPECIFICATIONS		Dimensions shown in inches and (mm).
Factory Calibrated, User Selectable	0 V to +5 V, 0 V to +10 V	
Input Impedance		0.35
0 V to +10 V Input Range	50 kΩ	0.03 (8.9)
0 V to +5 V Input Range	25 kΩ	
OUTPUT SPECIFICATIONS		
Current Output Range, User Selectable	4 to 20 mA, 0 to 20 mA	0.80 (20.3) TYP
Load Compliance Range, $V_{LOOP} = +15 \text{ V}$	8 V min	
$V_{\text{LOOP}} = +28 \text{ V}$	20 V min	
Maximum Output Current @ Input Overload	30 mA	
Output Noise, 100 Hz Bandwidth	300 nA p-p	
NONLINEARITY (% of Span)	$\pm 0.02\%$ ($\pm 0.05\%$ max)	– <u>BOTTOM</u> 2.10 (53.3)
ISOLATION		
CMV, Input to Output Continuous	1500 V rms max	1.80
CMR, @ 60 Hz	90 dB min	(45.7) TYP
Normal-Mode Output Protection	240 V rms Continuous	0.1
CMV Transient Protection	IEEE-STD 472 (SWC)	(2.54) TYP A
ACCURACY		
Warm-Up Time to Rated Performance	5 min	
Total Output Error @ +25°C	5 11111	
Offset ($V_{IN} = 0$ V)	±60 μA	
Span ($V_{IN} = +10$ V)	$\pm 0.7\%$ Full Scale	' ТҮР
vs. Temperature $(-25^{\circ}C \text{ to } +85^{\circ}C)$		
Offset	±300 nA/°C	ACINE MATINE SOCKET
Span	±50 ppm/°C	AC1225 MATING SOCKET
DYNAMIC RESPONSE		2.10 (53.3)
Settling Time to 0.1% of FS for 10 V Step	9 ms	0.30 (7.5) 1.50 (37.5) 0.0
Small Signal Bandwidth	400 Hz	
	400 112	- 1.00 0.800 0.100 DIA, C/S TO (25) (20) 0.180 DIA, TYP
POWER SUPPLY		$\begin{vmatrix} 25 \\ -1 \\ -1 \\ -1 \\ -1 \\ -1 \\ -1 \\ -1 \\ -$
Bipolar Input Supplies		0.50
Operating Voltage	$\pm 15 \text{ V} \pm 5\%$	
Quiescent Current	±7.5 mA	
Power Supply Rejection	$\pm 0.01\%/V$	1.800 (2.54) - 0.300 (7.5)
Loop Supply		(area) (1.04
Operating Voltage	+14 V to +30 V	
Operating Current, at Full-Scale Output	25 mA	PIN DESIGNATIONS
Loop Supply Rejection	±0.005%/V	
ENVIRONMENTAL		Pin Function
Temperature Range		1 OUT HI
Rated Performance	–25°C to +85°C	16 IN2
Operating	–40°C to +85°C	17 IN1
Storage	-40°C to +85°C	
Relative Humidity, Noncondensing	0 to 95% @ 60°C	18 S. NODE
CASE SIZE	1.0" × 2.1" × 0.35"	- 19 4 mA OFFSET
	1.0 ~ 2.1 ~ 0.33	– 20 +15 V
Specifications subject to change without notice.		21 ANA COM

22

23

24

25 38 -15 V

SYNC

VLOOP

OUT LO

POWER COM

INSIDE THE 1B22

The 1B22 produces an isolated 4 to 20 mA or 0 to 20 mA output current which is proportional to the input voltage and independent of the output load resistance (Figure 1). The input stage is configured as an inverting amplifier with a resistor network to provide pin-strappable input ranges of 0 V to +5 V and 0 V to +10 V and output ranges of 0 to 20 mA and 4 to 20 mA. The conditioned signal is modulated to generate a square wave that drives transformer T1. The peak-to-peak amplitude of the signal is proportional to V_{IN}. An internal, high stability reference with a nominal output voltage of +6.4 V is used to develop a 4 mA offset for the 4 to 20 mA current loop output.

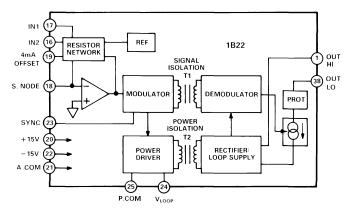


Figure 1. 1B22 Functional Block Diagram

After passing through signal transformer T1, the amplitude modulated signal is demodulated and filtered by a single pole filter. This filtered output is the control signal for the voltageto-current converter stage. Timing information for the demodulator is derived from the power transformer T2. The 1B22 outputs are protected from accidental shorts to ac line voltages up to 240 V rms. Combined with 1500 V input to output isolation, the 1B22 provides unbeatable protection against transients, wiring errors and current loop short circuits to power lines.

The dc-dc converter consists of a power driver, power transformer T2, a full wave rectifier and a filter. The dc-dc converter provides the power for the output circuitry as well as the isolated compliance voltage for the loop. This voltage is proportional to V_{LOOP} on the input side. The 1B22 requires ± 15 V supplies to power the input side circuitry and a +14 V to +30 V supply for the dc-dc converter.

USING THE 1B22

Basic Interconnections: The 1B22 may be applied to achieve rated performance as shown in Figure 2. For 0 V to 10 V signals either IN1 or IN2 can be used for input; for 0 V to +5 V signals jumper IN1 to IN2. Similarly, for 4 to 20 mA operation the 4 mA OFFSET node should be jumpered to the S. NODE, while for 0 to 20 mA it should be tied to COM. Figure 3 shows the functional diagram of the resistor network used in the 1B22.

For applications where a separate loop supply is not available, the ± 15 V supplies can be used by connecting +15 V to V_{LOOP} (Pin 24) and COM to P.COM (Pin 25). For additional compliance voltage, P.COM can be connected to -15 V to drive higher loads.

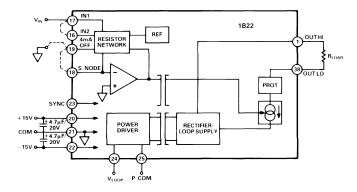


Figure 2. Basic Interconnections

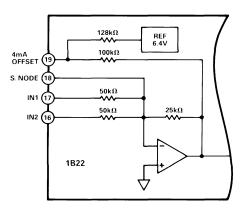


Figure 3. Internal Resistor Network

Optional Trim Adjustments: Figure 4 is an example of using potentiometers for trimming gain and offset for a 0 V to +10 V input and 4 to 20 mA output. The network for offset adjustment keeps the resistors relatively small to minimize noise effects while giving a sensitivity of $\pm 1\%$ of span. The value of R1 should be 5 M Ω for the 0 V to +10 V range (for 0 V to +5 V configurations, a 1.2 M Ω resistor should be used). For more adjustment range, resistors smaller than 274 k Ω can be used.

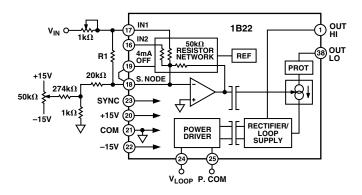


Figure 4. Optional Offset and Span Adjustment