Bus buffer/line driver; 3-state Rev. 10 — 11 January 2022

Product data sheet

1. General description

The 74AHC1G126; 74AHCT1G126 is a single buffer/line driver with 3-state output. Inputs are overvoltage tolerant. This feature allows the use of these devices as translators in mixed voltage environments.

2. Features and benefits

- Symmetrical output impedance
- Balanced propagation delays
- Wide supply voltage range from 2.0 to 5.5 V
- Overvoltage tolerant inputs to 5.5 V
- High noise immunity
- CMOS low power dissipation
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level A
- Input levels:
 - For 74AHC1G126: CMOS level
 - For 74AHCT1G126: TTL level
- ESD protection:
 - HBM JESD22-A114F: exceeds 2000 V
 - MM JESD22-A115-A: exceeds 200 V
 - CDM JESD22-C101E: exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

Type number	Package									
	Temperature range	Name	Description	Version						
74AHC1G126GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads;	SOT353-1						
74AHCT1G126GW			body width 1.25 mm							
74AHC1G126GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753						
74AHCT1G126GV										
74AHC1G126GM	-40 °C to +125 °C	XSON6	plastic extremely thin small outline package;	SOT886						
74AHCT1G126GM	1		no leads; 6 terminals; body 1 × 1.45 × 0.5 mm							

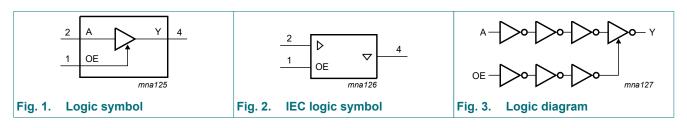
nexperia

4. Marking

Table 2. Marking codes	
Type number	Marking [1]
74AHC1G126GW	AN
74AHCT1G126GW	CN
74AHC1G126GV	A26
74AHCT1G126GV	C26
74AHC1G126GM	AN
74AHCT1G126GM	CN

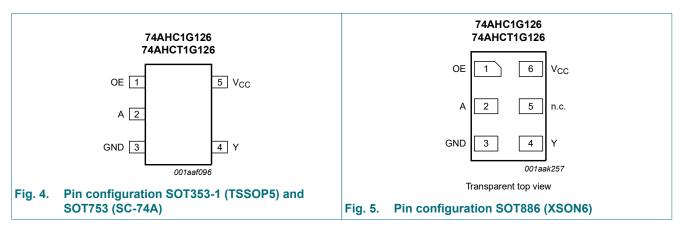
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1. Pinning



6.2. Pin description

Symbol	Pin	Pin			
	SOT353-1 and SOT753	SOT886			
OE	1	1	output enable input		
A	2	2	data input A		
GND	3	3	ground (0 V)		
Y	4	4	data output Y		
n.c.	-	5	not connected		
V _{CC}	5	6	supply voltage		

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state

Input	nput DE A					
OE	Α					
Н	L	L				
Н	Н	Н				
L	X	Z				

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+7.0	V
VI	input voltage		-0.5	+7.0	V
I _{IK}	input clamping current	V ₁ < -0.5 V [1]	-20	-	mA
I _{OK}	output clamping current	$V_{\rm O} < -0.5 \text{ V or } V_{\rm O} > V_{\rm CC} + 0.5 \text{ V}$ [1]	-	±20	mA
I _O	output current	-0.5 V < V _O < V _{CC} + 0.5 V	-	±25	mA
I _{CC}	supply current		-	75	mA
I _{GND}	ground current		-75	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 \text{ °C to } +125 \text{ °C}$ [2]	-	250	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT353-1 (TSSOP5) package: P_{tot} derates linearly with 3.3 mW/K above 74 °C.

For SOT753 (SC-74A) package: Ptot derates linearly with 3.8 mW/K above 85 °C.

For SOT886 (XSON6) package: Ptot derates linearly with 3.3 mW/K above 74 °C.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	74	AHC1G1	26	74	Unit		
			Min	Тур	Max	Min	Тур	Max	
V _{CC}	supply voltage		2.0	5.0	5.5	4.5	5.0	5.5	V
VI	input voltage		0	-	5.5	0	-	5.5	V
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C
Δt/ΔV	input transition rise and	V _{CC} = 3.3 V ± 0.3 V	-	-	100	-	-	-	ns/V
	fall rate	V _{CC} = 5.0 V ± 0.5 V	-	-	20	-	-	20	ns/V

10. Static characteristics

Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C t	o +125 °C	Unit
			Min	Тур	Max	Min	Мах	Min	Мах	
74AHC1	G126					I	I			
V _{IH}	HIGH-level	V _{CC} = 2.0 V	1.5	-	-	1.5	-	1.5	-	V
	input voltage	V _{CC} = 3.0 V	2.1	-	-	2.1	-	2.1	-	V
		V _{CC} = 5.5 V	3.85	-	-	3.85	-	3.85	-	V
V _{IL}	LOW-level	V _{CC} = 2.0 V	-	-	0.5	-	0.5	-	0.5	V
	input voltage	V _{CC} = 3.0 V	-	-	0.9	-	0.9	-	0.9	V
	V _{CC} = 5.5 V	-	-	1.65	-	1.65	-	1.65	V	
V _{OH}	HIGH-level	V _I = V _{IH} or V _{IL}								
	output voltage	I _O = -50 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I _O = -50 μA; V _{CC} = 3.0 V	2.9	3.0	-	2.9	-	2.9	-	V
		I _O = -50 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.58	-	-	2.48	-	2.40	-	V
		I _O = -8.0 mA; V _{CC} = 4.5 V	3.94	-	-	3.8	-	3.70	-	V
V _{OL}	LOW-level	V _I = V _{IH} or V _{IL}								
	output voltage	I _O = 50 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 3.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 50 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.36	-	0.44	-	0.55	V
		I _O = 8.0 mA; V _{CC} = 4.5 V	-	-	0.36	-	0.44	-	0.55	V
I _{OZ}	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL};$ $V_{O} = V_{CC} \text{ or GND};$ $V_{CC} = 5.5 \text{ V}$	-	-	±0.25	-	±2.5	-	±10	μA
lı	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA

Bus buffer/line driver; 3-state

Symbol	Parameter	Conditions		25 °C		-40 °C	to +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Мах	Min	Мах	Min	Мах	
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	2.0	-	20	-	40	μA
CI	input capacitance		-	3	10	-	10	-	10	pF
74AHCT	1G126									
V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	-	-	2.0	-	2.0	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	-	0.8	-	0.8	-	0.8	V
V _{OH} HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$									
	Ι _Ο = -50 μΑ	4.4	4.5	-	4.4	-	4.4	-	V	
		I _O = -8.0 mA	3.94	-	-	3.8	-	3.70	-	V
V _{OL}	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = 50 μA	-	0	0.1	-	0.1	-	0.1	V
		I _O = 8.0 mA	-	-	0.36	-	0.44	-	0.55	V
I _{OZ}	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL};$ $V_{O} = V_{CC} \text{ or GND};$ $V_{CC} = 5.5 \text{ V}$	-	-	±0.25	-	±2.5	-	±10	μA
l _l	input leakage current	V _I = 5.5 V or GND; V _{CC} = 0 V to 5.5 V	-	-	0.1	-	1.0	-	2.0	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	2.0	-	20	-	40	μA
ΔI _{CC}	additional supply current	per input pin; V _I = V _{CC} - 2.1 V; other inputs at V _{CC} or GND; I _O = 0 A; V _{CC} = 4.5 V to 5.5 V	-	-	1.35	-	1.5	-	1.5	mA
CI	input capacitance		-	3	10	-	10	-	10	pF

11. Dynamic characteristics

Table 8. Dynamic characteristics

GND = 0 V; for test circuit see Fig. 8.

Symbol	Parameter	Conditions			25 °C		-40 °C	to +85 °C	-40 °C to +125 °C		Unit
				Min	Тур	Max	Min	Max	Min	Max	
74AHC1	G126	J	I						1		
t _{pd}	propagation	A to Y; see Fig. 6	[1]								
	delay	V _{CC} = 3.0 V to 3.6 V	[2]								
		C _L = 15 pF		-	4.4	8.0	1.0	9.5	1.0	10.0	ns
		C _L = 50 pF		-	6.3	11.5	1.0	13.0	1.0	14.5	ns
		V _{CC} = 4.5 V to 5.5 V	[3]								
		C _L = 15 pF		-	3.4	5.5	1.0	6.5	1.0	7.0	ns
		C _L = 50 pF		-	4.7	7.5	1.0	8.5	1.0	9.5	ns
t _{en} en	enable time	OE to Y; see Fig. 7	[1]								
		V _{CC} = 3.0 V to 3.6 V	[2]								
		C _L = 15 pF		-	4.9	8.0	1.0	9.5	1.0	10.0	ns
		C _L = 50 pF		-	7.0	11.5	1.0	13.0	1.0	14.5	ns
		V _{CC} = 4.5 V to 5.5 V	[3]								
		C _L = 15 pF		-	3.6	5.6	1.0	6.3	1.0	7.0	ns
		C _L = 50 pF		-	5.4	8.0	1.0	9.0	1.0	9.5	ns
t _{dis}	disable time	OE to Y; see Fig. 7	[1]								
		V _{CC} = 3.0 V to 3.6 V	[2]								
		C _L = 15 pF		-	6.3	9.7	1.0	11.5	1.0	12.5	ns
		C _L = 50 pF		-	9.0	13.2	1.0	15.0	1.0	16.5	ns
		V _{CC} = 4.5 V to 5.5 V	[3]								
		C _L = 15 pF		-	4.3	6.8	1.0	8.0	1.0	8.5	ns
		C _L = 50 pF		-	6.1	8.8	1.0	10.0	1.0	11.0	ns
C _{PD}	power dissipation capacitance	per buffer; C_L = 50 pF; f = 1 MHz; V _I = GND to V _{CC}	[4]	-	9	-	-	-	-	-	pF

Bus buffer/line driver; 3-state

Symbol	Parameter	Conditions			25 °C		-40 °C	to +85 °C	-40 °C to +125 °C		Unit
				Min	Тур	Max	Min	Max	Min	Мах	
74AHCT	1G126	J									
t _{pd}	propagation	A to Y; see Fig. 6	[1]								
	delay	V _{CC} = 4.5 V to 5.5 V	[3]								
		C _L = 15 pF		-	3.4	5.5	1.0	6.5	1.0	7.0	ns
		C _L = 50 pF		-	4.7	7.5	1.0	8.5	1.0	9.5	ns
t _{en}	enable time	OE to Y; see Fig. 7	[1]								
		V _{CC} = 4.5 V to 5.5 V	[3]								
		C _L = 15 pF		-	3.4	5.6	1.0	6.3	1.0	6.5	ns
		C _L = 50 pF		-	4.8	8.0	1.0	9.0	1.0	9.0	ns
t _{dis}	disable time	OE to Y; see Fig. 7	[1]								
		V _{CC} = 4.5 V to 5.5 V	[3]								
		C _L = 15 pF		-	4.0	6.8	1.0	8.0	1.0	8.5	ns
		C _L = 50 pF		-	5.7	8.8	1.0	10.0	1.0	11.5	ns
C _{PD}	power dissipation capacitance	per buffer; C _L = 50 pF; f = 1 MHz; V _I = GND to V _{CC}	[4]	-	11	-	-	-	-	-	pF

[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

 \dot{t}_{en} is the same as t_{PZL} and $t_{PZH}.$

 t_{dis} is the same as t_{PLZ} and t_{PHZ} . Typical values are measured at V_{CC} = 3.3 V.

[2]

[3] Typical values are measured at $V_{CC} = 5.0 \text{ V}$. [4] C_{PD} is used to determine the dynamic power dissipation P_D (µW).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i + \sum (C_L \times V_{CC}^2 \times f_o)$ where:

 f_i = input frequency in MHz;

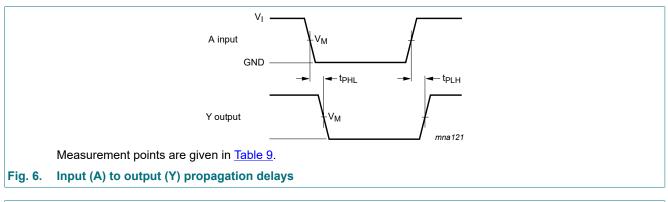
 f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in Volts.

Bus buffer/line driver; 3-state





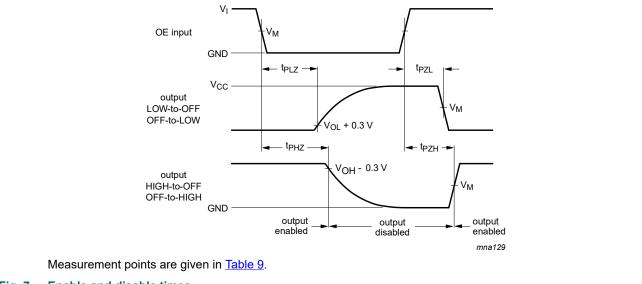


Fig. 7. Enable and disable times

Table 9. Measurement points

Туре	Input	Output	
	V _M	VI	V _M
74AHC1G126	0.5 × V _{CC}	GND to V _{CC}	$0.5 \times V_{CC}$
74AHCT1G126	1.5 V	GND to 3.0 V	$0.5 \times V_{CC}$

Bus buffer/line driver; 3-state

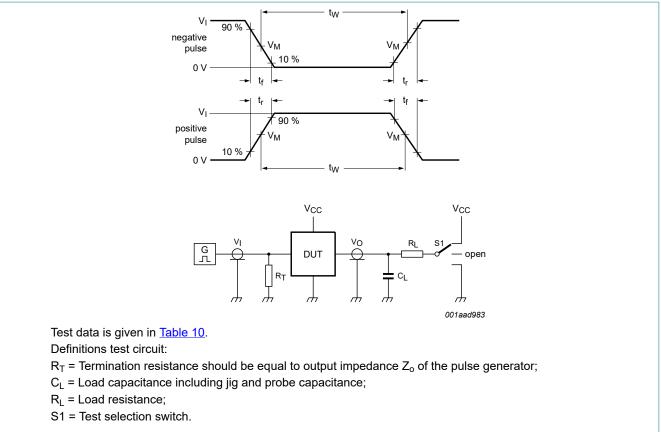


Fig. 8. Test circuit for measuring switching times

Table 10. Test data

Туре	Input		Load		S1 position		
	VI	t _r , t _f	CL	RL	t _{PHL} , t _{PLH}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
74AHC1G126	V _{CC}	≤ 3 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}
74AHCT1G126	3 V	≤ 3 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}

Bus buffer/line driver; 3-state

12. Package outline

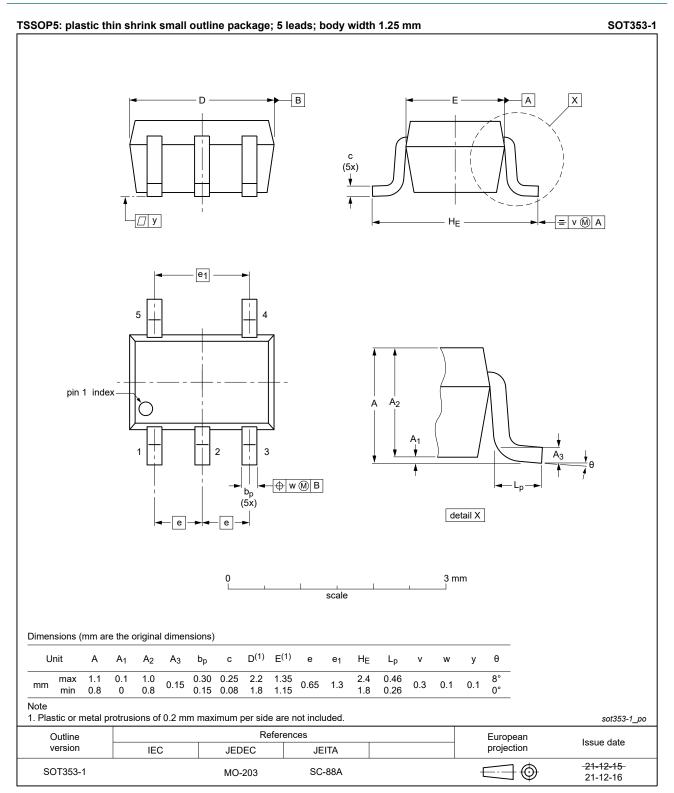


Fig. 9. Package outline SOT353-1 (TSSOP5)

Bus buffer/line driver; 3-state

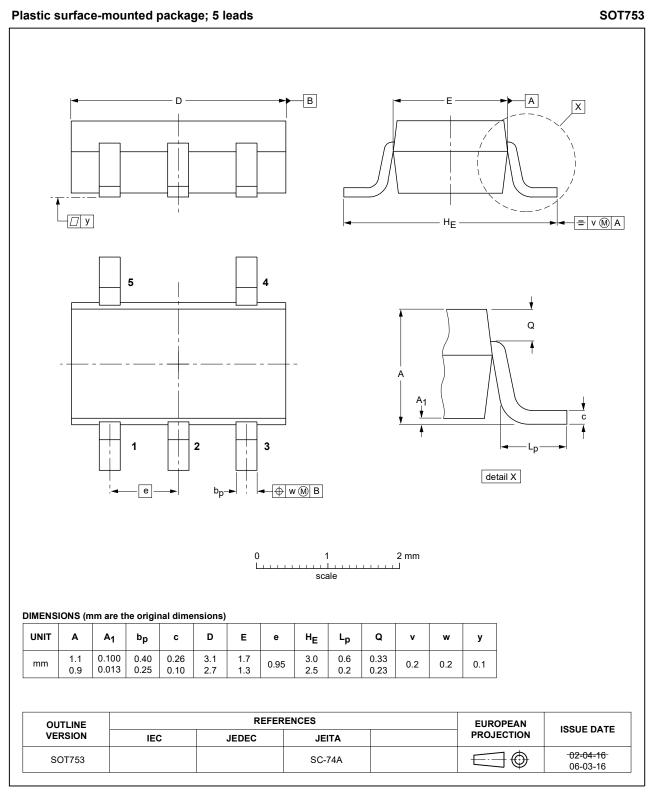


Fig. 10. Package outline SOT753 (SC-74A)

Bus buffer/line driver; 3-state

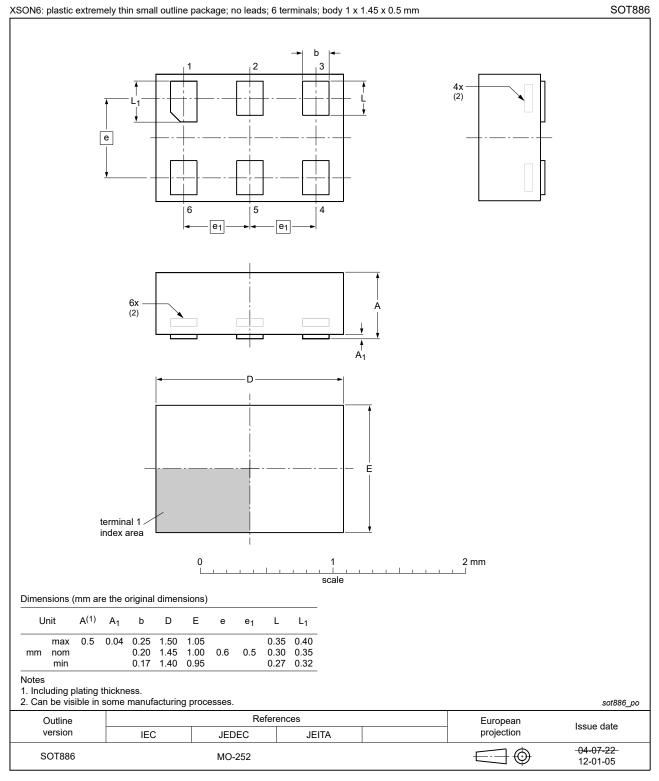


Fig. 11. Package outline SOT886 (XSON6)

13. Abbreviations

Acronym	Description		
CMOS	Complementary Metal Oxide Semiconductor		
CDM	Charged Device Model		
DUT	Device Under Test		
ESD	ElectroStatic Discharge		
НВМ	Human Body Model		
MM	Machine Model		
TTL	Transistor-Transistor Logic		

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AHC_AHCT1G126 v.10	20220111	Product data sheet	-	74AHC_AHCT1G126 v.9
Modifications:	• Fig. 9: Package outline drawing for SOT353-1 (TSSOP5) has changed.			
74AHC_AHCT1G126 v.9	20210518	Product data sheet	-	74AHC_AHCT1G126 v.8
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Type numbers 74AHC1G126GF and 74AHCT1G126GF (SOT891 / XSON6) removed. <u>Section 1</u> and <u>Section 2</u> updated. <u>Section 8</u>: Derating values for P_{tot} total power dissipation updated. 			
74AHC_AHCT1G126 v.8	20120823	Product data sheet	-	74AHC_AHCT1G126 v.7
Modifications:	Package ou	tline drawing of SOT886 (-ig. 11) modified.	,
74AHC_AHCT1G126 v.7	20090617	Product data sheet	-	74AHC_AHCT1G126 v.6
74AHC_AHCT1G126 v.6	20070525	Product data sheet	-	74AHC_AHCT1G126 v.5
74AHC_AHCT1G126 v.5	20070514	Product data sheet	-	74AHC_AHCT1G126 v.4
74AHC_AHCT1G126 v.4	20020606	Product specification	-	74AHC_AHCT1G126 v.3
74AHC_AHCT1G126 v.3	20020215	Product specification	-	74AHC_AHCT1G126 v.2
74AHC_AHCT1G126 v.2	20010406	Product specification	-	74AHC1G_AHCT1G126 v.1
74AHC1G_AHCT1G126 v.1	19990920	Product specification	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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