Product data sheet

1. General description

The 74AHCT14A is a hexadecimal inverter with Schmitt-trigger inputs, capable of transforming slowly changing input signals into sharply defined, jitter-free output signals.

Designed to operate over a V_{CC} range from 4.5 V to 5.5 V, the inputs are TTL compatible. This range allows the device to be used to translate from 3.3 V to 5 V.

This device is fully specified for partial Power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2. Features and benefits

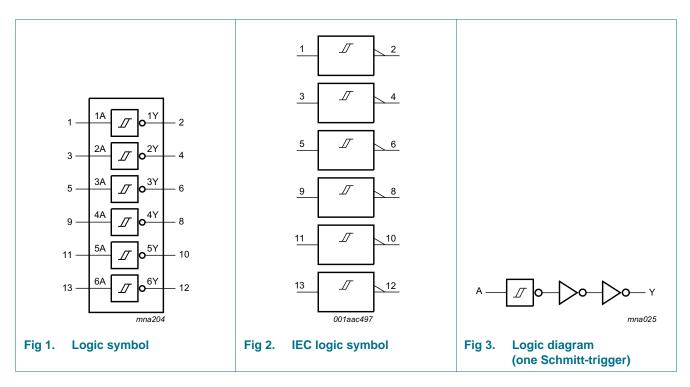
- Direct interface with TTL levels
- Supply voltage range from 4.5 V to 5.5 V
- Typical t_{pd} of 3.7 ns at 5 V
- Typical V_{OL(p)} < 0.8 V at V_{CC} = 5 V, T_{amb} = 25 °C
- Typical $V_{OH(v)} > 2.3$ V at $V_{CC} = 5$ V, $T_{amb} = 25$ °C
- Supports mixed-mode voltage operation on all ports
- I_{OFF} circuitry provides partial Power-down mode operation
- Latch-up performance exceeds 250 mA per JESD 78 Class II
- ESD protection:
 - HBM ANSI/ESDA/JEDEC JS-001 Class 2 exceeds 3 kV
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 2 kV
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C



3. Ordering information

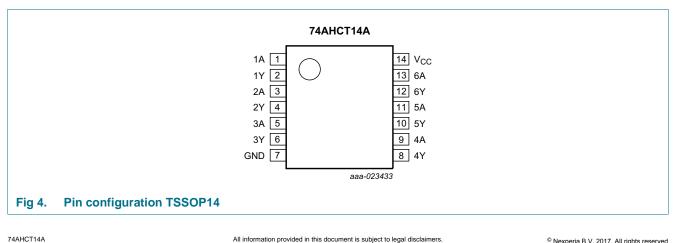
Table 1. Ordering information							
Type number Package							
	Temperature range	Name	Description	Version			
74AHCT14APW	–40 °C to +125 °C	TSSOP14	plastic thin shrink small outline package; 14 leads; body width 4.4 mm	SOT402-1			

4. Functional diagram



5. Pinning information

5.1 Pinning



5.2 Pin description

Table 2.Pin description		
Symbol	Pin	Description
1A, 2A, 3A, 4A, 5A, 6A	1, 3, 5, 9, 11, 13	data input
1Y, 2Y, 3Y, 4Y, 5Y, 6Y	2, 4, 6, 8, 10, 12	data output
GND	7	ground (0 V)
V _{CC}	14	supply voltage

6. Functional description

Table 3. Function	table ^[1]
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Input	Output
nA	nY
L	Н
Н	L

[1] H = HIGH voltage level;

L = LOW voltage level.

7. Limiting values

Table 4.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+7.0	V
VI	input voltage		<u>[1]</u>	-0.5	+7.0	V
Vo	output voltage	output HIGH or LOW state	[2][3]	-0.5	V _{CC} + 0.5	V
		output power-down	[2]	-0.5	+7.0	V
I _{IK}	input clamping current	V _I < 0 V		-20	-	mA
I _{OK}	output clamping current	V _O < 0 V		-20	-	mA
lo	output current	$V_{O} = 0 V$ to V_{CC}		-	±25	mA
I _{CC}	supply current			-	75	mA
I _{GND}	ground current			-75	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	$T_{amb} = -40 \text{ °C to } +125 \text{ °C}$	<u>[4]</u>	-	500	mW

[1] If the input current ratings are observed, the minimum input voltage ratings may be exceeded.

[2] If the output current ratings are observed, the output voltage ratings may be exceeded.

[3] This value is limited to 7 V maximum.

[4] For TSSOP14 packages: above 75 °C, the value of P_{tot} derates linearly at 7 mW/K.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		4.5	5.0	5.5	V
VI	input voltage		0	-	5.5	V
Vo	output voltage	output HIGH or LOW state	0	-	V _{CC}	V
		output power-down	0	-	5.5	V
T _{amb}	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	$V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	-	-	1	ms/V

9. Static characteristics

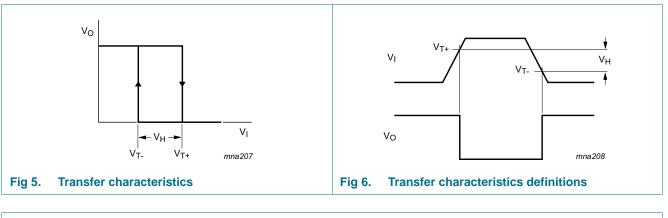
Table 6. Static characteristics

Voltages are referenced to GND (ground = 0 V).

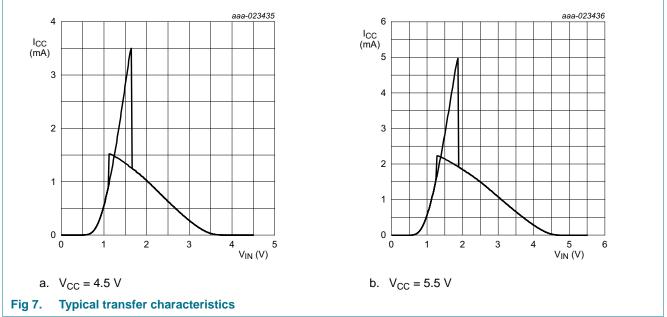
Symbol	Parameter	Conditions	25 °C			–40 °C to +85 °C		–40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Max	Min	Max	
V _{T+}	positive-going	V _{CC} = 4.5 V	-	-	1.9	-	1.9	-	1.9	V
	threshold voltage	V _{CC} = 5.5 V	-	-	2.1	-	2.1	-	2.1	V
V _{T-}	negative-going	V _{CC} = 4.5 V	0.5	-	-	0.5	-	0.5	-	V
	threshold voltage	V _{CC} = 5.5 V	0.6	-	-	0.6	-	0.6	-	V
V _H	hysteresis	V _{CC} = 4.5 V	0.4	-	1.4	0.4	1.4	0.4	1.4	V
	voltage	V _{CC} = 5.5 V	0.4	-	1.5	0.4	1.5	0.4	1.5	V
V _{OH} HIGH-level output voltage		$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = -50 μA	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -8 mA	3.94	-	-	3.8	-	3.7	-	V
V _{OL} LO	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = 50 μA	-	0	0.1	-	0.1	-	0.1	V
		I _O = 8 mA	-	-	0.36	-	0.44	-	0.55	V
I _{OFF}	power-off leakage current	$V_1 \text{ or } V_0 = \text{GND to 5.5 V};$ $V_{CC} = 0 \text{ V}$	-	-	0.5	-	5	-	5	μA
I _I	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 0 V$ to 5.5 V	-	-	±0.1	-	±1	-	±1	μA
I _{CC}	supply current		-	-	2	-	20	-	20	μA
Δl _{CC}	additional supply current	per input pin; V _I = 3.4 V; I _O = 0 A; other pins at V _{CC} or GND; V _{CC} = 5.5 V	-	-	1.35	-	1.5	-	1.5	mA

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Hex inverting Schmitt trigger



9.1 Transfer characteristics waveforms



10. Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V. For test circuit, see <u>Figure 9</u>.

Symbol	Parameter	Conditions		25 °C		_40 °C	to +85 °C	–40 °C t	o +125 °C	Unit
			Min	Typ <mark>[1]</mark>	Max	Min	Max	Min	Max	
t _{pd}	propagation	nA to nY; see Figure 8 [2]								
	delay	V_{CC} = 4.5 V to 5.5 V								
	C _L = 15 pF	-	3.7	7.6	1	9	1	9.7	ns	
		C _L = 50 pF	-	5	9.6	1	11	1	11.9	ns
CI	input capacitance	$V_I = V_{CC} \text{ or GND};$ $V_{CC} = 5 \text{ V}$	-	2	6	-	6	-	6	pF
Co	output capacitance	$V_{O} = V_{CC} \text{ or GND};$ $V_{CC} = 5 \text{ V}$	-	5	-	-	-	-	-	pF
C _{PD}	power dissipation capacitance	per buffer; [3] $C_L = 0 \text{ pF}; f = 10 \text{ MHz};$ $V_I = \text{GND to } V_{CC}$	-	10	-	-	-	-	-	pF

[1] Typical values are measured at T_{amb} = 25 $^\circ C$ and V_{CC} = 5 V.

- [2] t_{pd} is the same as t_{PLH} and t_{PHL} .

 V_{CC} = supply voltage in Volts.

Table 8.Noise characteristics

GND = 0 V. For test circuit, see Figure 9.

Symbol	Parameter	Conditions	Т	T _{amb} = 25 °C			
			Min	Тур	Max		
$V_{\rm CC} = 5$ V	/; C _L = 50 pF						
V _{OL(p)}	LOW-level output voltage (peak)		-	0.4	0.8	V	
V _{OL(v)}	LOW-level output voltage (valley)		-0.8	-0.2	-	V	
V _{OH(v)}	HIGH-level output voltage (valley)		-	4.5	-	V	
V _{IH(AC)}	AC HIGH-level input voltage		2	-	-	V	
V _{IL(AC)}	AC LOW-level input voltage		-	-	0.8	V	

11. Waveforms

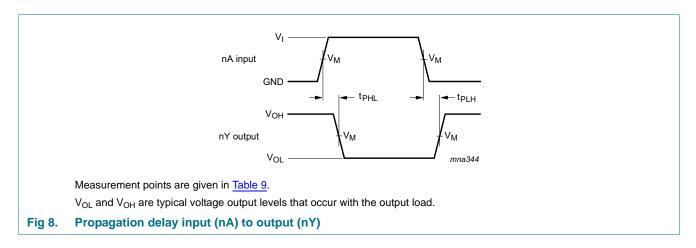


Table 9.Measurement points

Input	Output
V _M	V _M
1.5 V	0.5V _{CC}

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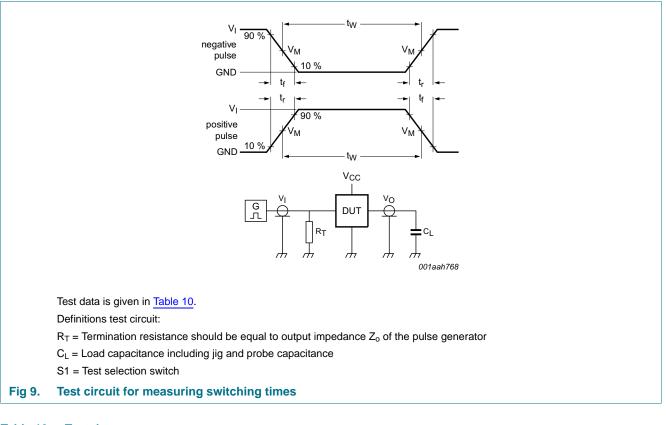


Table 10. Test data

Input		Load	Test	
VI	t _r , t _f	CL		
GND to 3 V	3.0 ns	15 pF, 50 pF	t _{PLH} , t _{PHL}	

12. Package outline

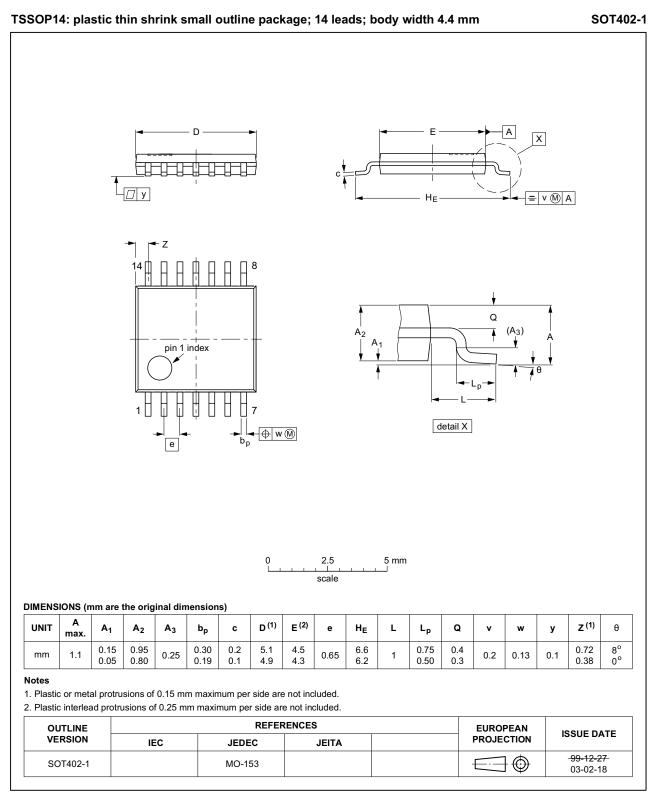


Fig 10. Package outline SOT402-1 (TSSOP14)

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13. Abbreviations

Table 11. Abbreviations					
Acronym	Description				
CDM	Charge Device Model				
DUT	Device Under Test				
ESD	ElectroStatic Discharge				
HBM	Human Body Model				
MM	Machine Model				
TTL	Transistor-Transistor Logic				

14. Revision history

Table 12. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AHCT14A v.2	20161026	Product data sheet	-	74AHCT14A v.1
Modifications:	 Type numbers 74AHCT14AD and 74AHCT14ABQ removed. 			
74AHCT14A v.1	20160614	Product data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition	
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.	
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.	
Product [short] data sheet	Production	This document contains the product specification.	

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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