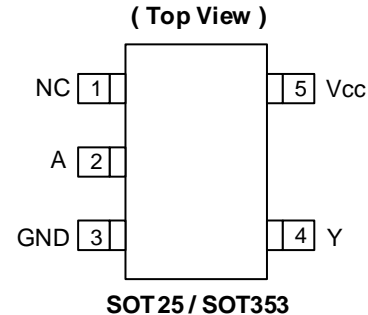


Description

The 74AHCT1G14Q is an automotive compliant Schmitt-trigger inverter gate with a standard push-pull output. The device is designed for operation with a power supply range of 4.5V to 5.5V. The gate performs the positive Boolean function:

$$Y = \overline{A}$$

Pin Assignments



Features

- Grade 1 Ambient Temperature Operation: -40°C to +125°C
- Supply Voltage Range from 4.5V to 5.5V
- ±8mA Output Drive at 5.0V
- CMOS Low-Power Consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time.
- Inputs not Limited by V_{cc}
- Balanced Propagation Delays
- Balanced Drive Capability
- ESD Protection Tested per AEC-Q100
- Exceeds 2000-V Human Body Model (AEC-Q100-002)
- Exceeds 1000-V Charged Device Model (AEC-Q100-011)
- Latch-Up Exceeds 100mA (AEC-Q100-004)
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **The 74AHCT1G14Q is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

Applications

- General Purpose Logic
- Wide Array of Products, such as:
 - Automotive Applications within Grade 1 Temperature Range
 - Industrial Computing/Controls/Automation
 - High Reliability Networking/Communications
 - Industrial/Agricultural Equipment

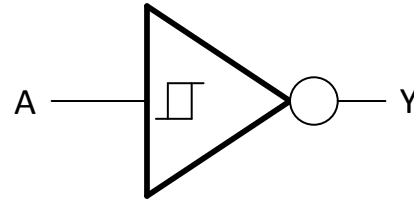
Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Pin Descriptions

| Pin Name | Description |
|----------|----------------|
| NC | No Connection |
| A | Data Input |
| GND | Ground |
| Y | Data Output |
| Vcc | Supply Voltage |

Logic Diagram



Function Table

| Inputs | Output |
|----------|----------|
| A | Y |
| H | L |
| L | H |

Absolute Maximum Ratings (Notes 4 & 5)

| Symbol | Description | Rating | Unit |
|---------------------|--------------------------------------------------------------------------------|------------------------------|------|
| ESD HBM | Human Body Model ESD Protection | 2 | kV |
| ESD CDM | Charged Device Model ESD Protection | 1 | kV |
| V _{CC} | Supply Voltage Range | -0.5 to 6.5 | V |
| V _I | Input Voltage Range | -0.5 to 6.5 | V |
| V _O | Voltage Applied to Output in High or Low State | -0.5 to V _{CC} +0.5 | V |
| I _{IK} | Input Clamp Current V _I < 0 | -20 | mA |
| I _{OK} | Output Clamp Current (V _O < 0 or V _O > V _{CC}) | ±20 | mA |
| I _O | Continuous Output Current (V _O = 0 to V _{CC}) | ±25 | mA |
| I _{CC} | Continuous Current Through V _{CC} | 75 | mA |
| I _{GND} | Continuous Current Through GND | -75 | mA |
| T _J | Operating Junction Temperature | -40 to +150 | °C |
| T _{STG} | Storage Temperature | -65 to +150 | °C |
| P _{totTOT} | Total Power Dissipation (Note 6) | 250 | mW |

- Notes:
- Stresses beyond the absolute maximum can result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.
 - Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.
 - This will need to be derated at higher operating temperatures to prevent exceeding maximum T_J, refer to package thermal characteristics section.

Recommended Operating Conditions (Note 7)

| Symbol | Parameter | | Min | Max | Unit |
|-----------------|---------------------------|-----------------------------|-----|-----------------|------|
| V _{CC} | Operating Voltage | — | 4.5 | 5.5 | V |
| V _{IH} | High-Level Input Voltage | V _{CC} = 5V ± 0.5V | 2.0 | — | V |
| V _{IL} | Low-Level Input Voltage | V _{CC} = 5V ± 0.5V | — | 0.8 | V |
| V _I | Input Voltage | | 0 | 5.5 | V |
| V _O | Output Voltage | | 0 | V _{CC} | V |
| I _{OH} | High-Level Output Current | V _{CC} = 5V ± 0.5V | — | -8 | mA |
| I _{OL} | Low-Level Output Current | V _{CC} = 5V ± 0.5V | — | 8 | mA |
| T _A | Ambient Temperature | — | -40 | +125 | °C |

Note: 7. Unused inputs should be held at V_{CC} or Ground.

Electrical Characteristics (All typical values are at V_{CC} = 5V, T_A = +25°C)

| Symbol | Parameter | Test Conditions | V _{CC} | +25°C | | | -40°C to +85°C | | -40°C to +125°C | | Unit |
|------------------|-------------------------------------------------|-------------------------------------------------------------|-----------------|-------|-----|-------|----------------|------|-----------------|------|------|
| | | | | Min | Typ | Max | Min | Max | Min | Max | |
| V _{T+} | Positive-Going Input Threshold Voltage | — | 4.5V | — | — | 2.0 | — | 2.0 | — | 2.0 | V |
| | | | 5.5V | — | — | 2.0 | — | 2.0 | — | 2.0 | |
| V _{T-} | Negative-Going Input Threshold Voltage | — | 4.5V | 0.5 | — | — | 0.5 | — | 0.5 | — | V |
| | | | 5.5V | 0.6 | — | — | 0.6 | — | 0.6 | — | |
| ΔV _T | Hysteresis (V _{T+} - V _{T-}) | — | 4.5V | 0.4 | — | 1.4 | 0.4 | 1.4 | 0.35 | 1.4 | V |
| | | | 5.5V | 0.4 | — | 1.6 | 0.6 | 1.6 | 0.35 | 1.6 | |
| V _{OL} | Low Level Output Voltage | V _I = V _{T+} I _{OL} = 50μA | 4.5V | — | — | 0.1 | — | 0.1 | — | 0.1 | V |
| | | V _I = V _{T+} I _{OL} = 8mA | 4.5V | — | — | 0.36 | — | 0.44 | — | 0.55 | |
| V _{OH} | High Level Output Voltage | V _I = V _{T-} I _{OH} = -50μA | 4.5V | 4.4 | 4.5 | — | 4.4 | — | 4.4 | — | V |
| | | V _I = V _{T-} I _{OH} = -8mA | 4.5V | 3.94 | — | — | 3.8 | — | 3.70 | — | |
| I _I | Input Current | V _I = 5.5V or GND | 0V to 5.5V | — | — | ± 0.1 | — | ± 1 | — | ± 2 | μA |
| ΔI _{CC} | Additional Supply Current | V _I = 5.5V or GND I _O = 0 | 5.5V | — | — | 2 | — | 20 | — | 40 | μA |
| I _{CC} | Supply Current | V _I = 3.4V, I _O = 0 | 5.5V | — | — | 1.35 | — | 1.5 | — | 1.5 | mA |
| C _I | Input Capacitance | V _I = V _{CC} - or GND | 5.5V | — | 1.5 | 10 | — | 10 | — | 10 | pF |

Package Characteristics

| Symbol | Parameter | Package | Test Conditions | Min | Typ | Max | Unit |
|---------------|-------------------------------------------|---------|-----------------|-----|-----|-----|------|
| θ_{JA} | Thermal Resistance Junction-to-Ambient | SOT25 | Note 8 | — | 184 | — | °C/W |
| | | SOT353 | | — | 385 | — | |
| θ_{JC} | Thermal Resistance Junction-to-Case | SOT25 | Note 8 | — | 62 | — | °C/W |
| | | SOT353 | | — | 164 | — | |

Note: 8. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

$V_{CC} = 5V \pm 0.5V$ (See Figure 1, typical values at $V_{CC} = 5V$)

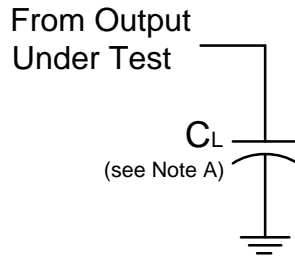
| Parameter | From (Input) | To (Output) | Test Conditions | +25°C | | | -40°C to +85°C | | -40°C to +125°C | | Unit |
|-----------|-----------------|----------------|--------------------|-------|-----|-----|----------------|------|-----------------|------|------|
| | | | | Min | Typ | Max | Min | Max | Min | Max | |
| t_{PD} | A | Y | $C_L = 15pF$ | 1.0 | 4.1 | 7.0 | 1.0 | 8.0 | 1.0 | 9.0 | ns |
| | | | $C_L = 50pF$ | 1.0 | 5.9 | 8.5 | 1.0 | 10.0 | 1.0 | 11.0 | ns |

Operating Characteristics

$T_A = +25^\circ C$

| Parameter | Test Conditions | Typ | Unit |
|-----------|-----------------------------------------------------------------------------|-----|------|
| C_{PD} | $V_{CC} = 5.0V, f = 1MHz$ $C_L = 50pF$ $V_I = GND \text{ to } V_{CC}$ | 12 | pF |

Measurement Information



| V _{CC} | Inputs | | | Output | C _L |
|-----------------|----------------|--------------------------------|----------------|--------------------|----------------|
| | V _I | t _r /t _f | V _M | V _M | |
| 5V±0.5V | GND to 3.0V | ≤3ns | 1.5V | V _{CC} /2 | 15pF |
| 5V±0.5V | GND to 3.0V | ≤3ns | 1.5V | V _{CC} /2 | 50pF |

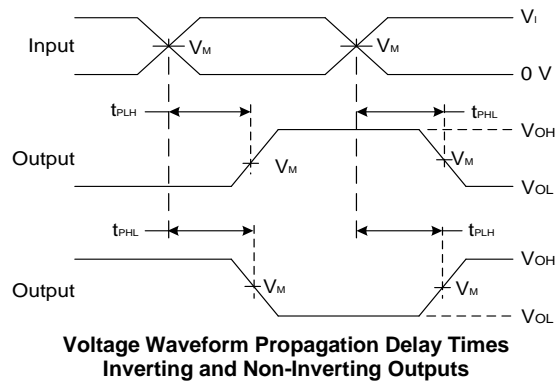
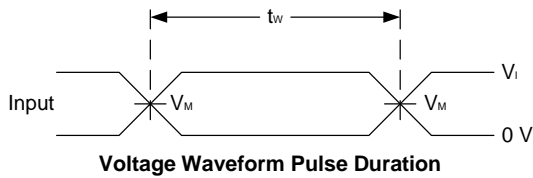
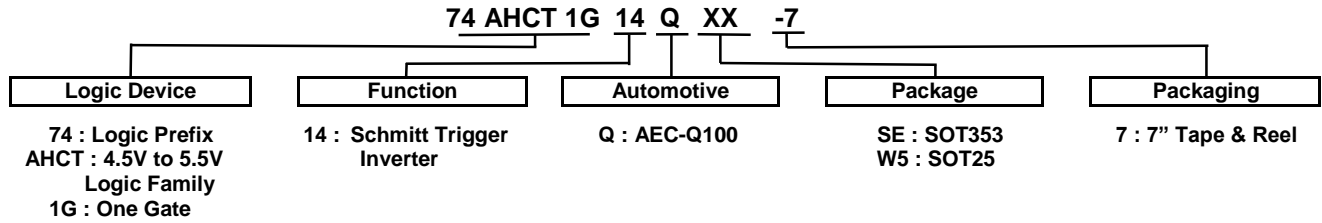


Figure 1. Load Circuit and Voltage Waveforms

- Notes:
- A. Includes test lead and test apparatus capacitance.
 - B. All pulses are supplied at pulse repetition rate ≤ 1MHz.
 - C. Inputs are measured separately one transition per measurement.

Ordering Information (Notes 9 to 11)



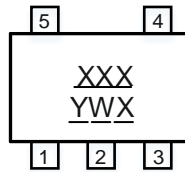
| Part Number | Package Code | Package (Notes 9 & 10) | Package Size | Packaging |
|-----------------|--------------|------------------------|---------------------------------------------|----------------------|
| 74AHCT1G14QSE-7 | SE | SOT353 | 2.15mm x 2.1mm x 1.1mm 0.65mm lead pitch | 3,000/7" Tape & Reel |
| 74AHCT1G14QW5-7 | W5 | SOT25 | 3.0mm x 2.8mm x 1.2mm 0.95mm lead pitch | 3,000/7" Tape & Reel |

Notes: 9. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.
 10. Pad layout as shown in Diodes Incorporated suggested pad layouts, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
 11. The taping orientation is located on our website at <https://www.diodes.com/assets/Packaging-Support-Docs/ap02007.pdf>.

Marking Information

SOT25, SOT353

(Top View)



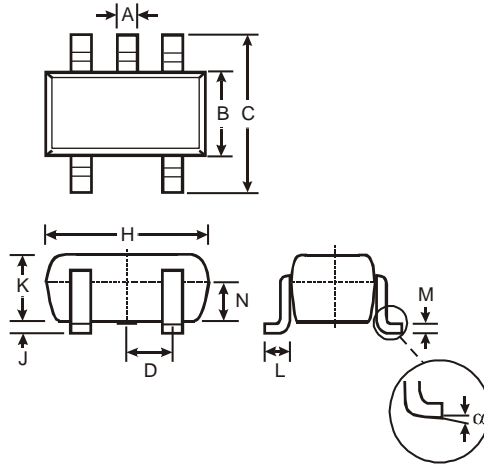
XXX: Identification Code
Y : Year 0 to 9
W : Week: A-Z: 1 to 26 Week;
 a-z: 27 to 52 Week;
 z Represents 52 to 53 Week
X : A-Z: Internal Code

| Part Number | Package | Identification Code |
|-----------------|---------|---------------------|
| 74AHCT1G14QW5-7 | SOT25 | ZVQ |
| 74AHCT1G14QSE-7 | SOT353 | ZVQ |

Package Outline Dimensions

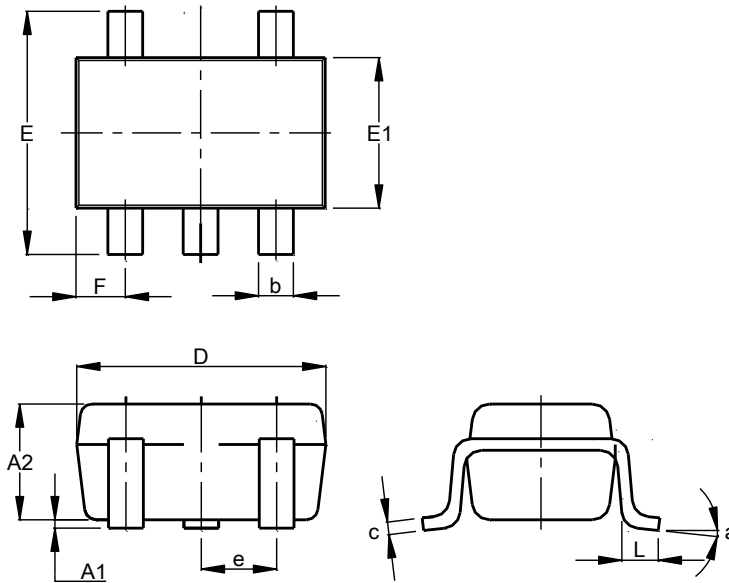
Please see <https://www.diodes.com/package-outlines.html> for the latest version.

(1) Package Type: SOT25



| SOT25 | | | |
|----------------------|-------|------|------|
| Dim | Min | Max | Typ |
| A | 0.35 | 0.50 | 0.38 |
| B | 1.50 | 1.70 | 1.60 |
| C | 2.70 | 3.00 | 2.80 |
| D | - | - | 0.95 |
| H | 2.90 | 3.10 | 3.00 |
| J | 0.013 | 0.10 | 0.05 |
| K | 1.00 | 1.30 | 1.10 |
| L | 0.35 | 0.55 | 0.40 |
| M | 0.10 | 0.20 | 0.15 |
| N | 0.70 | 0.80 | 0.75 |
| α | 0° | 8° | - |
| All Dimensions in mm | | | |

(2) Package Type: SOT353

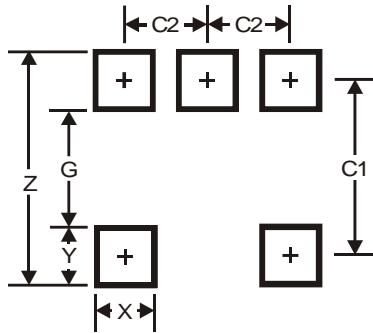


| SOT353 | | | |
|----------------------|-----------|------|-------|
| Dim | Min | Max | Typ |
| A1 | 0.00 | 0.10 | 0.05 |
| A2 | 0.90 | 1.00 | 0.95 |
| b | 0.10 | 0.30 | 0.25 |
| c | 0.10 | 0.22 | 0.11 |
| D | 1.80 | 2.20 | 2.15 |
| E | 2.00 | 2.20 | 2.10 |
| E1 | 1.15 | 1.35 | 1.30 |
| e | 0.650 BSC | | |
| F | 0.40 | 0.45 | 0.425 |
| L | 0.25 | 0.40 | 0.30 |
| a | 0° | 8° | -- |
| All Dimensions in mm | | | |

Suggested Pad Layout

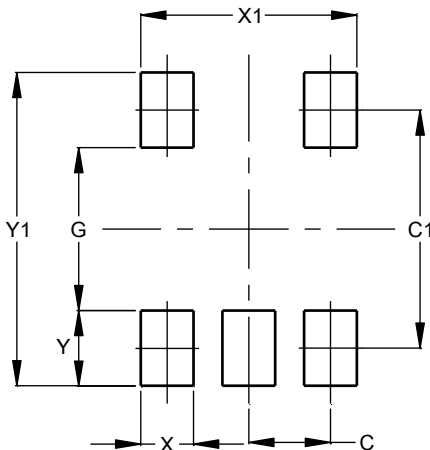
Please see <https://www.diodes.com/package-outlines.html> for the latest version.

(1) Package Type: SOT25



| Dimensions | Value |
|------------|-------|
| Z | 3.20 |
| G | 1.60 |
| X | 0.55 |
| Y | 0.80 |
| C1 | 2.40 |
| C2 | 0.95 |

(2) Package Type: SOT353



| Dimensions | Value (in mm) |
|------------|---------------|
| C | 0.650 |
| C1 | 1.900 |
| G | 1.300 |
| X | 0.420 |
| X1 | 1.720 |
| Y | 0.600 |
| Y1 | 2.500 |

Mechanical Data

SOT25

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 15.8mg (Approximate)

SOT353

- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 6.4mg (Approximate)