ne<mark>x</mark>peria



1 General description

The 74AUP1G04 provides the single inverting buffer.

Schmitt trigger action at all inputs makes the circuit tolerant to slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2 Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Complies with JEDEC standards:
 - JESD8-12 (0.8 V to 1.3 V)
 - JESD8-11 (0.9 V to 1.65 V)
 - JESD8-7 (1.2 V to 1.95 V)
 - JESD8-5 (1.8 V to 2.7 V)
 - JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 Class 3A exceeds 5000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 Class C3 exceeds 1000 V
 - MM: JESD22-A115-A exceeds 200 V
- Low static power consumption; $I_{CC} = 0.9 \ \mu A$ (maximum)
- Latch-up performance exceeds 100 mA per JESD 78B Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial Power-down mode operation
- Multiple package options
- Specified from -40 ° C to +85 ° C and -40 ° C to +125 ° C

3 Ordering information

Type number	Package			
	Temperature range	Name	Description	Version
74AUP1G04GV	-40 ° C to +125 ° C	SC-74A	plastic surface-mounted package; 5 leads	SOT753
74AUP1G04GW	-40 ° C to +125 ° C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1
74AUP1G04GM	-40 ° C to +125 ° C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm	SOT886
74AUP1G04GF	-40 ° C to +125 ° C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1 x 0.5 mm	SOT891
74AUP1G04GN	-40 ° C to +125 ° C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 0.9 x 1.0 x 0.35 mm	SOT1115
74AUP1G04GS	-40 ° C to +125 ° C	XSON6	extremely thin small outline package; no leads; 6 terminals; body 1.0 x 1.0 x 0.35 mm	SOT1202
74AUP1G04GX	-40 ° C to +125 ° C	25 ° C X2SON5 plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.35 mm		SOT1226
74AUP1G04GX4	-40 °C to +125 °C	X2SON4	plastic thermal enhanced extremely thin small outline package; no leads; 4 terminals; body 0.6 x 0.6 x 0.32 mm	SOT1269-2

4 Marking

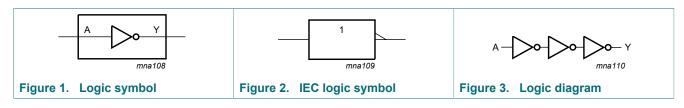
Table 2. Marking

Type number	Marking code ^[1]
74AUP1G04GV	p04
74AUP1G04GW	pC
74AUP1G04GM	pC
74AUP1G04GF	pC
74AUP1G04GN	pC
74AUP1G04GS	pC
74AUP1G04GX	pC
74AUP1G04GX4	pC

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

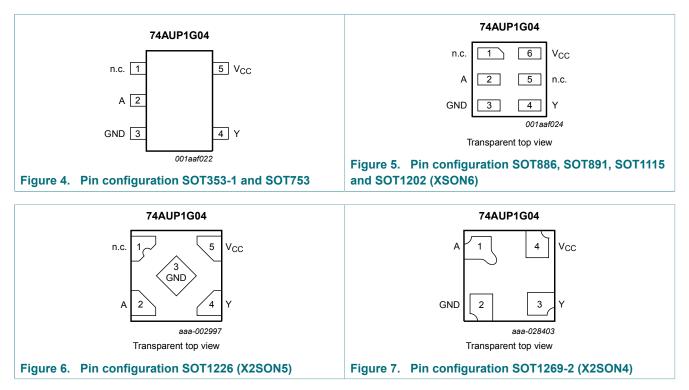
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5 Functional diagram



6 Pinning information

6.1 Pinning



6.2 Pin description

Symbol	Pin	Description		
	SC-74A, TSSOP5 and X2SON5	XSON6	X2SON4	
n.c.	1	1, 5	-	not connected
A	2	2	1	data input
GND	3	3	2	ground (0 V)
Y	4	4	3	data output
V _{CC}	5	6	4	supply voltage

Functional description 7

Table 4. Function table ^[1]

Input	Output
A	Y
L	Н
Н	L

[1] H = HIGH voltage level; L = LOW voltage level.

Limiting values 8

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+4.6	V
I _{IK}	input clamping current	V _I < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+4.6	V
I _{ОК}	output clamping current	V _O < 0 V		-50	-	mA
Vo	output voltage	active mode	[1]	-0.5	V _{CC} + 0.5	V
		power-down mode	[1]	-0.5	+4.6	V
lo	output current	V_{O} = 0 V to V_{CC}		-	± 20	mA
I _{CC}	supply current			-	+50	mA
I _{GND}	ground current			-50	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T_{amb} = -40 ° C to +125 ° C				
		TSSOP5, SC-74A, XSON6 and X2SON5 package	[2]	-	250	mW
		X2SON4 package	[3]	-	150	mW

The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.
 For TSSOP5 and SC-74A packages: above 87.5 ° C the value of P_{tot} derates linearly with 4.0 mW/K.
 For XSON6 and X2SON5 packages: above 118 ° C the value of P_{tot} derates linearly with 7.8 mW/K.
 For X2SON4 packages: above 57 °C the value of P_{tot} derates linearly with 1.7 mW/K.

9 Recommended operating conditions

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	active mode	0	V _{CC}	V
		power-down mode; V_{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 0.8 V to 3.6 V	0	200	ns/V

10 Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 28	5°C				1	
V _{IH}	HIGH-level input voltage	l input voltage V _{CC} = 0.8 V		-	-	V
		V_{CC} = 0.9 V to 1.95 V	0.65V _{CC}	-	-	V
		V_{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V_{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35V _{CC}	V
		V_{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V_{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = -20 $\mu\text{A};$ V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		$I_{\rm O}$ = -2.3 mA; $V_{\rm CC}$ = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		$I_{\rm O}$ = -2.7 mA; $V_{\rm CC}$ = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{\rm O}$ = 20 µA; $V_{\rm CC}$ = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.1	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-		V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
l _l	input leakage current	V_I = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	± 0.1	μA
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	± 0.2	μA
ΔI _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	± 0.2	μA
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A};$ $V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.5	μA
ΔI _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	40	μA
CI	input capacitance	V_{CC} = 0 V to 3.6 V; V _I = GND or V _{CC}	-	0.8	-	pF
Co	output capacitance	$V_{O} = GND; V_{CC} = 0 V$	-	1.7	-	pF
T _{amb} = -4	0 ° C to +85 ° C					
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70V _{CC}	-	- 40 0.8 - 1.7 - 	V
		V _{CC} = 0.9 V to 1.95 V	0.65V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	0.3V _{CC} 0.31 0.31 0.31 0.31 0.44 0.31 0.44 10.31 0.44 10.31 0.44 ±0.1 ±0.2 ±0.2 ±0.2 ±0.3 40 - 0.5 40 - 0.5 40 - 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.5 0.30V _{CC} 0.7 0.9 0.1 - - - - - - - - - -	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.31 0.31 0.31 0.31 0.44 0.31 0.44 0.31 0.44 1	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.3V _{CC} 0.31 0.31 0.31 0.44 0.31 0.44 0.31 0.44 10.1 ± 0.1 ± 0.2 ± 0.2 1 40 - 0.5 40 - 0.5 40 - 0.5 0.30V _{CC} 0.30V _{CC} 0.35V _{CC} 0.7 0.9 - - - - - - - 0.35V _{CC} 0.7 0.9 - - - - - - - - - - - - - - - - <	V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = -20 µA; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V
		I_0 = -2.3 mA; V_{CC} = 2.3 V	1.97	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.67	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.55	-	-	V

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Low-power inverter

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.1	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-		V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	V
l _l	input leakage current	V_I = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	± 0.5	μA
I _{OFF}	power-off leakage current	V_1 or V_0 = 0 V to 3.6 V; V_{CC} = 0 V	-	-	± 0.5	μA
ΔI _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	± 0.6	μA
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A}; V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	0.9	μA
Δl _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	50	μA
T _{amb} = -4	0 ° C to +125 ° C	,			1	
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.75V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.70V _{CC}	-	 	V
		V _{CC} = 2.3 V to 2.7 V	1.6	 	V	
		V _{CC} = 3.0 V to 3.6 V	2.0	-	 0.37 0.35 0.33 0.45 0.45 ± 0.5 ± 0.6 ± 0.6 0.9 50 50 - - 0.25V_{CC} 0.30V_{CC} 0.7 0.9 - 	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.25V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.3V _{CC} 0.37 0.35 0.35 0.33 0.45 0.33 0.45 0.33 0.45 0.33 0.45 0.33 0.45 10.33 0.45 10.33 0.45 10.33 10.45 10.33 10.45 10.30 10.5 10.5 10.5 10.9 10.25V _{CC} 0.30V _{CC} 0.7 0.9 10.25V _{CC} 0.30V _{CC} 0.7 0.9 10.10 10.25V _{CC} 10.10 10.25V _{CC} 10.10 10.10 10.10 10.10 10.10 10.10 10.10 10.10 10.10 10.10 10.10 10.10	V
		V _{CC} = 2.3 V to 2.7 V	-	-		V
		V _{CC} = 3.0 V to 3.6 V	-	-		V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_{\rm O}$ = -20 µA; $V_{\rm CC}$ = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		$I_{O} = -4.0 \text{ mA}; V_{CC} = 3.0 \text{ V}$	2.30	-	_	V

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		$I_{\rm O}$ = 2.3 mA; $V_{\rm CC}$ = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.36	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.50	V
lį –	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	± 0.75	μA
I _{OFF}	power-off leakage current	V_1 or V_0 = 0 V to 3.6 V; V_{CC} = 0 V	-	-	± 0.75	μA
ΔI _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	± 0.75	μA
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A}; \\ V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-	-	1.4	μA
Δl _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	75	μA

11 Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions	Min	Typ ^[1]	Мах	Unit
T _{amb} = 2	5 ° C; C _L = 5 pF	· · · ·		1	1	
t _{pd}	propagation delay	A to Y; see <u>Figure 8</u> ^[2]				
		V _{CC} = 0.8 V	-	16.0	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.4	5.0	10.3	ns
		V _{CC} = 1.4 V to 1.6 V	1.8	3.6	6.4	ns
		V _{CC} = 1.65 V to 1.95 V	1.5	2.9	5.0	ns
		V_{CC} = 2.3 V to 2.7 V	1.2	2.4	3.9	ns
		V_{CC} = 3.0 V to 3.6 V	1.1	2.1	3.2	ns
T _{amb} = 2	5 ° C; C _L = 10 pF			1	1	
t _{pd}	propagation delay	A to Y; see <u>Figure 8</u> ^[2]				
		V _{CC} = 0.8 V	-	19.8	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.8	5.9	12.2	ns
		V _{CC} = 1.4 V to 1.6 V	2.3	4.2	7.5	ns
		V _{CC} = 1.65 V to 1.95 V	2.0	3.5	5.9	ns
		$V_{\rm CC}$ = 2.3 V to 2.7 V	1.7	2.9	4.6	ns

 $V_{\rm CC}$ = 3.0 V to 3.6 V

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3.8

1.6

2.7

ns

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Symbo	I Parameter	Conditions	Min	Typ ^[1]	Мах	Unit
T _{amb} = 2	25 ° C; C _L = 15 pF	· · · · ·				
t _{pd}	propagation delay	A to Y; see Figure 8 ^[2]				
		V _{CC} = 0.8 V	-	23.3	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.2	6.7	13.0	ns
		V _{CC} = 1.4 V to 1.6 V	2.6	4.7	8.6	ns
		V _{CC} = 1.65 V to 1.95 V	2.3	4.0	6.7	ns
		V_{CC} = 2.3 V to 2.7 V	2.1	3.3	5.1	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	3.1	4.2	ns
T _{amb} = 2	25 ° C; C _L = 30 pF	· · · ·		-		
t _{pd}	propagation delay	A to Y; see Figure 8 ^[2]				
		V _{CC} = 0.8 V	-	33.6	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.4	8.9	16.0	ns
		V _{CC} = 1.4 V to 1.6 V	3.6	6.3	10.8	ns
		V _{CC} = 1.65 V to 1.95 V	3.2	5.3	9.0	ns
		V_{CC} = 2.3 V to 2.7 V	2.9	4.5	6.5	ns
		$V_{\rm CC}$ = 3.0 V to 3.6 V	2.9	4.2	5.4	ns
T _{amb} = 2	25 ° C	· · · ·				
C _{PD}	power dissipation capacit	ance $f = 1 \text{ MHz}$; $V_I = \text{GND to } V_{CC}$ ^[3]				
		V _{CC} = 0.8 V	-	2.5	-	pF
		V _{CC} = 1.1 V to 1.3 V	-	2.7	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.8	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	3.0	-	pF
		V_{CC} = 2.3 V to 2.7 V	-	3.5	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.0	-	pF

[1] All typical values are measured at nominal V_{CC} .

[2] tr_{pd} is the same as t_{PLH} and t_{PHL}. [3] C_{PD} is used to determine the dynamic power dissipation (P_D in µW). $P_{D} = C_{PD} \times V_{CC}^{2} \times f_{i} \times N + \sum (C_{L} \times V_{CC}^{2} \times f_{o}) \text{ where:}$

 f_i = input frequency in MHz;

 f_o = output frequency in MHz;

C_L = output load capacitance in pF;

$$\begin{split} &V_{CC} = \text{supply voltage in V;} \\ &N = \text{number of inputs switching;} \\ &\sum (C_L \times V_{CC}^2 \times f_o) = \text{sum of the outputs.} \end{split}$$

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Table 9. Dynamic characteristics

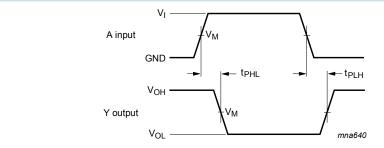
Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 9

Symbol	Parameter	Conditions	-40 ° C 1	o +85 ° C	-40 ° C t	Unit	
			Min	Мах	Min	Мах	
C _L = 5 pF	=				1		
t _{pd} propagation delay		A to Y; see <u>Figure 8</u> [1]					
		V _{CC} = 1.1 V to 1.3 V	2.1	11.4	2.1	12.6	ns
		V _{CC} = 1.4 V to 1.6 V	1.6	7.4	1.6	8.2	ns
		V _{CC} = 1.65 V to 1.95 V	1.4	5.9	1.4	6.5	ns
		V_{CC} = 2.3 V to 2.7 V	1.1	4.5	1.1	5.0	ns
		V _{CC} = 3.0 V to 3.6 V	1.0	3.9	1.0	4.3	ns
C _L = 10 p	oF				1	1	
t _{pd}	propagation delay	A to Y; see <u>Figure 8</u> ^[1]					
		V _{CC} = 1.1 V to 1.3 V	2.6	13.7	2.6	15.1	ns
		V _{CC} = 1.4 V to 1.6 V	2.1	8.7	2.1	9.6	ns
		V _{CC} = 1.65 V to 1.95 V	1.8	7.0	1.8	7.7	ns
		V_{CC} = 2.3 V to 2.7 V	1.5	5.4	1.5	6.0	ns
		V_{CC} = 3.0 V to 3.6 V	1.4	4.5	1.4	5.0	ns
C _L = 15 p	oF				1	1	
t _{pd}	propagation delay	A to Y; see <u>Figure 8</u> ^[1]					
		V _{CC} = 1.1 V to 1.3 V	3.0	15.8	3.0	17.4	ns
		V _{CC} = 1.4 V to 1.6 V	2.4	10.0	2.4	11.0	ns
		V _{CC} = 1.65 V to 1.95 V	2.1	8.0	2.1	8.8	ns
		V_{CC} = 2.3 V to 2.7 V	1.8	6.1	1.8	6.8	ns
		V_{CC} = 3.0 V to 3.6 V	1.8	5.0	1.8	5.5	ns
C _L = 30 p	р F						
t _{pd}	propagation delay	A to Y; see <u>Figure 8</u> ^[1]					
		V _{CC} = 1.1 V to 1.3 V	4.0	19.0	4.0	20.9	ns
		V _{CC} = 1.4 V to 1.6 V	3.2	12.9	3.2	14.2	ns
		V _{CC} = 1.65 V to 1.95 V	2.9	10.5	2.9	11.6	ns
		V_{CC} = 2.3 V to 2.7 V	2.6	7.6	2.6	8.4	ns
		V _{CC} = 3.0 V to 3.6 V	2.6	6.2	2.6	6.9	ns

[1] t_{pd} is the same as t_{PLH} and t_{PHL} .

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11.1 Waveform and test circuit



Measurement points are given in Table 10.

Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Figure 8. The data input (A) to output (Y) propagation delays

Table 10.	Measurement points	
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Supply voltage	pply voltage Output		Input					
V _{cc}	V _M	V _M	VI	t _r = t _f				
0.8 V to 3.6 V	0.5 x V _{CC}	0.5 x V _{CC}	V _{CC}	≤ 3.0 ns				

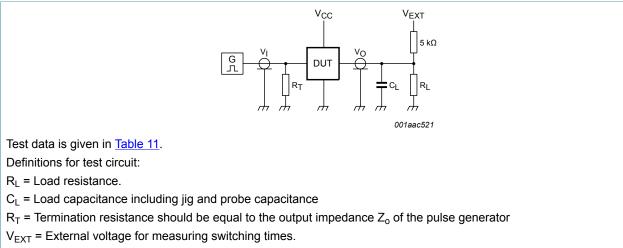


Figure 9. Test circuit for measuring switching times

Table 11. Test data

Supply voltage	Load		V _{EXT}			
V _{cc}	CL	R _L ^[1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}	
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 kΩ or 1 MΩ	open	GND	2 x V _{CC}	

[1] For measuring enable and disable times $R_L = 5 k\Omega$, for measuring propagation delays, setup and hold times and pulse width $R_L = 1 M\Omega$.

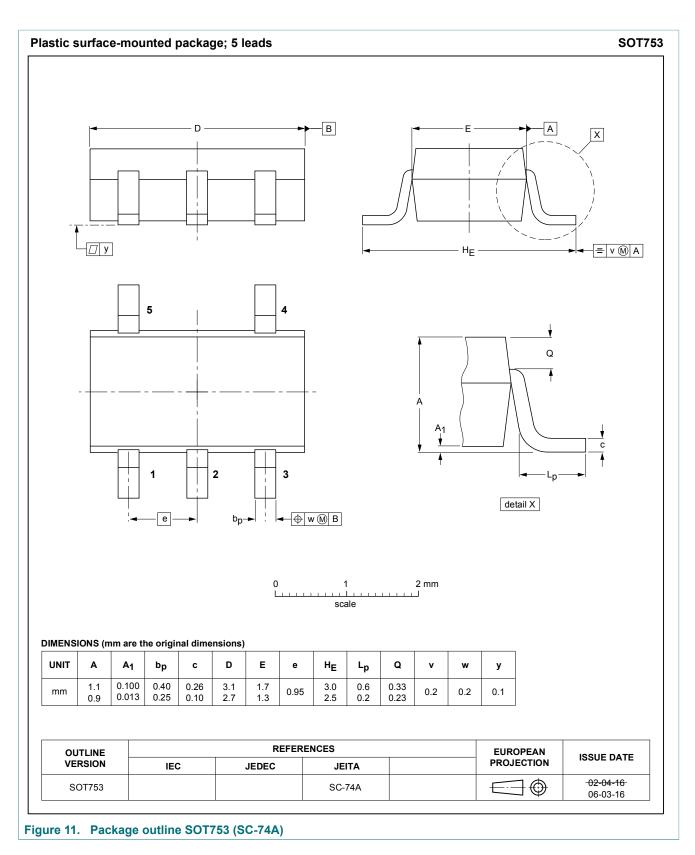
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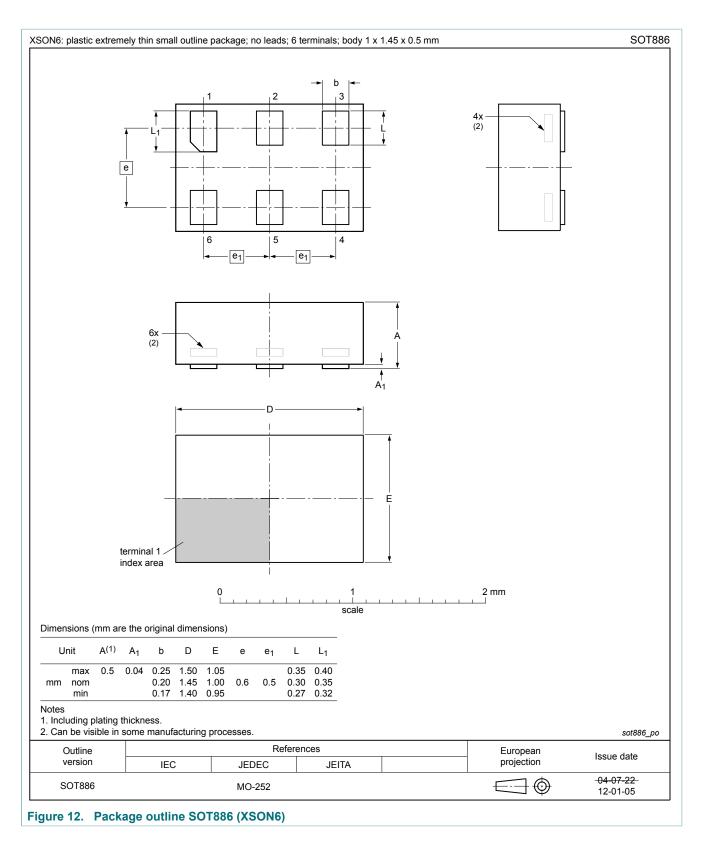
12 Package outline

	: plas	tic thi	n shr	ink sr	nall o	utline	pack	age; 5	i leads	s; boc	ly wid	th 1.2	5 mm				SC	DT353
		Ĺ	- <i>□</i> y						с	¥ ¥		E		X	(M) A			
		-		- Z		4 3 - ⊕ w	(M)				A ₁	detail		(A ₃)	A A A A A A A A A A A A A A A A A A A			
					0		1.t			3 mm 								
	IONS (m				nension		sca	le	e		He			v	w	v	Z(1)	θ
DIMENS UNIT mm		A₁ 0.1	A₂ 1.0	inal din A ₃ 0.15	bp 0.30	c 0.25	sca D ⁽¹⁾ 2.25	E(1)	e 0.65	3 mm e 1 1.3	Н _Е 2.25 2.0	L 0.425	Lp 0.46 0.21	v 0.3	w 0.1	y 0.1	Z(1) 0.60 0.15	θ 7° 0°
UNIT mm lote	A max. 1.1	A₁ 0.1 0	A₂ 1.0 0.8	A ₃ 0.15	b p 0.30 0.15	c 0.25 0.08	sca D(1) 2.25 1.85	E(1) 1.35 1.15	0.65	e1	Н Е 2.25 2.0							
UNIT mm Note	A max. 1.1	A₁ 0.1 0	A₂ 1.0 0.8	A ₃ 0.15	b p 0.30 0.15	c 0.25 0.08	sca D(1) 2.25 1.85 side are	E(1) 1.35 1.15	0.65 luded.	e1			0.46	0.3	0.1	0.1	0.60 0.15	7° 0°
UNIT mm Note . Plastic	A max. 1.1	A₁ 0.1 0	A ₂ 1.0 0.8 sions of	A ₃ 0.15	b p 0.30 0.15	c 0.25 0.08	D(1) 2.25 1.85 side ard REFEI	E(1) 1.35 1.15 e not inc	0.65 luded.	e ₁			0.46 0.21		0.1 PEAN	0.1	0.60	7° 0°

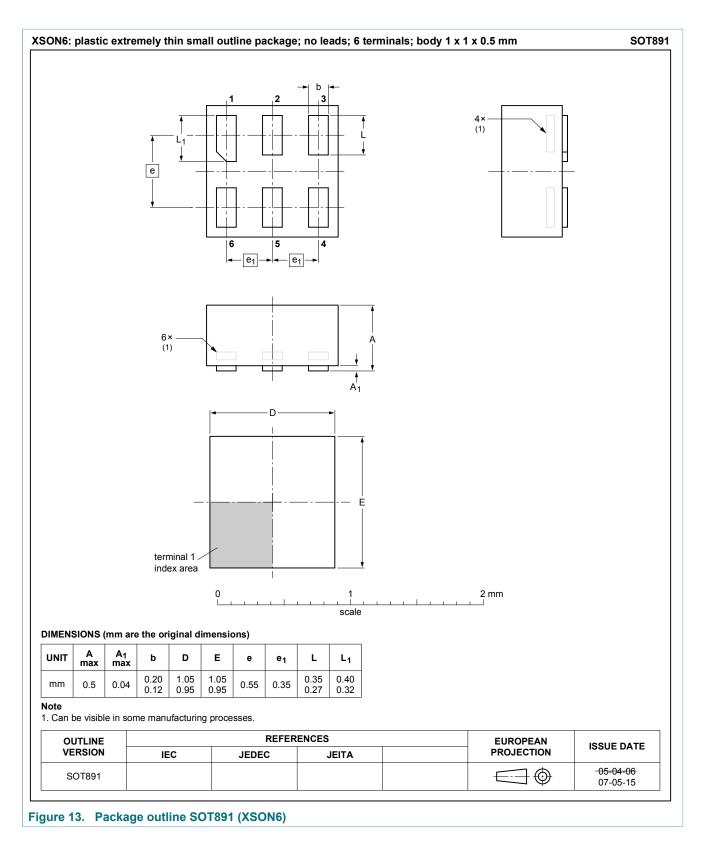
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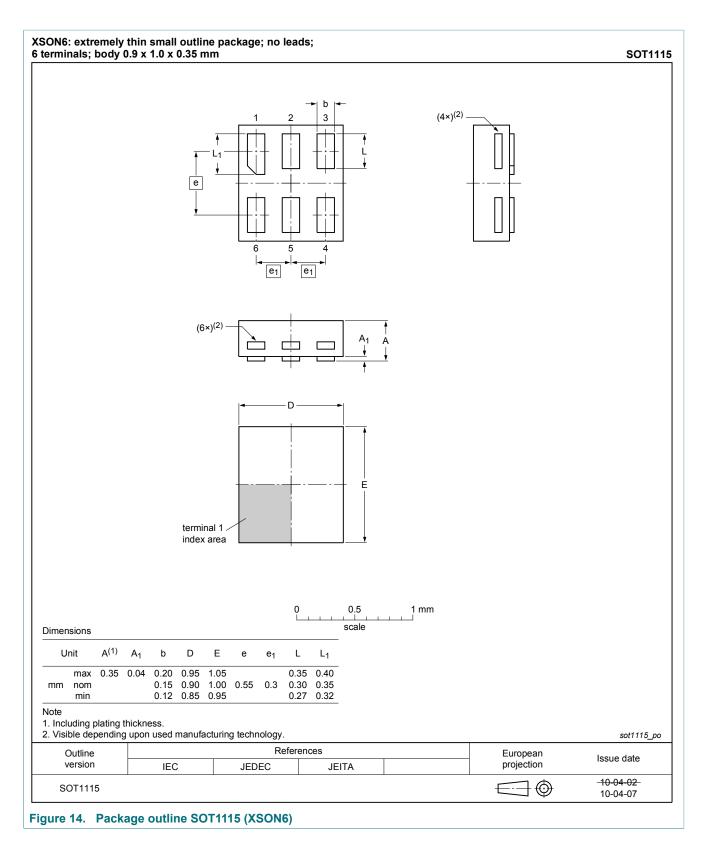
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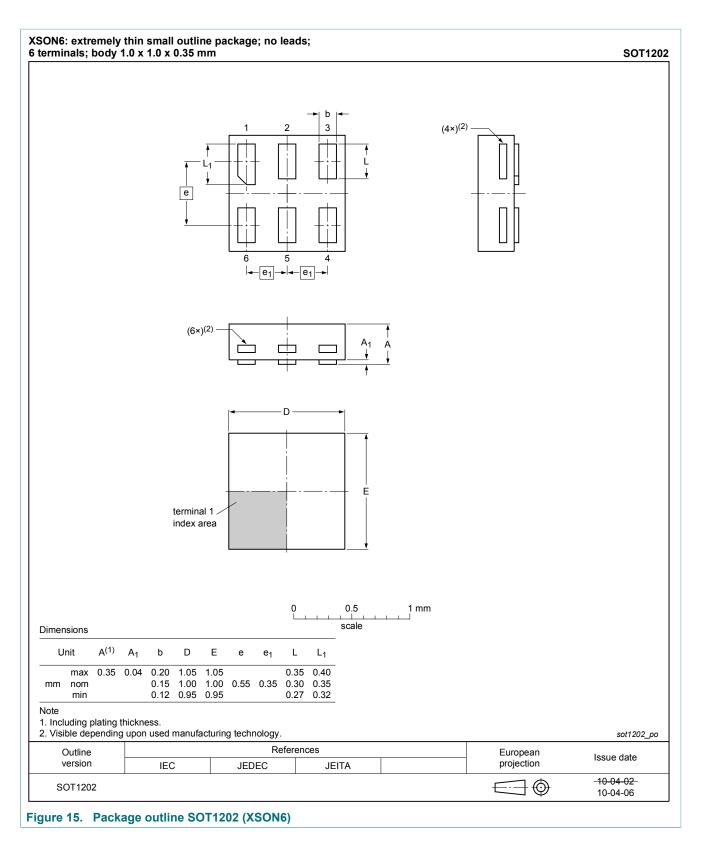
Low-power inverter



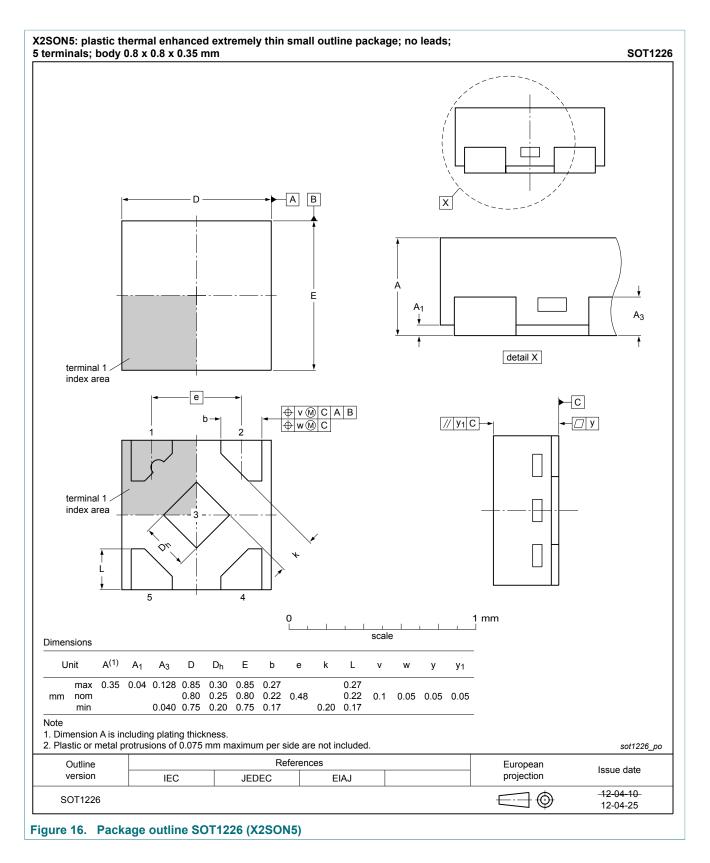
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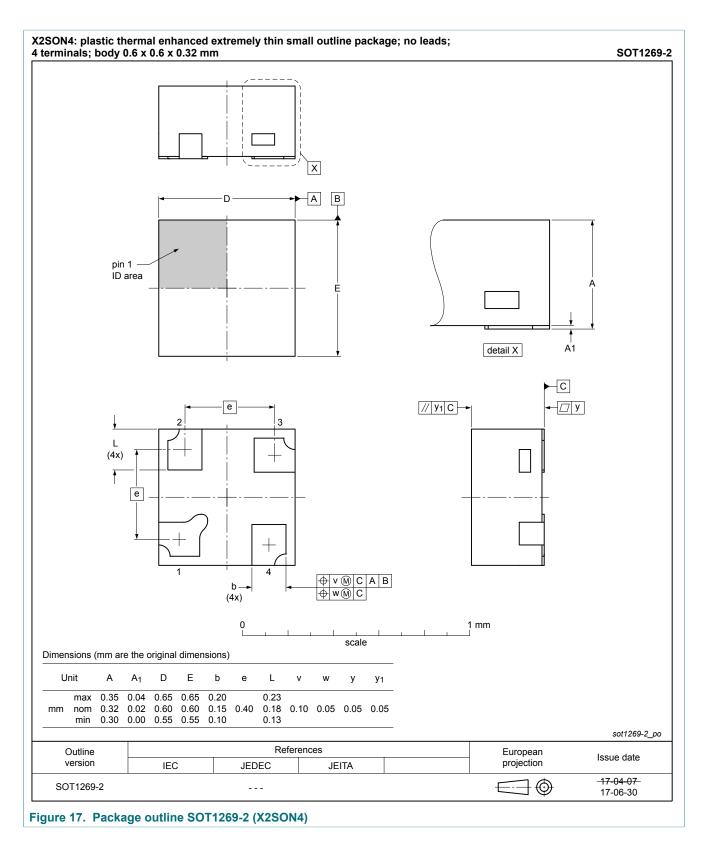
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13 Abbreviations

Table 12. Abbreviations						
Acronym	Description					
CDM	Charged Device Model					
DUT	Device Under Test					
ESD	ElectroStatic Discharge					
НВМ	Human Body Model					
MM	Machine Model					

14 Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes				
74AUP1G04 v.9	20180608	Product data sheet	-	74AUP1G04 v.8				
Modifications:	Added type number 74AUP1G04GX4 (SOT1269-2/X2SON4)							
74AUP1G04 v.8	20171107	Product data sheet	-	74AUP1G04 v.7				
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guideline Nexperia. Legal texts have been adapted to the new company name where appropriate. 							
74AUP1G04 v.7	20120627	Product data sheet	-	74AUP1G04 v.6				
Modifications:	Added type number 74AUP1G04GX (SOT1226)							
74AUP1G04 v.6	20120214	Product data sheet	-	74AUP1G04 v.5				
Modifications:	 Package outline dra 	awing of SOT886 (<u>Figure 12</u>) mod	dified.					
74AUP1G04 v.5	20111205	Product data sheet	-	74AUP1G04 v.4				
Modifications:	Legal pages updated.							
74AUP1G04 v.4	20100630	Product data sheet	-	74AUP1G04 v.3				
74AUP1G04 v.3	20091105	Product data sheet	-	74AUP1G04 v.2				
74AUP1G04 v.2	20060628	Product data sheet	-	74AUP1G04 v.1				
74AUP1G04 v.1 20050718 Product		Product data sheet	-	-				

15 Legal information

15.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

The term 'short data sheet' is explained in section "Definitions".

[2] [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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