Low-power buffer with voltage-level translator Rev. 1 — 28 November 2017

Product data sheet

General description 1

The 74AUP1T50 provides the single buffer function. This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 2.3 V to 3.6 V.

The 74AUP1T50 is designed for logic-level translation applications with input switching levels that accept 1.8 V low-voltage CMOS signals, while operating from either a single 2.5 V or 3.3 V supply voltage.

The wide supply voltage range ensures normal operation as battery voltage drops from 3.6 V to 2.3 V.

This device is fully specified for partial power-down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

Schmitt trigger inputs make the circuit tolerant to slower input rise and fall times across the entire V_{CC} range.

Features and benefits 2

- Wide supply voltage range from 2.3 V to 3.6 V
- High noise immunity
- ESD protection:
 - HBM JESD22-A114F Class 3A exceeds 5000 V
 - CDM JESD22-C101E exceeds 1000 V
- Low static power consumption; I_{CC} = 1.5 μA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78 Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- I_{OFF} circuitry provides partial power-down mode operation
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

ne<mark>x</mark>peria

3 Ordering information

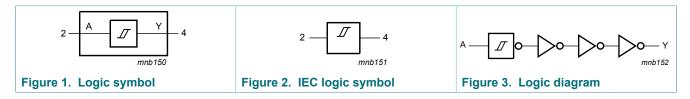
Table 1. Ordering information										
Type number	Package									
	Temperature range	Name	Description	Version						
74AUP1T50GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1						
74AUP1T50GX	-40 °C to +125 °C	X2SON5	plastic thermal enhanced extremely thin small outline package; no leads; 5 terminals; body 0.8 x 0.8 x 0.35 mm	SOT1226						

4 Marking

Table 2. Marking							
Type number	Marking code ^[1]						
74AUP1T50GW	5E						
74AUP1T50GX	5E						

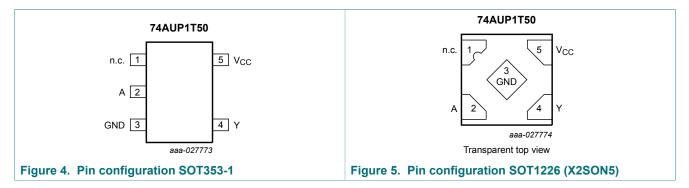
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5 Functional diagram



6 **Pinning information**

6.1 Pinning



74AUP1T50 Product data sheet

Low-power buffer with voltage-level translator

6.2 Pin description

Table 3. Pin description								
Symbol	Pin	Description						
n.c.	1	not connected						
A	2	data input						
GND	3	ground (0 V)						
Y	4	data output						
V _{CC}	5	supply voltage						

Functional description 7

Table 4. Function table ^[1]

Input	Output
Α	Y
L	L
Н	Н

[1] H = HIGH voltage level;

L = LOW voltage level.

Limiting values 8

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+4.6	V
l _{IK}	input clamping current	V _I < 0 V		-50	-	mA
VI	input voltage		[1]	-0.5	+4.6	V
I _{OK}	output clamping current	V _O < 0 V		-50	-	mA
Vo	output voltage	Active mode and Power-down mode	[1]	-0.5	+4.6	V
l _O	output current	$V_{O} = 0 V$ to V_{CC}		-	±20	mA
I _{CC}	supply current			-	50	mA
I _{GND}	ground current			-50	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T_{amb} = -40 °C to +125 °C	[2]	-	250	mW

The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.
For TSSOP5 packages: above 87.5 °C the value of P_{tot} derates linearly with 4.0 mW/K.
For X2SON5 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

Low-power buffer with voltage-level translator

9 Recommended operating conditions

Table 6. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		2.3	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V_{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C

10 Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	5 °C				1	
V _{T+}	positive-going threshold	V_{CC} = 2.3 V to 2.7 V	0.60	-	1.10	V
	voltage	V _{CC} = 3.0 V to 3.6 V	0.75	-	1.16	V
V _{T-}	negative-going threshold	V_{CC} = 2.3 V to 2.7 V	0.35	-	0.60	V
	voltage	V _{CC} = 3.0 V to 3.6 V	0.50	-	0.85	V
V _H	hysteresis voltage	$(V_{H} = V_{T+} - V_{T-})$				
		V _{CC} = 2.3 V to 2.7 V	0.23	-	0.60	V
		V _{CC} = 3.0 V to 3.6 V	0.25	-	0.56	V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{T+}$ or V_{T-}				
		I_{O} = -20 µA; V_{CC} = 2.3 V to 3.6 V	V _{CC} - 0.1	-	-	V
		$I_{\rm O}$ = -2.3 mA; $V_{\rm CC}$ = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		$I_{\rm O}$ = -2.7 mA; $V_{\rm CC}$ = 3.0 V	2.72	-	-	V
		I_{O} = -4.0 mA; V_{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{T+}$ or V_{T-}				
		I_{O} = 20 µA; V_{CC} = 2.3 V to 3.6 V	-	-	0.10	V
		$I_{\rm O}$ = 2.3 mA; $V_{\rm CC}$ = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I_{O} = 2.7 mA; V_{CC} = 3.0 V	-	-	0.31	V
		I_{O} = 4.0 mA; V_{CC} = 3.0 V	-	-	0.44	V
l _l	input leakage current	V_1 = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.1	μA
I _{OFF}	power-off leakage current	V_1 or V_0 = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.1	μA
ΔI _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V;}$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.1	μA
I _{CC}	supply current	V_I = GND or V_{CC} ; I_O = 0 A; V_{CC} = 2.3 V to 3.6 V	-	-	1.2	μA

© Nexperia B.V. 2017. All rights reserved.

Nexperia

74AUP1T50

Low-power buffer with voltage-level translator

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Cı	input capacitance	V_{CC} = 0 V to 3.6 V; V _I = GND or V _{CC}	-	0.8	-	pF
Co	output capacitance	V_{O} = GND; V_{CC} = 0 V	-	1.7	-	pF
T _{amb} = -4	0 °C to +85 °C					
V _{T+}	positive-going threshold	V _{CC} = 2.3 V to 2.7 V	0.60	-	1.10	V
	voltage	V _{CC} = 3.0 V to 3.6 V	0.75	-	1.19	V
V _{T-}	negative-going threshold	V _{CC} = 2.3 V to 2.7 V	0.35	-	0.60	V
	voltage	V _{CC} = 3.0 V to 3.6 V	0.50	-	0.85	V
V _H	hysteresis voltage	$(V_{H} = V_{T+} - V_{T-})$				
		V _{CC} = 2.3 V to 2.7 V	0.10	-	0.60	V
		V _{CC} = 3.0 V to 3.6 V	0.15	-	0.56	V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{T+}$ or V_{T-}				
		I_{O} = -20 µA; V_{CC} = 2.3 V to 3.6 V	V _{CC} - 0.1	-	-	V
		$I_{\rm O}$ = -2.3 mA; $V_{\rm CC}$ = 2.3 V	1.97	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V
		$I_{\rm O}$ = -2.7 mA; $V_{\rm CC}$ = 3.0 V	2.67	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.55	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{T+}$ or V_{T-}				
		I_{O} = 20 µA; V_{CC} = 2.3 V to 3.6 V	-	-	0.1	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.45	V
l _l	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.5	μA
I _{OFF}	power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±0.5	μA
Δl _{OFF}	additional power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.5	μA
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A}; \\ V_{CC} = 2.3 \text{ V to } 3.6 \text{ V}$	-	-	1.5	μA
∆l _{CC}	additional supply current	$v_{\rm CC} = 2.3 \ v \ 10 \ 2.7 \ v, \ 10 = 0 \ {\rm A}$	1] _	-	0.6	μA
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V; } I_{O} = 0 \text{ A}$	2] _	-	10	μA

Nexperia

74AUP1T50

Low-power buffer with voltage-level translator

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -4	10 °C to +125 °C				1	_
V _{T+}	positive-going threshold	V_{CC} = 2.3 V to 2.7 V	0.60	-	1.10	V
	voltage	V _{CC} = 3.0 V to 3.6 V	0.75	-	1.19	V
V _{T-}	negative-going threshold	V _{CC} = 2.3 V to 2.7 V	0.33	-	0.64	V
	voltage	V _{CC} = 3.0 V to 3.6 V	0.46	-	0.85	V
V _H	hysteresis voltage	$(V_{H} = V_{T+} - V_{T-})$				
		V _{CC} = 2.3 V to 2.7 V	0.10	-	0.60	V
		V _{CC} = 3.0 V to 3.6 V	0.15	-	0.56	V
V _{OH}	HIGH-level output voltage	$V_{I} = V_{T+}$ or V_{T-}				
		I_{O} = -20 μ A; V _{CC} = 2.3 V to 3.6 V	V _{CC} - 0.11	-	-	V
		$I_{\rm O}$ = -2.3 mA; $V_{\rm CC}$ = 2.3 V	1.77	-	-	V
		$I_{\rm O}$ = -3.1 mA; $V_{\rm CC}$ = 2.3 V	1.67	-	-	V
		$I_{\rm O}$ = -2.7 mA; $V_{\rm CC}$ = 3.0 V	2.40	-	-	V
		$I_{\rm O}$ = -4.0 mA; $V_{\rm CC}$ = 3.0 V	2.30	-	-	V
V _{OL}	LOW-level output voltage	$V_1 = V_{T+}$ or V_{T-}				
		I_{O} = 20 µA; V_{CC} = 2.3 V to 3.6 V	-	-	0.11	V
		$I_{\rm O}$ = 2.3 mA; $V_{\rm CC}$ = 2.3 V	-	-	0.36	V
		I_{O} = 3.1 mA; V_{CC} = 2.3 V	-	-	0.50	V
		$I_{\rm O}$ = 2.7 mA; $V_{\rm CC}$ = 3.0 V	-	-	0.36	V
		I_{O} = 4.0 mA; V_{CC} = 3.0 V	-	-	0.50	V
l	input leakage current	V_1 = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.75	μA
I _{OFF}	power-off leakage current	V_1 or V_0 = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.75	μA
ΔI _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.75	μA
I _{CC}	supply current	V_I = GND or V_{CC} ; I_O = 0 A; V_{CC} = 2.3 V to 3.6 V	-	-	3.5	μA
∆l _{CC}	additional supply current	$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V; } I_{O} = 0 \text{ A}$	1] _	-	1.8	μA
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V; } I_{O} = 0 \text{ A}$	2] _	-	18	μA

11 Dynamic characteristics

Table 8. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Figure 7.

Symbol	Parameter	Conditions		25 °C		-40 °C to +125 °C			Unit
			Min	Тур ^[1]	Max	Min	Max (85 °C)	Max (125 °C)	
V _{CC} = 2.5	3 V to 2.7 V; V _I	= 1.65 V to 1.95 V						1	
t _{pd}	propagation	A, B to Y; see Figure 6	[2]						
	delay	C _L = 5 pF	2.0	3.2	4.9	0.5	6.8	7.5	ns
		C _L = 10 pF	2.4	3.8	5.6	1.0	7.9	8.7	ns
		C _L = 15 pF	2.7	4.3	6.1	1.0	8.7	9.6	ns
		C _L = 30 pF	3.6	5.4	7.6	1.5	10.8	11.9	ns
V _{CC} = 2.3	3 V to 2.7 V; V _I	= 2.3 V to 2.7 V						1	
	propagation	A, B to Y; see Figure 6	[2]						
	delay	C _L = 5 pF	1.6	3.1	5.0	0.5	6.0	6.6	ns
		C _L = 10 pF	2.0	3.7	5.7	1.0	7.1	7.9	ns
		C _L = 15 pF	2.3	4.1	6.3	1.0	7.9	8.7	ns
		C _L = 30 pF	3.2	5.3	7.8	1.5	10.0	11.0	ns
V _{CC} = 2.3	3 V to 2.7 V; V _I	= 3.0 V to 3.6 V					1	1	
t _{pd}	propagation delay	A, B to Y; see Figure 6	[2]						
		C _L = 5 pF	1.2	2.8	4.5	0.5	5.5	6.1	ns
		C _L = 10 pF	1.7	3.4	5.1	1.0	6.5	7.2	ns
		C _L = 15 pF	2.0	3.9	5.7	1.0	7.4	8.2	ns
		C _L = 30 pF	2.8	5.0	7.2	1.5	9.5	10.5	ns
V _{CC} = 3.	0 V to 3.6 V; V _I	= 1.65 V to 1.95 V					1		
t _{pd}	propagation	A, B to Y; see Figure 6	[2]						
	delay	C _L = 5 pF	1.8	2.7	3.7	0.5	8.0	8.8	ns
		C _L = 10 pF	2.2	3.2	4.4	1.0	8.5	9.4	ns
		C _L = 15 pF	2.7	3.7	5.0	1.0	9.1	10.1	ns
		C _L = 30 pF	3.5	4.9	6.3	1.5	9.8	10.8	ns
V _{CC} = 3.	0 V to 3.6 V; V _I	= 2.3 V to 2.7 V							
t _{pd}	propagation	A, B to Y; see Figure 6	[2]						
	delay	C _L = 5 pF	1.4	2.6	3.8	0.5	5.3	5.9	ns
		C _L = 10 pF	1.9	3.1	4.5	1.0	6.1	6.8	ns
		C _L = 15 pF	2.2	3.6	5.1	1.0	6.8	7.5	ns
		C _L = 30 pF	3.0	4.8	6.6	1.5	8.5	9.4	ns

Low-power buffer with voltage-level translator

Symbol	Parameter	ter Conditions		25 °C		-40 °C to +125 °C			Unit
			Min	Typ ^[1]	Мах	Min	Мах (85 °С)	Max (125 °C)	
$V_{\rm CC}$ = 3.0	V to 3.6 V; V _I	= 3.0 V to 3.6 V						1	
t _{pd}	propagation	A, B to Y; see <u>Figure 6</u> ^[2]							
	delay	C _L = 5 pF	1.1	2.5	4.0	0.5	4.7	5.2	ns
		C _L = 10 pF	1.6	3.1	4.5	1.0	5.7	6.3	ns
		C _L = 15 pF	1.9	3.6	5.1	1.0	6.2	6.9	ns
		C _L = 30 pF	2.7	4.7	6.6	1.5	7.8	8.6	ns
T _{amb} = 2	5 °C					1		1	
C _{PD}	power	f_i = 1 MHz; V_I = GND to V_{CC} ^[3]							
	dissipation capacitance	V _{CC} = 2.3 V to 2.7 V	-	4	-	-	-	-	pF
		V_{CC} = 3.0 V to 3.6 V	-	5	-	-	-	-	pF

[1] All typical values are measured at nominal V_{CC}. [2] t_{pd} is the same as t_{PLH} and t_{PHL} [3] C_{PD} is used to determine the dynamic power dissipation (P_D in µW). P_D = $C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where: f_i = input frequency in MHz;

fo = output frequency in MHz;

 \tilde{C}_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching; $\Sigma(C_L \times V_{CC}^2 \times f_0)$ = sum of the outputs.

Product data sheet

74AUP1T50

Low-power buffer with voltage-level translator

11.1 Waveforms and test circuit

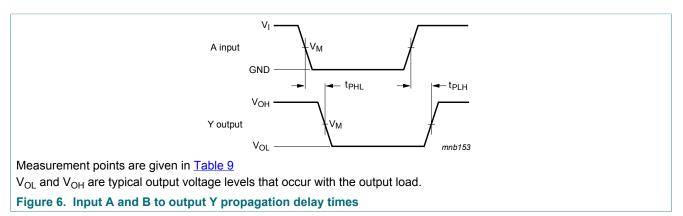


Table 9. Measurement points

Supply voltage	Output	Input					
V _{cc}	V _M	V _M	VI	t _r = t _f			
2.3 V to 3.6 V	$0.5 \times V_{CC}$	0.5 × V _I	1.65 V to 3.6 V	≤ 3.0 ns			

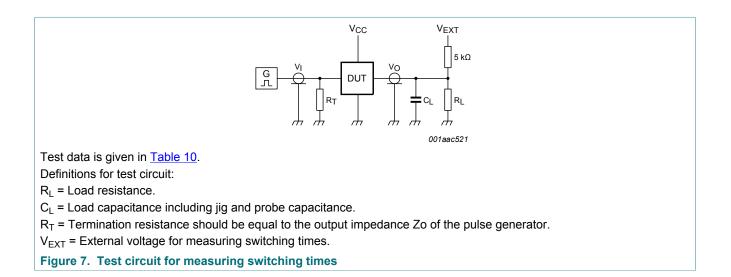


Table 10. Test data

Supply voltage	Load		V _{EXT}				
V _{cc}	CL	R _L ^[1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}		
2.3 V to 3.6 V	5 pF, 10 pF, 15 pF and 30 pF	5 k Ω or 1 M Ω	open	GND	$2 \times V_{CC}$		

[1] For measuring enable and disable times $R_L = 5 k\Omega$.

For measuring propagation delays, setup and hold times and pulse width R_L = 1 M Ω .

Low-power buffer with voltage-level translator

12 Package outline

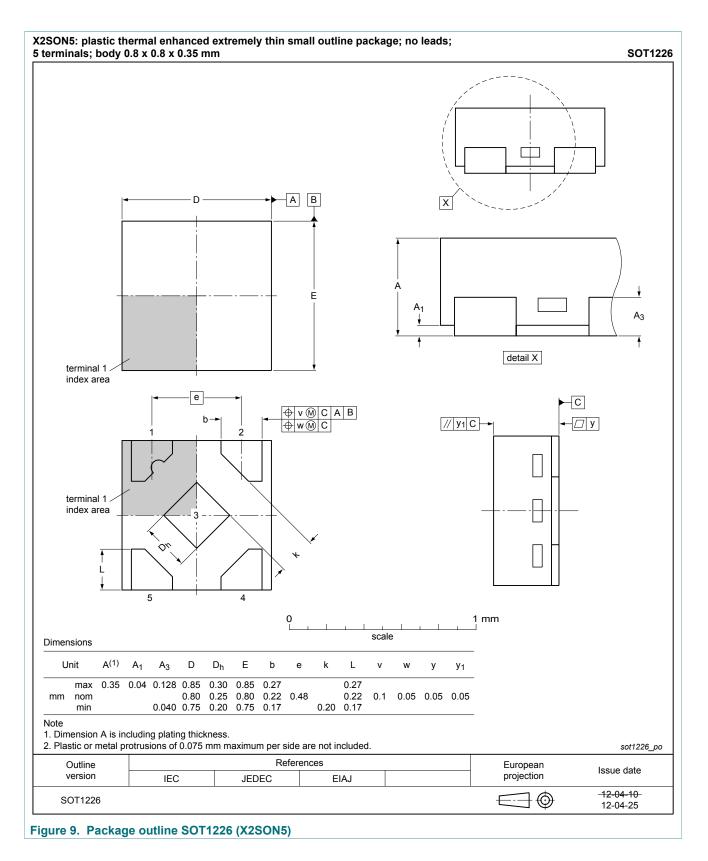
	. pias	uc in	in shr			utiine	раска	age; s	lead	5; 00	ay wic	ith 1.2	ອ mn	n			50	OT35
		Ī							с	¥ *		- E		X] () A			
		-		- z		4				A ₂	A ₁		Lp	(A ₃)	A A 7 0			
			-	e	⊢ - ⊳ b _p	- ⊕ w	/ M					detail	< L →	.				
	IONS (n			jinal din	0 ∟ nension	s)	1.5 sca	le		3 mm		detail					-(1)	
UNIT	A max.	A ₁	A ₂	jinal din	0 bp	s) c	1.5 sca	le E ⁽¹⁾	e	e ₁	Н _Е 2.25	L	Lp	v	w	У	Z(1) 0.60	θ 7°
UNIT mm	Α			jinal din	0 ∟ nension	s)	1.5 sca	le	e 0.65		Н _Е 2.25 2.0			0.2	w 0.1	y 0.1	Z(1) 0.60 0.15	
UNIT mm lote	A max. 1.1	A₁ 0.1 0	A2	jinal din A3 0.15	0 0 0.30 0.15	s) c 0.25 0.08	1.5 sca D(1) 2.25 1.85	E(1) 1.35 1.15	0.65	e ₁	2.25	L	L р 0.46	0.2			0.60	7°
UNIT mm lote . Plastic	A max.	A₁ 0.1 0	A2	jinal din A3 0.15	0 0 0.30 0.15	s) c 0.25 0.08	1.5 sca D(1) 2.25 1.85	E(1) 1.35 1.15	0.65 cluded.	e ₁	2.25	L	L р 0.46	0.2	0.1	0.1	0.60 0.15	7° 0°
UNIT mm Note I. Plastic	A max. 1.1	A₁ 0.1 0	A2 1.0 0.8	jinal din A3 0.15	0 0 0.30 0.15	s) c 0.25 0.08	1.5 sca D(1) 2.25 1.85 side are REFEF	E(1) 1.35 1.15 e not inc	0.65 cluded.	e ₁	2.25	L	L р 0.46	0.3	0.1 PEAN	0.1	0.60	7° 0°

Figure 8. Package outline SOT353-1 (TSSOP5)

74AUP1T50 Product data sheet

10 / 15

Low-power buffer with voltage-level translator



13 Abbreviations

Table 11. Abbreviations					
Acronym	Description				
CDM	Charged Device Model				
CMOS	Complementary Metal-Oxide Semiconductor				
DUT	Device Under Test				
ESD	ElectroStatic Discharge				
НВМ	Human Body Model				

14 Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP1T50 v.1	20171128	Product data sheet	-	-

15 Legal information

15.1 Data sheet status

Document status ^{[1][2]}	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

The term 'short data sheet' is explained in section "Definitions".

[2] [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

15.2 Definitions

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. Nexperia does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local Nexperia sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between Nexperia and its customer, unless Nexperia and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the Nexperia product is deemed to offer functions and qualities beyond those described in the Product data sheet.

15.3 Disclaimers

Limited warranty and liability - Information in this document is believed to be accurate and reliable. However, Nexperia does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. Nexperia takes no responsibility for the content in this document if provided by an information source outside of Nexperia. In no event shall Nexperia be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory. Notwithstanding any damages that customer might incur for any reason whatsoever, Nexperia's aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of Nexperia

Right to make changes - Nexperia reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use - Nexperia products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an Nexperia product can reasonably be expected to result in personal injury, death or severe property or environmental damage. Nexperia and its suppliers accept no liability for inclusion and/or use of Nexperia products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. Nexperia makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification. Customers are responsible for the design and operation of their applications and products using Nexperia products, and Nexperia accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the Nexperia product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products. Nexperia does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using Nexperia products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). Nexperia does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale - Nexperia products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nexperia.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. Nexperia hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of Nexperia products by customer

No offer to sell or license - Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Low-power buffer with voltage-level translator

Non-automotive qualified products — Unless this data sheet expressly states that this specific Nexperia product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. Nexperia accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications. In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without Nexperia's warranty of the product for such automotive applications, use and specifications beyond Nexperia's specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies Nexperia for any liability, damages or failed product claims resulting from customer

design and use of the product for automotive applications beyond Nexperia's standard warranty and Nexperia's product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

15.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.