Single unbuffered inverter Rev. 6 — 25 July 2018

Product data sheet

1. General description

The 74HC1GU04 is a single unbuffered inverter. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC} .

2. Features and benefits

- Symmetrical output impedance
- Wide operating voltage range from 2.0 V to 6.0 V
- Low power dissipation
- Balanced propagation delays
- SOT353-1 and SOT753 package options

3. Ordering information

Table 1. Ordering information

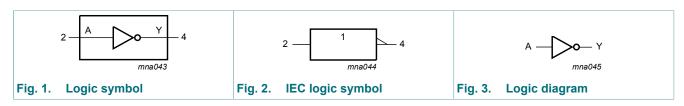
Type number	Package						
	Temperature range	Name	Description	Version			
74HC1GU04GW	-40 °C to +125 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1			
74HC1GU04GV	-40 °C to +125 °C	SC-74A	plastic surface-mounted package; 5 leads	SOT753			

4. Marking

Table 2. Marking codes				
Type number	Marking[1]			
74HC1GU04GW	HD			
74HC1GU04GV	HU4			

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

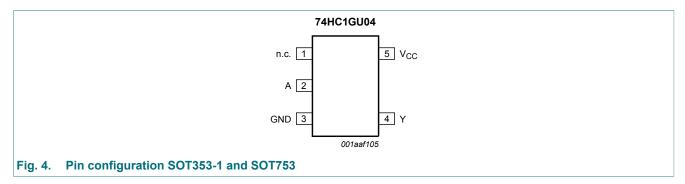
5. Functional diagram





6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description					
Symbol	Pin	Description			
n.c.	1	not connected			
A	2	data input			
GND	3	ground (0 V)			
Y	4	data output			
V _{CC}	5	supply voltage			

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level

Input	Output
A	Y
L	Н
Н	L

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Мах	Unit
V _{CC}	supply voltage			-0.5	+7.0	V
I _{IK}	input clamping current	$V_{\rm I}$ < -0.5 V or $V_{\rm I}$ > $V_{\rm CC}$ + 0.5 V	[1]	-	±20	mA
I _{OK}	output clamping current	$V_{\rm O}$ < -0.5 V or $V_{\rm O}$ > $V_{\rm CC}$ + 0.5 V	[1]	-	±20	mA
lo	output current	$-0.5 V < V_O < V_{CC} + 0.5 V$	[1]	-	±12.5	mA
I _{CC}	supply current			-	25	mA
I _{GND}	ground current			-25	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	[2]	-	200	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] Above 55 °C the value of P_{tot} derates linearity with 2.5 mW/K.

9. Recommended operating conditions

Table 6. Recommended operating conditions

Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage		2.0	5.0	6.0	V
VI	input voltage		0	-	V _{CC}	V
Vo	output voltage		0	-	V _{CC}	V
T _{amb}	ambient temperature		-40	+25	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 2.0 V	-	-	625	ns/V
		V _{CC} = 4.5 V	-	-	139	ns/V
		V _{CC} = 6.0 V	-	-	83	ns/V

10. Static characteristics

Table 7. Static characteristics

Voltages are referenced to GND (ground = 0 V). All typical values are measured at T_{amb} = 25 °C.

Symbol Parameter		Conditions	-40 °C to +85 °C			-40 °C t	Unit	
			Min	Тур	Мах	Min	Max	
V _{IH} HIGH-level input voltage	V _{CC} = 2.0 V	1.7	1.4	-	1.7	-	V	
	voltage	V _{CC} = 4.5 V	3.6	2.6	-	3.6	-	V
		V _{CC} = 6.0 V	4.8	3.4	-	4.8	-	V
V _{IL}	LOW-level input	V _{CC} = 2.0 V	-	0.6	0.3	-	0.3	V
	voltage	V _{CC} = 4.5 V	-	1.9	0.9	-	0.9	V
		V _{CC} = 6.0 V	-	2.6	1.2	-	1.2	V

Single unbuffered inverter

Symbol	Parameter	Conditions	-40	-40 °C to +85 °C			-40 °C to +125 °C	
			Min	Тур	Мах	Min	Max	
V _{OH}	HIGH-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$						
	voltage	I_{O} = -20 µA; V_{CC} = 2.0 V	1.8	2.0	-	1.8	-	V
		I_{O} = -20 µA; V_{CC} = 4.5 V	4.0	4.5	-	4.0	-	V
		I_{O} = -20 µA; V_{CC} = 6.0 V	5.5	6.0	-	5.5	-	V
		I_{O} = -2.0 mA; V_{CC} = 4.5 V	4.13	4.32	-	3.7	-	V
		I_{O} = -2.6 mA; V_{CC} = 6.0 V	5.63	5.81	-	5.2	-	V
V _{OL} LO	LOW-level output	$V_{I} = V_{IH} \text{ or } V_{IL}$						
	voltage	I_{O} = 20 µA; V_{CC} = 2.0 V	-	0	0.2	-	0.2	V
		I_{O} = 20 µA; V_{CC} = 4.5 V	-	0	0.5	-	0.5	V
		I_{O} = 20 µA; V_{CC} = 6.0 V	-	0	0.5	-	0.5	V
		I_0 = 2.0 mA; V_{CC} = 4.5 V	-	0.15	0.33	-	0.4	V
		I_{O} = 2.6 mA; V_{CC} = 6.0 V	-	0.16	0.33	-	0.4	V
I _I	input leakage current	V_{I} = V_{CC} or GND; V_{CC} = 6.0 V	-	-	1.0	-	1.0	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 6.0$ V	-	-	10	-	20	μA
CI	input capacitance		-	5	-	-	-	pF

11. Dynamic characteristics

Table 8. Dynamic characteristics

GND = 0 V; $t_r = t_f = 6.0$ ns; For test circuit see Fig. 6. All typical values are measured at $T_{amb} = 25$ °C.

Symbol	Parameter	rameter Conditions		-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Мах	Min	Max	
t _{pd}	propagation delay	A to Y; see <u>Fig. 5</u> [1]						
		V _{CC} = 2.0 V; C _L = 50 pF	-	10	90	-	105	ns
		V _{CC} = 4.5 V; C _L = 50 pF	-	7	18	-	21	ns
		V _{CC} = 6.0 V; C _L = 50 pF	-	6	15	-	18	ns
		V _{CC} = 5.0 V; C _L = 15 pF	-	5	-	-	-	ns
C _{PD}	power dissipation capacitance	$V_{I} = GND$ to V_{CC} [2]	-	14	-	-	-	pF

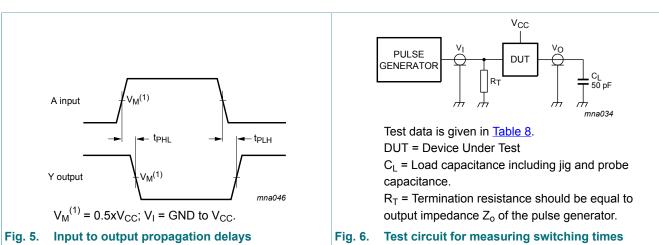
 f_i = input frequency in MHz;

 f_o = output frequency in MHz;

C_L = output load capacitance in pF;

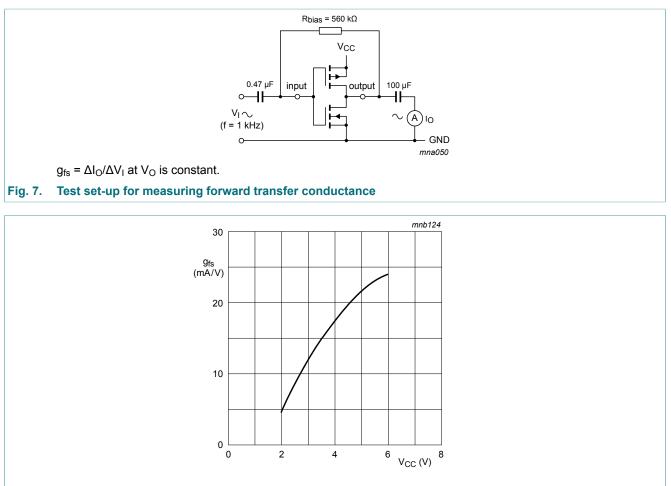
V_{CC} = supply voltage in Volts.

Single unbuffered inverter



11.1. Waveform and test circuit

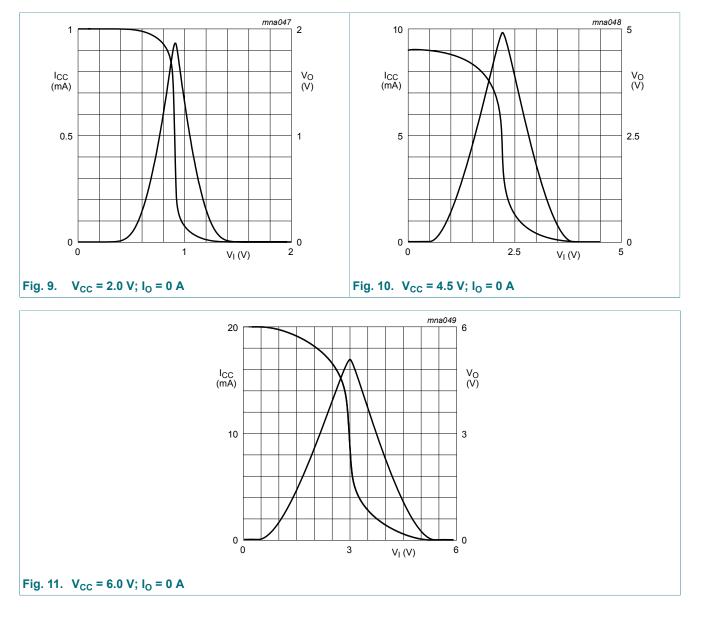




 T_{amb} = 25 °C

Fig. 8. Typical forward transconductance as a function of supply voltage

Single unbuffered inverter



11.3. Typical transfer characteristics

74HC1GU04

mna053

12. Application information

Some applications are:

- Linear amplifier (see Fig. 12)
- In crystal oscillator design (see Fig. 13)

Remark: All values given are typical unless otherwise specified.

R1

C1 = 47 pF (typ.)

C2 = 22 pF (typ.)

R1 = 1 M Ω to 10 M Ω (typ.)

 V_{CC} = 3 V and f = 1 MHz).

Fig. 13. Crystal oscillator configuration

out

R2 optimum value depends on the frequency

average minimum I_{CC} (I_{\text{CC}} is typically 2 mA at

and required stability against changes in $V_{CC} \mbox{ or }$

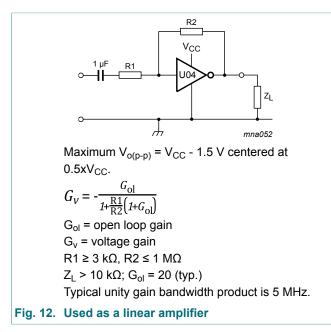


Table 9. External components for resonator (f < 1 MHz)

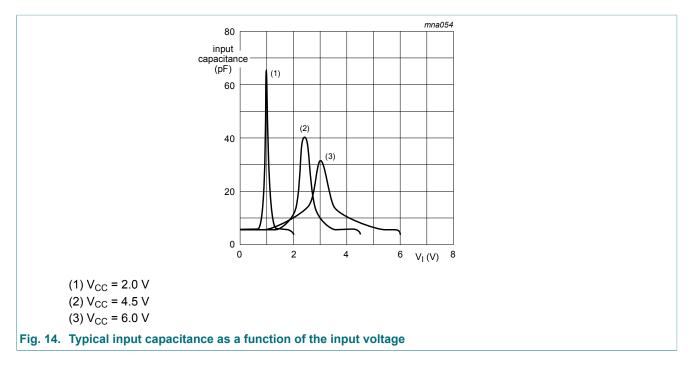
All values given are typical and must be used as an initial set-up

e ,,				
Frequency	R1	R2	C1	C2
10 kHz to 15.9 kHz	2.2 ΜΩ	220 kΩ	56 pF	20 pF
16 kHz to 24.9 kHz	2.2 ΜΩ	220 kΩ	56 pF	10 pF
25 kHz to 54.9 kHz	2.2 ΜΩ	100 kΩ	56 pF	10 pF
55 kHz to 129.9 kHz	2.2 ΜΩ	100 kΩ	47 pF	5 pF
130 kHz to 199.9 kHz	2.2 ΜΩ	47 kΩ	47 pF	5 pF
200 kHz to 349.9 kHz	2.2 ΜΩ	47 kΩ	47 pF	5 pF
350 kHz to 600 kHz	2.2 ΜΩ	47 kΩ	47 pF	5 pF
	2.2 10132	17 1132	ii pi	σpi

Table 10. Optimum value for R2

Frequency	R2	Optimum for
3 kHz	2.0 kΩ	minimum required I _{CC}
	8.0 kΩ	minimum influence due to change in V _{CC}
6 kHz	1.0 kΩ	minimum required I _{CC}
	4.7 kΩ	minimum influence by V_{CC}
10 kHz	0.5 kΩ	minimum required I _{CC}
	2.0 kΩ	minimum influence by V_{CC}
14 kHz	0.5 kΩ	minimum required I _{CC}
	1.0 kΩ	minimum influence by V_{CC}
>14 kHz	-	replace R2 by C3 with a typical value of 35 pF

Single unbuffered inverter



74HC1GU04

Single unbuffered inverter

13. Package outline

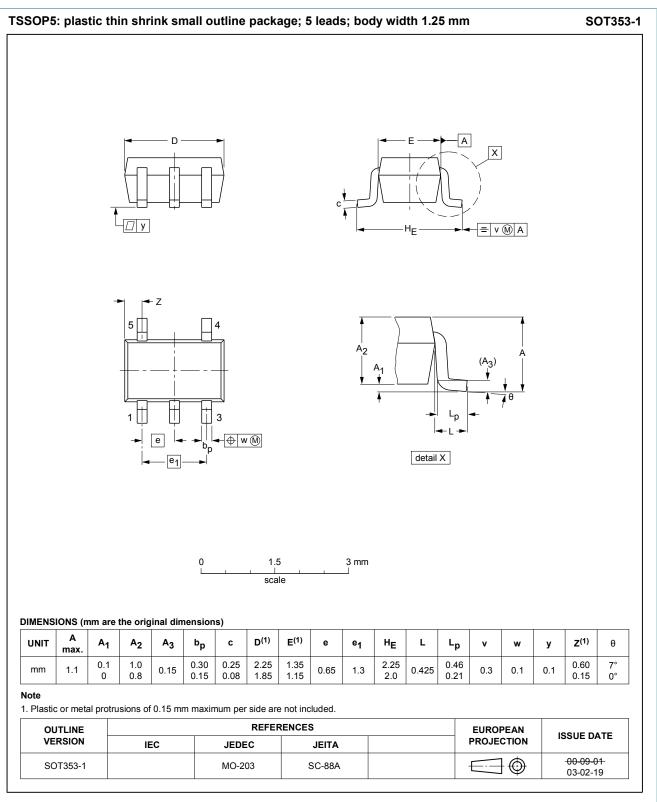
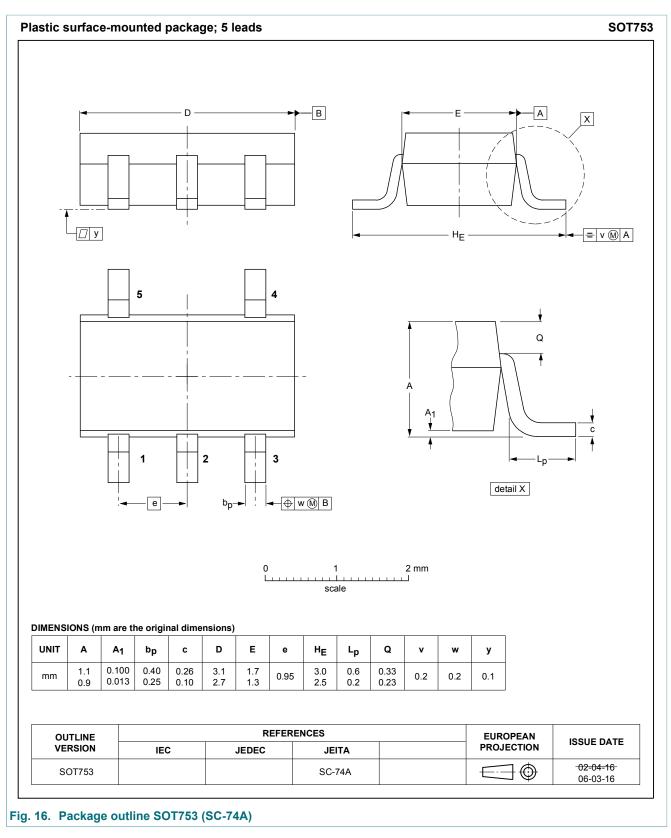


Fig. 15. Package outline SOT353-1 (TSSOP5)

Single unbuffered inverter



⁷⁴HC1GU04

14. Abbreviations

Table 11. Abbreviations			
Acronym	Description		
CMOS	Complementary Metal-Oxide Semiconductor		
DUT	Device Under Test		

15. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74HC1GU04 v.6	20180725	Product data sheet	-	74HC1GU04 v.5
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Fig. 8: forward transconductance graph added. 			
74HC1GU04 v.5	20070710	Product data sheet	-	74HC1GU04 v.4
Modifications:	 The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name where appropriate. Package SOT353 changed to SOT353-1 in <u>Table 1</u> and <u>Fig. 15</u>. Quick Reference Data and Soldering sections removed. <u>Section 2</u> updated. 			
74HC1GU04 v.4	20020527	Product specification	-	74HC1GU04 v.3
74HC1GU04 v.3	20020513	Product specification	-	74HC1GU04 v.2
74HC1GU04 v.2	20010427	Product specification	-	74HC1GU04 v.1
74HC1GU04 v.1	19981118	Product specification		

Single unbuffered inverter

16. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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