74HC245-Q100; 74HCT245-Q100

Octal bus transceiver; 3-state Rev. 2 — 14 July 2020

Product data sheet

1. General description

The 74HC245-Q100; 74HCT245-Q100 is an 8-bit transceiver with 3-state outputs. The device features an output enable (\overline{OE}) and send/receive (DIR) for direction control. A HIGH on \overline{OE} causes the outputs to assume a high-impedance OFF-state. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC}.

This product has been qualified to the Automotive Electronics Council (AEC) standard Q100 (Grade 1) and is suitable for use in automotive applications.

2. Features and benefits

- Automotive product qualification in accordance with AEC-Q100 (Grade 1)
 Specified from -40 °C to +85 °C and from -40 °C to +125 °C
- Complies with JEDEC standard JESD7A
- Input levels:
 - For 74HC245-Q100: CMOS level
 - For 74HCT245-Q100: TTL level
- Octal bidirectional bus interface
- Non-inverting 3-state outputs
- ESD protection:
 - MIL-STD-883, method 3015 exceeds 2000 V
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V (C = 200 pF, R = 0 Ω)
- Multiple package options
- DHVQFN package with Side-Wettable Flanks enabling Automatic Optical Inspection (AOI) of solder joints

3. Ordering information

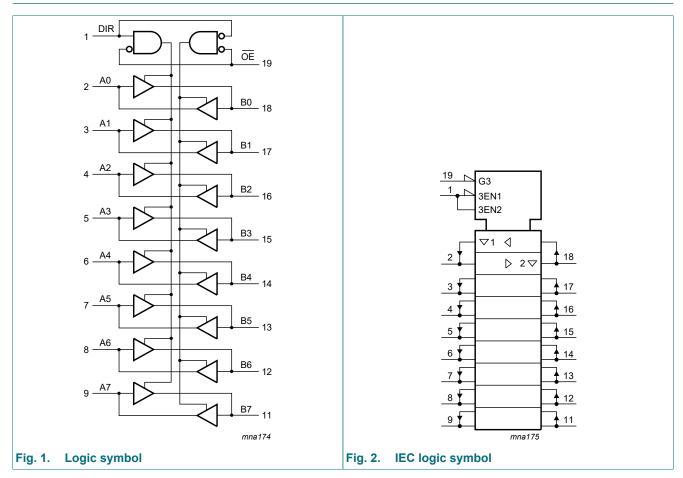
Table 1. Ordering infor	mation			
Type number	Package			
	Temperature range	Name	Description	Version
74HC245D-Q100	-40 °C to +125 °C	SO20	plastic small outline package; 20 leads;	SOT163-1
74HCT245D-Q100			body width 7.5 mm	
74HC245PW-Q100	-40 °C to +125 °C	TSSOP20	plastic thin shrink small outline package;	SOT360-1
74HCT245PW-Q100			20 leads; body width 4.4 mm	
74HC245BQ-Q100	-40 °C to +125 °C	DHVQFN20	plastic dual in-line compatible thermal	SOT764-1
74HCT245BQ-Q100			enhanced very thin quad flat package; no leads; 20 terminals; body 2.5 × 4.5 × 0.85 mm	

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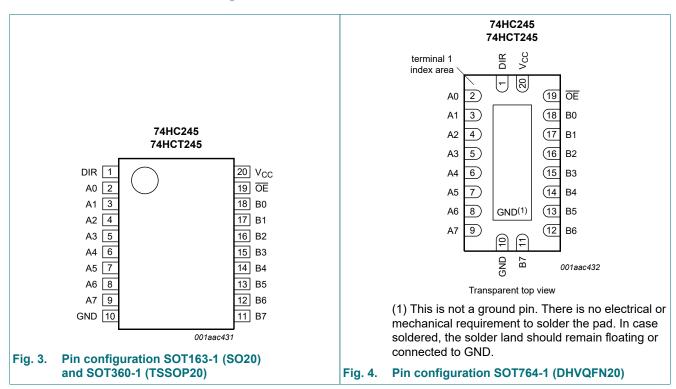
74HC245-Q100; 74HCT245-Q100

Octal bus transceiver; 3-state

4. Functional diagram



5. Pinning information



5.1. Pinning

5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
DIR	1	direction control
A0, A1, A2, A3, A4, A5, A6, A7	2, 3, 4, 5, 6, 7, 8, 9	data input/output
GND	10	ground (0 V)
B7, B6, B5, B4, B3, B2, B1, B0	11, 12, 13, 14, 15, 16, 17, 18	data input/output
OE	19	output enable input (active LOW)
V _{CC}	20	supply voltage

6. Functional description

Table 3. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care; Z = high-impedance OFF-state.

		Input/output			
ŌE DIR A		An	Bn		
L	L	A = B	input		
L	Н	input	B = A		
Н	Х	Z	Z		

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+7	V
I _{IK}	input clamping current	$V_{\rm I}$ < -0.5 V or $V_{\rm I}$ > $V_{\rm CC}$ + 0.5 V	-	±20	mA
I _{OK}	output clamping current	$V_{\rm O}$ < -0.5 V or $V_{\rm O}$ > $V_{\rm CC}$ + 0.5 V	-	±20	mA
lo	output current	-0.5 V < V _O < V _{CC} + 0.5 V	-	±35	mA
I _{CC}	supply current		-	70	mA
I _{GND}	ground current		-70	-	mA
T _{stg}	storage temperature		-65	+150	°C
P _{tot}	total power dissipation		[1] -	500	mW

For SOT163-1 (SO20) package: P_{tot} derates linearly with 12.3 mW/K above 109 °C.
 For SOT360-1 (TSSOP20) package: P_{tot} derates linearly with 10.0 mW/K above 100 °C.
 For SOT764-1 (DHVQFN20) package: P_{tot} derates linearly with 12.9 mW/K above 111 °C.

8. Recommended operating conditions

Symbol Parameter	Parameter	Conditions	74	IC245-Q	100	74H	ICT245-0	2100	Unit
			Min	Тур	Max	Min	Тур	Max	
V _{CC}	supply voltage		2.0	5.0	6.0	4.5	5.0	5.5	V
VI	input voltage		0	-	V _{CC}	0	-	V _{CC}	V
Vo	output voltage		0	-	V _{CC}	0	-	V _{CC}	V
Δt/ΔV	input transition rise and	V _{CC} = 2.0 V	-	-	625	-	-	-	ns/V
	fall rate	V _{CC} = 4.5 V	-	1.67	139	-	1.67	139	ns/V
		V _{CC} = 6.0 V	-	-	83	-	-	-	ns/V
T _{amb}	ambient temperature		-40	+25	+125	-40	+25	+125	°C

Table 5. Recommended operating conditions

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Тур	Max	Min	Мах	Min	Max	-
74HC24	5-Q100	1				I				1
		V _{CC} = 2.0 V	1.5	1.2	-	1.5	-	1.5	-	V
	input voltage	V _{CC} = 4.5 V	3.15	2.4	-	3.15	-	3.15	-	V
		V _{CC} = 6.0 V	4.2	3.2	-	4.2	-	4.2	-	V
V _{IL}	LOW-level	V _{CC} = 2.0 V	-	0.8	0.5	-	0.5	-	0.5	V
	input voltage	V _{CC} = 4.5 V	-	2.1	1.35	-	1.35	-	1.35	V
		V _{CC} = 6.0 V	-	2.8	1.8	-	1.8	-	1.8	V
V _{OH}	HIGH-level	V _I = V _{IH} or V _{IL}								
	output voltage	I _O = -20 μA; V _{CC} = 2.0 V	1.9	2.0	-	1.9	-	1.9	-	V
		I _O = -20 μA; V _{CC} = 4.5 V	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -20 μA; V _{CC} = 6.0 V	5.9	6.0	-	5.9	-	5.9	-	V
		I _O = -6.0 mA; V _{CC} = 4.5 V	3.98	4.32	-	3.84	-	3.7	-	V
		I _O = -7.8 mA; V _{CC} = 6.0 V	5.48	5.81	-	5.34	-	5.2	-	V
V _{OL}	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}$								
	output voltage	I _O = 20 μA; V _{CC} = 2.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 20 μA; V _{CC} = 4.5 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 20 μA; V _{CC} = 6.0 V	-	0	0.1	-	0.1	-	0.1	V
		I _O = 6.0 mA; V _{CC} = 4.5 V	-	0.15	0.26	-	0.33	-	0.4	V
		I _O = 7.8 mA; V _{CC} = 6.0 V	-	0.16	0.26	-	0.33	-	0.4	V
I _I	input leakage current	$V_{I} = V_{CC}$ or GND; $V_{CC} = 6.0 V$	-	-	±0.1	-	±1.0	-	±1.0	μA
I _{OZ}	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 6.0 \text{ V};$ $V_{O} = V_{CC} \text{ or GND}$	-	-	±0.5	-	±5.0	-	±10	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 6.0$ V	-	-	8.0	-	80	-	160	μA
CI	input capacitance		-	3.5	-	-	-	-	-	pF
C _{I/O}	input/output capacitance		-	10	-	-	-	-	-	pF

Symbol	Parameter	Conditions		25 °C		-40 °C t	o +85 °C	-40 °C to +125 °C		Unit
			Min	Тур	Max	Min Max		Min	Max	
74HCT2	45-Q100	1						1		1
V _{IH}	HIGH-level input voltage	V _{CC} = 4.5 V to 5.5 V	2.0	1.6	-	2.0	-	2.0	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 4.5 V to 5.5 V	-	1.2	0.8	-	0.8	-	0.8	V
V _{OH}	HIGH-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = -20 μA	4.4	4.5	-	4.4	-	4.4	-	V
		I _O = -6 mA	3.98	4.32	-	3.84	-	3.7	-	V
V _{OL}	LOW-level	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 4.5 \text{ V}$								
	output voltage	I _O = 20 μA	-	0	0.1	-	0.1	-	0.1	V
		I _O = 6.0 mA	-	0.15	0.26	-	0.33	-	0.4	V
I _I	input leakage current	$V_{I} = V_{CC}$ or GND; $V_{CC} = 5.5 V$	-	-	±0.1	-	±1.0	-	±1.0	μA
I _{OZ}	OFF-state output current	$V_{I} = V_{IH} \text{ or } V_{IL}; V_{CC} = 5.5 \text{ V};$ $V_{O} = V_{CC} \text{ or GND}$	-	-	±0.5	-	±5.0	-	±10	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}; I_O = 0 \text{ A}$	-	-	8.0	-	80	-	160	μA
ΔI _{CC}	additional supply current	$V_I = V_{CC} - 2.1 V$; other inputs at V_{CC} or GND; $V_{CC} = 4.5 V$ to 5.5 V; $I_O = 0 A$								
		An or Bn inputs	-	40	144	-	180	-	196	μA
		OE input	-	150	540	-	675	-	735	μA
		DIR input	-	90	324	-	405	-	441	μA
CI	input capacitance		-	3.5	-	-	-	-	-	pF
C _{I/O}	input/output capacitance		-	10	-	-	-	-	-	pF

10. Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V; for test circuit see Fig. 7.

Symbol	Parameter	Conditions			25 °C		-40 °C to	o +125 °C	Unit
			-	Min	Тур	Тур Мах		Max (125 °C)	
74HC24	5-Q100	,						<u> </u>	
t _{pd}	propagation delay	An to Bn or Bn to An; see <u>Fig. 5</u>	[1]						
		V _{CC} = 2.0 V		-	25	90	115	135	ns
		V _{CC} = 4.5 V		-	9	18	23	27	ns
		V _{CC} = 5.0 V; C _L = 15 pF		-	7	-	-	-	ns
		V _{CC} = 6.0 V		-	7	15	20	23	ns
t _{en}	enable time	OE to An or Bn; see Fig. 6	[2]						
		V _{CC} = 2.0 V		-	30	150	190	225	ns
		V _{CC} = 4.5 V		-	11	30	38	45	ns
		V _{CC} = 6.0 V		-	9	26	33	38	ns
t _{dis}	disable time	OE to An or Bn; see <u>Fig. 6</u>	[3]						
		V _{CC} = 2.0 V		-	41	150	190	225	ns
		V _{CC} = 4.5 V		-	15	30	38	45	ns
		V _{CC} = 6.0 V		-	12	26	33	38	ns
t _t	transition time	see <u>Fig. 5</u>	[4]						
		V _{CC} = 2.0 V		-	14	60	75	90	ns
		V _{CC} = 4.5 V		-	5	12	15	18	ns
		V _{CC} = 6.0 V		-	4	10	13	15	ns
C _{PD}	power dissipation capacitance	per transceiver; V_1 = GND to V_{CC}	[5]	-	30	-	-	-	pF
74HCT2	45-Q100							<u> </u>	
t _{pd}	propagation delay	An to Bn or Bn to An; see <u>Fig. 5</u>	[1]						
		V _{CC} = 4.5 V		-	12	22	28	33	ns
		V _{CC} = 5.0 V; C _L = 15 pF		-	10	-	-	-	ns
t _{en}	enable time	OE to An or Bn; see Fig. 6	[2]	-	16	30	38	45	ns
t _{dis}	disable time	OE to An or Bn; see Fig. 6	[3]	-	16	30	38	45	ns
t _t	transition time	V _{CC} = 4.5 V; see <u>Fig. 5</u>	[4]	-	5	12	15	18	ns
C _{PD}	power dissipation capacitance	per transceiver; V _I = GND to V _{CC} - 1.5 V	[5]	-	30	-	-	-	pF

 t_{pd} is the same as t_{PHL} and $t_{PLH}.$ t_{en} is the same as t_{PZH} and $t_{PZL}.$ [1]

[2]

 t_{dis} is the same as t_{PHZ} and t_{PLZ} . [3]

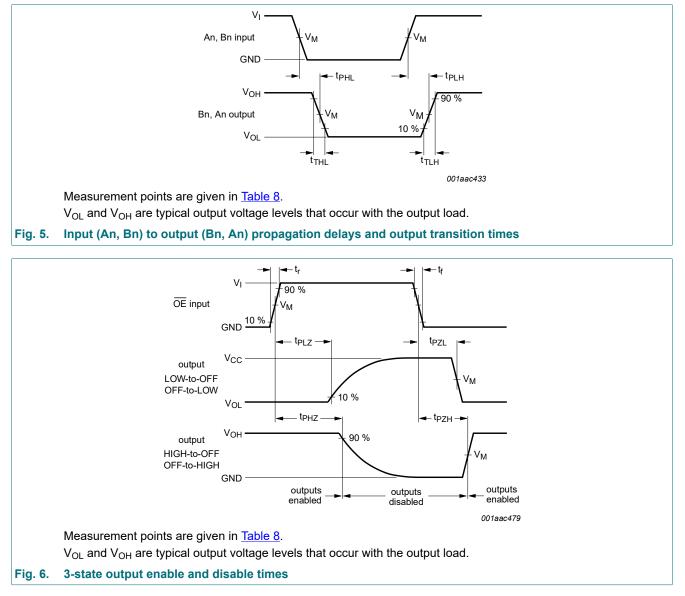
[4] t_t is the same as t_{THL} and t_{TLH} . [5] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W): $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum (C_L \times V_{CC}^2 \times f_o)$ where: f_i = input frequency in MHz; f_o = output frequency in MHz;

 C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 Σ (C_L x V_{CC}² x f_o) = sum of outputs.



10.1. Waveforms and test circuit

Table 8. Measurement points

Туре	Input	Output
	V _M	V _M
74HC245-Q100	0.5V _{CC}	0.5V _{CC}
74HCT245-Q100	1.3 V	1.3 V

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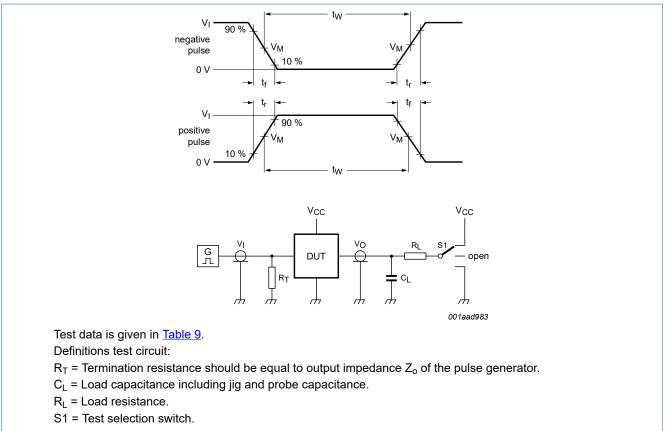


Fig. 7. Test circuit for measuring switching times

Table 9. Test data

Туре	Input		Load		S1 position		
	VI	t _r , t _f	CL	RL	t _{PHL} , t _{PLH}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
74HC245-Q100	V _{CC}	6 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}
74HCT245-Q100	3 V	6 ns	15 pF, 50 pF	1 kΩ	open	GND	V _{CC}

11. Package outline

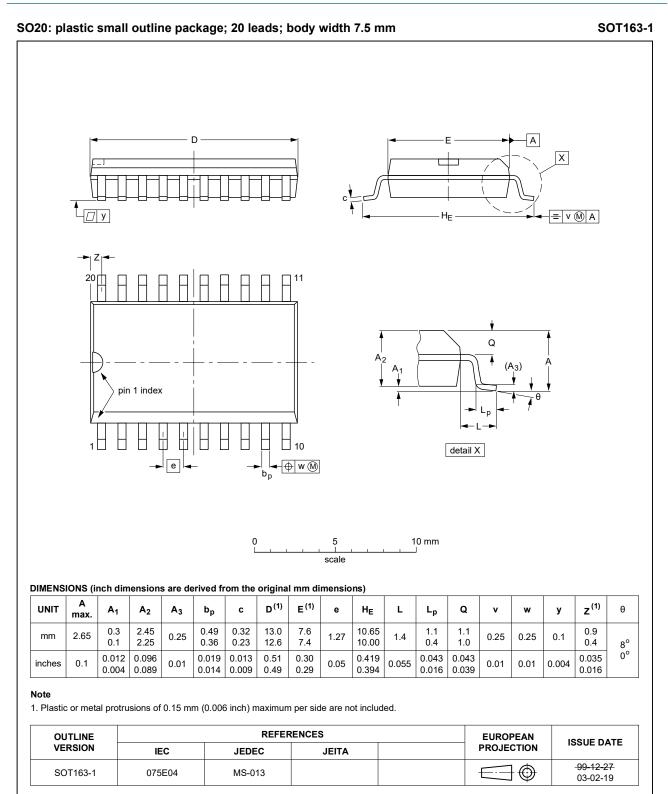


Fig. 8. Package outline SOT163-1 (SO20)

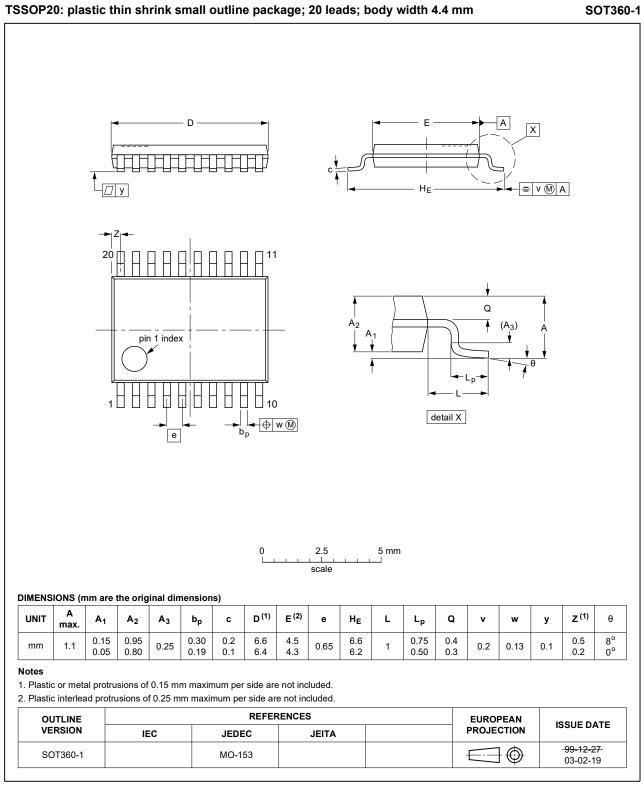


Fig. 9. Package outline SOT360-1 (TSSOP20)

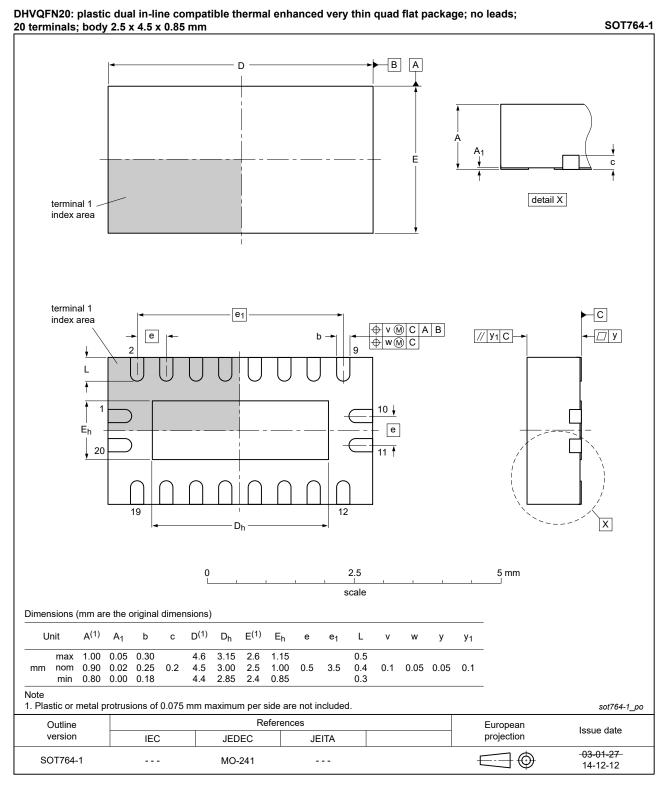


Fig. 10. Package outline SOT764-1 (DHVQFN20)

12. Abbreviations

Acronym	Description
CMOS	Complementary Metal Oxide Semiconductor
DUT	Device Under Test
ESD	ElectroStatic Discharge
HBM	Human Body Model
MIL	Military
MM	Machine Model
TTL	Transistor-Transistor Logic

13. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74HC_HCT245_Q100 v.2	20200714	Product data sheet	-	74HC_HCT245_Q100 v.1
Modifications:	guidelines o Legal texts I <u>Section 2</u> up <u>Table 4</u> : Der	have been adapted to the r	new company nan ower dissipation h	ne where appropriate. nave been updated.
74HC_HCT245_Q100 v.1	20130722	Product data sheet	-	-

14. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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