74LV123

Dual retriggerable monostable multivibrator with reset Rev. 9 — 13 September 2021 Product data sheet

1. General description

The 74LV123 is a dual retriggerable monostable multivibrator with reset. The basic output pulse width is programmed by selection of external components (R_{EXT} and C_{EXT}). Once triggered this basic pulse width may be extended by retriggering either of the edge triggered inputs ($n\overline{A}$ or (nB). By repeating this process, the output pulse period (nQ = HIGH, $n\overline{Q} = LOW$) can be made as long as desired. Alternatively, an output delay can be terminated at any time by a LOW-going edge on input $n\overline{R}D$. Control inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess V_{CC} . Schmitt-trigger action at $n\overline{A}$ and nB inputs makes the circuit tolerant of slower input rise and fall times.

2. Features and benefits

- Wide supply voltage range from 1.0 V to 5.5 V
- · CMOS low power dissipation
- Latch-up performance exceeds 100 mA per JESD 78 Class II Level B
- Optimized for low-voltage applications: 1.0 V to 3.6 V
- Accepts TTL input levels between V_{CC} = 2.7 V and V_{CC} = 3.6 V
- Typical output ground bounce: < 0.8 V at V_{CC} = 3.3 V and T_{amb} = 25 °C
- Typical HIGH-level output voltage (V_{OH}) undershoot: > 2 V at V_{CC} = 3.3 V and T_{amb} = 25 °C
- · DC triggered from active HIGH or active LOW inputs
- Retriggerable for very long pulses up to 100 % duty factor
- Direct reset terminates output pulses
- Schmitt-trigger action on all inputs except for the reset input
- Complies with JEDEC standards:
 - JESD8-7 (1.65 V to 1.95 V)
 - JESD8-5 (2.3 V to 2.7 V)
 - JESD8C (2.7 V to 3.6 V)
 - JESD36 (4.5 V to 5.5 V)
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
- Multiple package options
- Specified from -40 °C to +85 °C and from -40 °C to +125 °C

3. Ordering information

Table 1. Ordering information

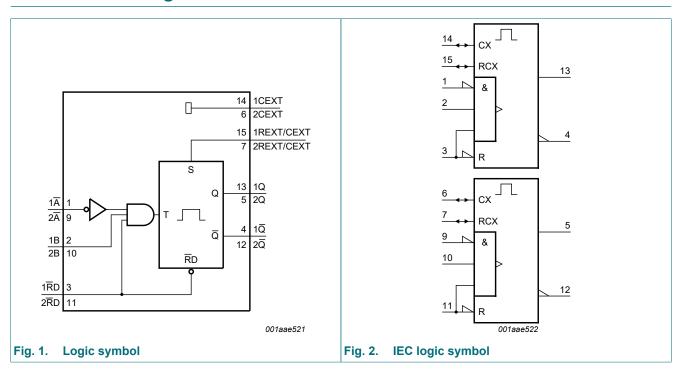
Type number	Package	ackage					
	Temperature range	Name	Description	Version			
74LV123D	-40 °C to +125 °C	SO16	plastic small outline package; 16 leads; body width 3.9 mm	SOT109-1			
74LV123PW	-40 °C to +125 °C	TSSOP16	plastic thin shrink small outline package; 16 leads; body width 4.4 mm	SOT403-1			

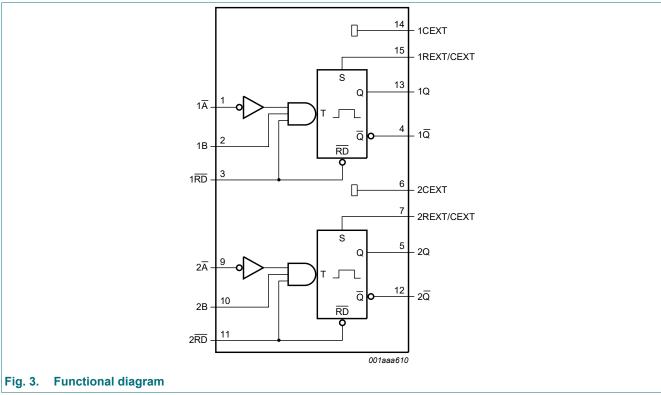


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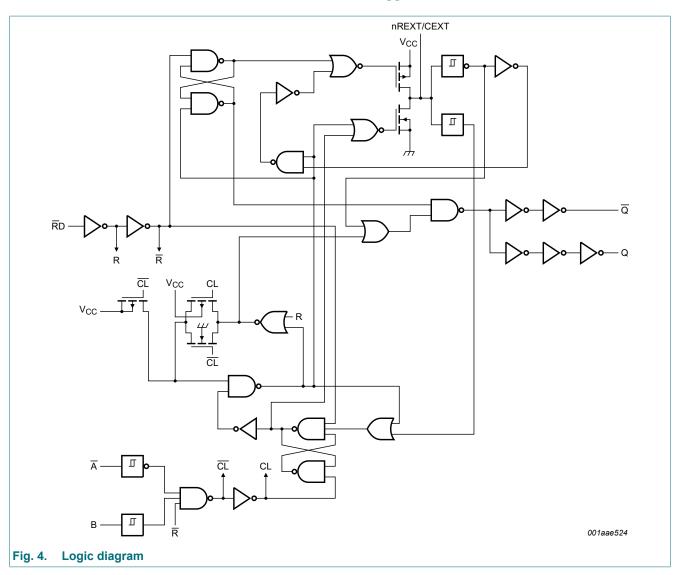
Type number	Package					
	Temperature range	Name	Description	Version		
74LV123BQ	-40 °C to +125 °C		plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 16 terminals; body 2.5 × 3.5 × 0.85 mm	SOT763-1		

4. Functional diagram





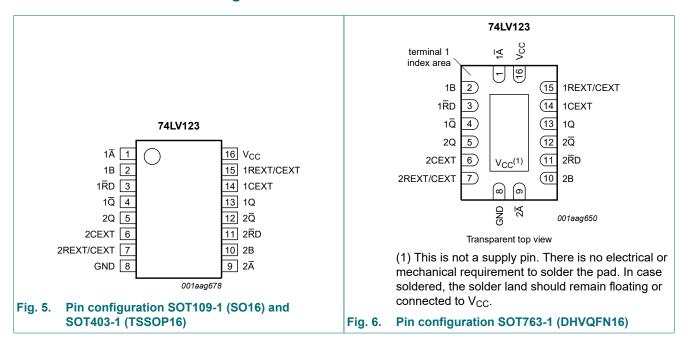
Dual retriggerable monostable multivibrator with reset



Dual retriggerable monostable multivibrator with reset

5. Pinning information

5.1. Pinning



5.2. Pin description

Table 2. Pin description

Symbol	Pin	Description
1 A	1	negative-edge triggered input 1
1B	2	positive-edge triggered input 1
1RD	3	direct reset LOW and positive-edge triggered input 1
1Q	4	active LOW output 1
2Q	5	active HIGH output 2
2CEXT	6	external capacitor connection 2
2REXT/CEXT	7	external resistor and capacitor connection 2
GND	8	ground (0 V)
2Ā	9	negative-edge triggered input 2
2B	10	positive-edge triggered input 2
2RD	11	direct reset LOW and positive-edge triggered input 2
2Q	12	active LOW output 2
1Q	13	active HIGH output 1
1CEXT	14	external capacitor connection 1
1REXT/CEXT	15	external resistor and capacitor connection 1
V _{CC}	16	supply voltage

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6. Functional description

Table 3. Function table

 $H = HIGH \text{ voltage level; } L = LOW \text{ voltage level; } X = don't \text{ care; } \uparrow = LOW-to-HIGH \text{ transition; } \downarrow = HIGH-to-LOW \text{ transition; }$

 Π = one HIGH level output pulse; Π = one LOW level output pulse.

	Input	Out	put	
nRD	nĀ	nB	nQ	nQ
L	Х	Х	L	Н
X	Н	Х	L [1]	H [1]
X	X	L	L [1]	H [1]
Н	L	1	Л	П
Н	↓	Н	Л	П
1	L	Н	Л	П

^[1] If the monostable multivibrator was triggered before this condition was established, the pulse will continue as programmed.

7. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CC}	supply voltage			-0.5	+7	V
I _{IK}	input clamping current	$V_{I} < -0.5 \text{ V or } V_{I} > V_{CC} + 0.5 \text{ V}$	[1]	-	±20	mA
I _{OK}	output clamping current	V_{O} < -0.5 V or V_{O} > V_{CC} + 0.5 V	[1]	-	±50	mA
Io	output current	except for pins nREXT/CEXT; $V_0 = -0.5 \text{ V to } (V_{CC} + 0.5 \text{ V})$	[1]	-	±25	mA
I _{CC}	supply current			-	+50	mA
I _{GND}	ground current			-50	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	[2]	-	500	mW

^[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

8. Recommended operating conditions

Table 5. Recommended operating conditions

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CC}	supply voltage	[1]	1.0	3.3	5.5	V
V_{I}	input voltage		0	-	V_{CC}	V
Vo	output voltage		0	-	V _{CC}	V
T _{amb}	ambient temperature	in free air	-40	+25	+125	°C

^[2] For SOT109-1 (SO16) package: P_{tot} derates linearly with 12.4 mW/K above 110 °C. For SOT403-1 (TSSOP16) package: P_{tot} derates linearly with 8.5 mW/K above 91 °C. For SOT763-1 (DHVQFN16) package: P_{tot} derates linearly with 11.2 mW/K above 106 °C.

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Δt/ΔV	input transition rise and fall rate	$V_{CC} = 1.0 \text{ V to } 2.0 \text{ V}$ [2]	-	-	500	ns/V
		$V_{CC} = 2.0 \text{ V to } 2.7 \text{ V}$ [2]	-	-	200	ns/V
		$V_{CC} = 2.7 \text{ V to } 3.6 \text{ V}$ [2]	-	-	100	ns/V
		V _{CC} = 3.6 V to 5.5 V [2]	-	-	50	ns/V

^[1] The 74LV123 is guaranteed to function down to $V_{CC} = 1.0 \text{ V}$ (input levels GND or V_{CC}); The "Static characteristics" Section 9 are guaranteed from V_{CC} = 1.2 V to V_{CC} = 5.5 V. Except for Schmitt-trigger inputs n \overline{A} and n \overline{B} .

9. Static characteristics

Table 6. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ[1]	Max	Unit
T _{amb} = -4	40 °C to +85 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 1.2 V	0.9	-	-	V
		V _{CC} = 2.0 V	1.4	-	-	V
		V _{CC} = 2.7 V to 3.6 V	2.0	-	-	V
		V _{CC} = 4.5 V to 5.5 V	0.7V _{CC}	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 1.2 V	-	-	0.3	V
		V _{CC} = 2.0 V	-	-	0.6	V
		V _{CC} = 2.7 V to 3.6 V	-	-	0.8	V
		V _{CC} = 4.5 V to 5.5 V	-	-	0.3V _{CC}	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = -100 μA; V _{CC} = 1.2 V	-	1.2	-	V
		I _O = -100 μA; V _{CC} = 2.0 V	1.8	2.0	-	V
		I _O = -100 μA; V _{CC} = 2.7 V	2.5	2.7	-	V
		I _O = -100 μA; V _{CC} = 3.0 V	2.8	3.0	-	V
		I _O = -100 μA; V _{CC} = 4.5 V	4.3	4.5	-	V
		I _O = -6 mA; V _{CC} = 3.0 V	2.40	2.82	-	V
		I _O = -12 mA; V _{CC} = 4.5 V	3.60	4.20	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = 100 μA; V _{CC} = 1.2 V	-	0	-	V
		I _O = 100 μA; V _{CC} = 2.0 V	-	0	0.2	V
		I _O = 100 μA; V _{CC} = 2.7 V	-	0	0.2	V
		I _O = 100 μA; V _{CC} = 3.0 V	-	0	0.2	V
		I _O = 100 μA; V _{CC} = 4.5 V	-	0	0.2	V
		I _O = 6 mA; V _{CC} = 3.0 V	-	0.25	0.40	V
		I _O = 12 mA; V _{CC} = 4.5 V	-	0.35	0.55	V
I _I	input leakage current	V _I = V _{CC} or GND; V _{CC} = 5.5 V	-	-	1.0	μΑ
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	20.0	μΑ
ΔI _{CC}	additional supply current	V _I = V _{CC} - 0.6 V; V _{CC} = 2.7 V to 3.6 V	-	-	500	μΑ
Cı	input capacitance		-	3.5	-	pF

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Symbol	Parameter	Conditions	Min	Typ[1]	Max	Unit
T _{amb} = -	40 °C to +125 °C					
V _{IH}	HIGH-level input voltage	V _{CC} = 1.2 V	0.9	-	-	V
		V _{CC} = 2.0 V	1.4	-	-	V
		V _{CC} = 2.7 V to 3.6 V	2.0	-	-	V
		V _{CC} = 4.5 V to 5.5 V	0.7V _{CC}	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 1.2 V	-	-	0.3	V
		V _{CC} = 2.0 V	-	-	0.6	V
		V _{CC} = 2.7 V to 3.6 V	-	-	0.8	V
		V _{CC} = 4.5 V to 5.5 V	-	-	0.3V _{CC}	V
V _{OH}	HIGH-level output voltage	$V_I = V_{IH}$ or V_{IL}				
		I _O = -100 μA; V _{CC} = 1.2 V	-	-	-	V
		I _O = -100 μA; V _{CC} = 2.0 V	1.8	-	-	V
		I _O = -100 μA; V _{CC} = 2.7 V	2.5	-	-	V
		I _O = -100 μA; V _{CC} = 3.0 V	2.8	-	-	V
		I _O = -100 μA; V _{CC} = 4.5 V	4.3	-	-	V
		I _O = -6 mA; V _{CC} = 3.0 V	2.2	-	-	V
		I _O = -12 mA; V _{CC} = 4.5 V	3.5	-	-	V
V _{OL}	LOW-level output voltage	V _I = V _{IH} or V _{IL}				
		I _O = 100 μA; V _{CC} = 1.2 V	-	-	-	V
		I _O = 100 μA; V _{CC} = 2.0 V	-	-	0.2	V
		I _O = 100 μA; V _{CC} = 2.7 V	-	-	0.2	V
		I _O = 100 μA; V _{CC} = 3.0 V	-	-	0.2	V
		I _O = 100 μA; V _{CC} = 4.5 V	-	-	0.2	V
		I _O = 6 mA; V _{CC} = 3.0 V	-	-	0.5	V
		I _O = 12 mA; V _{CC} = 4.5 V	-	-	0.65	V
l _l	input leakage current	$V_I = V_{CC}$ or GND; $V_{CC} = 5.5 \text{ V}$	-	-	1.0	μA
I _{CC}	supply current	$V_I = V_{CC}$ or GND; $I_O = 0$ A; $V_{CC} = 5.5$ V	-	-	160	μA
ΔI _{CC}	additional supply current	$V_I = V_{CC} - 0.6 \text{ V}; V_{CC} = 2.7 \text{ V to } 3.6 \text{ V}$	-	-	850	μA

^[1] All typical values are measured at T_{amb} = 25 °C.

Dual retriggerable monostable multivibrator with reset

10. Dynamic characteristics

Table 7. Dynamic characteristics

GND = 0 V; $t_r = t_f \le 2.5$ ns; for test circuit see Fig. 8.

Symbol	Parameter	Conditions	-40	°C to +85	°C	-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	1
Propaga	ation delay; see	Fig. 7						
t _{pd}	propagation	nRD, nĀ and nB to nQ	[2]					
	delay	V _{CC} = 1.2 V	-	120	-	-	-	ns
		V _{CC} = 2.0 V	-	40	76	-	92	ns
		V _{CC} = 2.7 V	-	30	56	-	68	ns
		V _{CC} = 3.0 V to 3.6 V	-	25	48	-	57	ns
		V _{CC} = 4.5 V to 5.5 V	-	18	40	-	46	ns
		nRD to nQ (reset)	[2]					
		V _{CC} = 1.2 V	-	100	-	-	-	ns
		V _{CC} = 2.0 V	-	30	57	-	68	ns
		V _{CC} = 2.7 V	-	23	43	-	51	ns
		V _{CC} = 3.0 V to 3.6 V	-	20	38	-	45	ns
		V _{CC} = 4.5 V to 5.5 V	-	14	31	-	36	ns
Inputs n	A, nB and nRD	; see <u>Fig. 7</u>	'	'				
t _W	pulse width	nĀ = LOW						
		V _{CC} = 2.0 V	30	5	-	40	-	ns
		V _{CC} = 2.7 V	25	3.5	-	30	-	ns
		V _{CC} = 3.0 V to 3.6 V	20	3.0	-	25	-	ns
		V _{CC} = 4.5 V to 5.5 V	15	2.5	-	20	-	ns
		nB = HIGH						
		V _{CC} = 2.0 V	30	13	-	40	-	ns
		V _{CC} = 2.7 V	25	8	-	30	-	ns
		V _{CC} = 3.0 V to 3.6 V	20	7	-	25	-	ns
		V _{CC} = 4.5 V to 5.5 V	15	5	-	20	-	ns
		nRD = LOW; see Fig. 13						
		V _{CC} = 2.0 V	35	6	-	45	-	ns
		V _{CC} = 2.7 V	30	5	-	40	-	ns
		V _{CC} = 3.0 V to 3.6 V	25	4	-	30	-	ns
		V _{CC} = 4.5 V to 5.5 V	20	3	-	25	-	ns
t _{rtrig}	retrigger time	nB to nĀ; see Fig. 12						
-		V _{CC} = 2.0 V	-	70	-	-	-	ns
		V _{CC} = 2.7 V	-	55	-	-	-	ns
		V _{CC} = 3.0 V to 3.6 V	-	45	-	-	-	ns
		V _{CC} = 4.5 V to 5.5 V	-	40	-	-	-	ns

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Symbol	Parameter	Conditions	-40	°C to +85	°C	-40 °C to +125 °C		Unit
			Min	Typ[1]	Max	Min	Max	
Outputs	; nQ = LOW and n	Q = HIGH, see <u>Fig. 7</u>						<u>'</u>
t _W	pulse width	C_{EXT} = 100 nF; R_{EXT} = 10 k Ω						
		V _{CC} = 2.0 V	-	470	-	-	-	ns
		V _{CC} = 2.7 V	-	460	-	-	-	ns
		V _{CC} = 3.0 V to 3.6 V	-	450	-	-	-	ns
		V _{CC} = 4.5 V to 5.5 V	-	430	-	-	-	ns
		$C_{EXT} = 0 \text{ pF}; R_{EXT} = 5 \text{ k}\Omega$						
		V _{CC} = 2.0 V	-	100	-	-	-	ns
		V _{CC} = 2.7 V	-	90	-	-	-	ns
		V _{CC} = 3.0 V to 3.6 V	-	80	-	-	-	ns
		V _{CC} = 4.5 V to 5.5 V	-	70	-	-	-	ns
Externa	components					-		'
R _{EXT}	external resistance	see <u>Fig. 11</u> [3]						
		V _{CC} = 1.2 V	10	-	1000	-	-	kΩ
		V _{CC} = 2.0 V	5	-	1000	-	-	kΩ
		V _{CC} = 2.7 V	3	-	1000	-	-	kΩ
		V _{CC} = 3.0 V to 3.6 V	2	-	1000	-	-	kΩ
		V _{CC} = 4.5 V to 5.5 V	2	-	1000	-	-	kΩ
C _{EXT}	external	see <u>Fig. 11</u> [3] [4]						
	capacitance	V _{CC} = 1.2 V	-	-	-	-	-	pF
		V _{CC} = 2.0 V	-	-	-	-	-	pF
		V _{CC} = 2.7 V	-	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	-	-	-	-	pF
		V _{CC} = 4.5 V to 5.5 V	-	-	-	-	-	pF
Dynami	c power dissipatio	n						
C _{PD}	power dissipation capacitance	$V_{CC} = 3.3 \text{ V}; V_{I} = \text{GND to } V_{CC}$ [5]	-	60	-	-	-	pF

- [1] All typical values are measured at T_{amb} = 25 °C and nominal supply values (V_{CC} = 3.3 V and 5.0 V).
- [2] t_{pd} is the same as t_{PLH} and t_{PHL} ; $C_{EXT} = 0$ pF; $R_{EXT} = 5$ k Ω .
- [3] For other R_{EXT} and C_{EXT} combinations see Fig. 11 and Section 11.1.1.
- [4] C_{EXT} has no limits.
- [5] C_{PD} is used to determine the dynamic power dissipation (P_D in μ W).

 $P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma (C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

 $\Sigma(C_L \times V_{CC}^2 \times f_0)$ = sum of the outputs.

Dual retriggerable monostable multivibrator with reset

10.1. Waveforms and test circuit

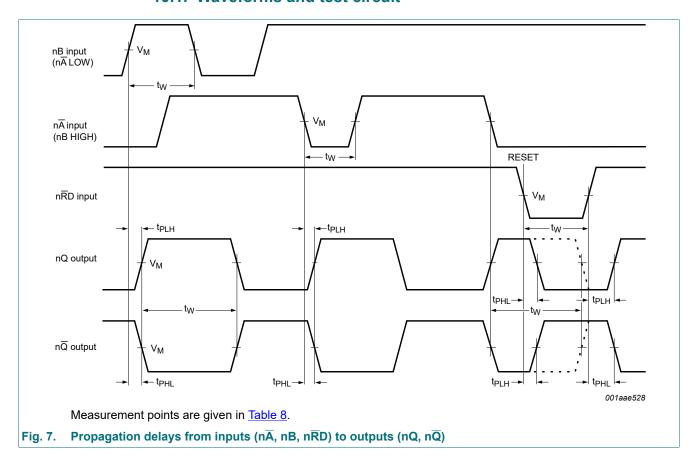
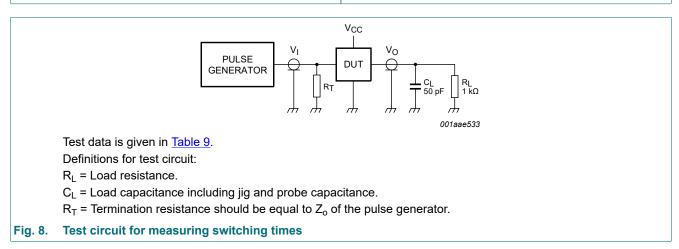


Table 8. Measurement points

V _{CC}	V_{M}
≥ 2.7 V	1.5 V
< 2.7 V	0.5 × V _{CC}



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Table 9. Test data

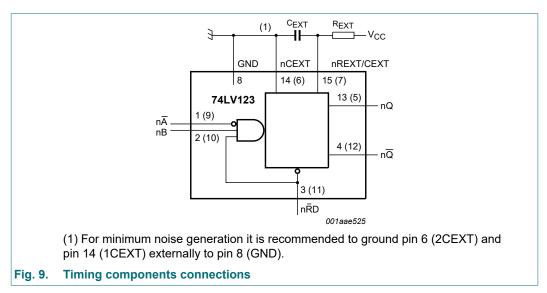
Supply voltage	Input		Load	Load		
V _{CC}	Vı	t _r , t _f	CL	R _L		
< 2.7 V	V _{CC}	≤ 2.5 ns	50 pF	1 kΩ	t _{PHL} , t _{PLH}	
2.7 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	1 kΩ	t _{PHL} , t _{PLH}	
≥ 4.5 V	V _{CC}	≤ 2.5 ns	50 pF	1 kΩ	t _{PHL} , t _{PLH}	

11. Application information

11.1. Timing components

11.1.1. Basic timing

The basic output pulse width is essentially determined by the values of the external timing components R_{EXT} and C_{EXT} .

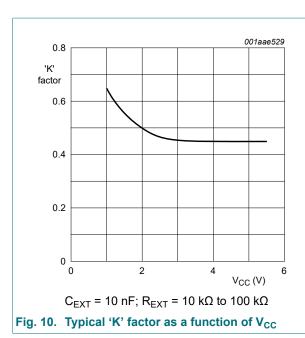


If $C_{EXT} > 10$ nF, the following formula is valid: $t_W = K \times R_{EXT} \times C_{EXT}$ (typical) where:

- t_W = output pulse width in ns
- R_{EXT} = external resistor in kΩ
- C_{EXT} = external capacitor in pF
- K = constant: this is 0.45 for V_{CC} = 5.0 V and 0.48 for V_{CC} = 2.0 V (see Fig. 10)

The inherent test jig and pin capacitance at pin 15 and pin 7 (nREXT/CEXT) is approximately 7 pF.

Dual retriggerable monostable multivibrator with reset



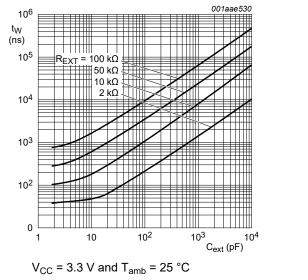


Fig. 11. Typical output pulse width as a function of the external capacitance values

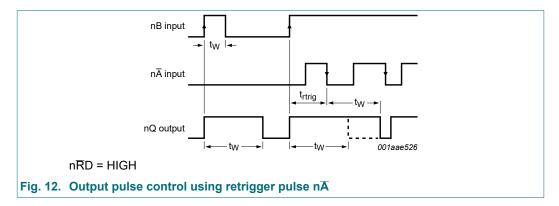
11.1.2. Retrigger timing

The time to retrigger the monostable multivibrator depends on the values of R_{EXT} and C_{EXT} . The output pulse width will only be extended when the time between the active going edges of the trigger pulses meets the minimum retrigger time. If $C_{EXT} > 10$ pF, the next formula for the set-up time of a retrigger pulse is valid:

at
$$V_{CC}$$
 = 5.0 V: t_{rtrig} = 30 + 0.19 R_{EXT} x $C_{EXT}^{0.9}$ + 13 x $R_{EXT}^{1.05}$ (typical) at V_{CC} = 3.0 V: t_{rtrig} = 41 + 0.15 R_{EXT} x $C_{EXT}^{0.9}$ x 1 x R_{EXT} (typical)

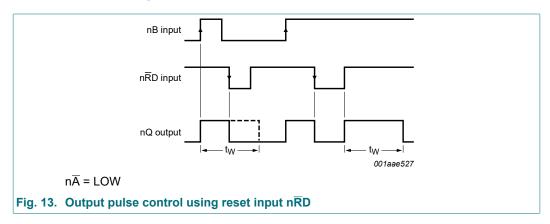
where:

- t_{rtrig} = retrigger time in ns
- C_{EXT} = external capacitor in pF
- R_{EXT} = external resistor in kΩ



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11.1.3. Reset timing



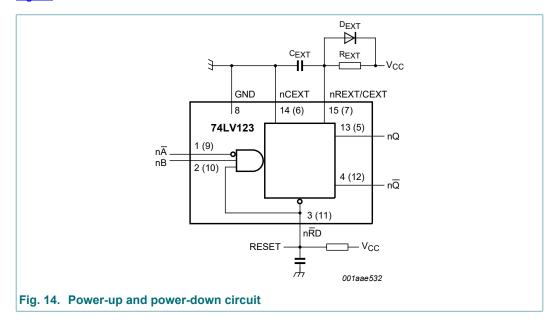
11.2. Power considerations

11.2.1. Power-up

When the monostable multivibrator is powered-up, it may produce an output pulse with a pulse width defined by the values of R_{EXT} and C_{EXT} . This output pulse can be eliminated using the RC circuit on pin nRD shown in Fig. 14.

11.2.2. Power-down

A large capacitor (C_{EXT}) may cause problems when powering-down the monostable due to the energy stored in this capacitor. When a system containing this device is powered-down or a rapid decrease of V_{CC} to zero occurs, the monostable may sustain damage, due to the capacitor discharging through the input protection diodes. To avoid this possibility, connect a damping diode D_{EXT} (preferably a germanium or Schottky type diode) able to withstand large current surges. See Fig. 14.

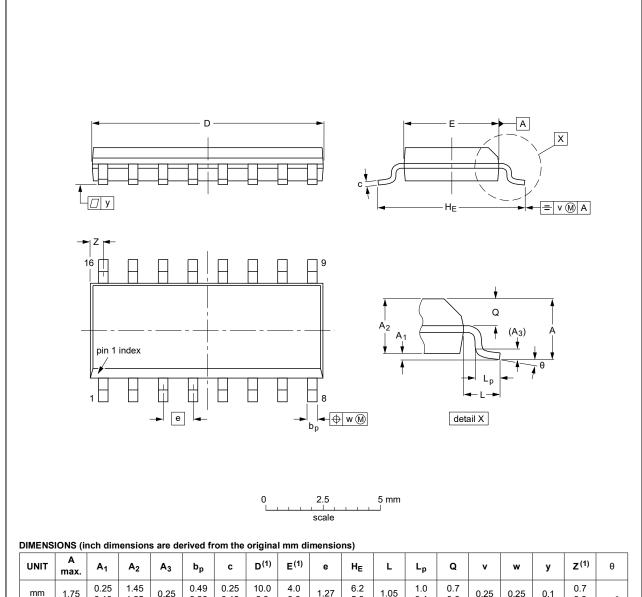


Dual retriggerable monostable multivibrator with reset

12. Package outline

SO16: plastic small outline package; 16 leads; body width 3.9 mm

SOT109-1



UNIT	A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E ⁽¹⁾	е	HE	L	Lp	Q	v	w	у	Z ⁽¹⁾	θ
mm	1.75	0.25 0.10	1.45 1.25	0.25	0.49 0.36	0.25 0.19	10.0 9.8	4.0 3.8	1.27	6.2 5.8	1.05	1.0 0.4	0.7 0.6	0.25	0.25	0.1	0.7 0.3	8°
inches	0.069	0.010 0.004	0.057 0.049	0.01		0.0100 0.0075	0.39 0.38	0.16 0.15	0.05	0.244 0.228	0.041	0.039 0.016	0.028 0.020	0.01	0.01	0.004	0.028 0.012	0°

1. Plastic or metal protrusions of 0.15 mm (0.006 inch) maximum per side are not included.

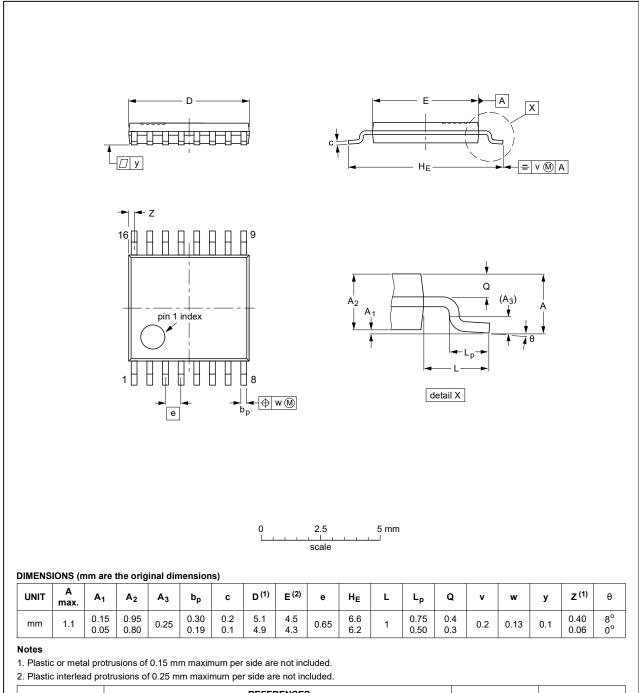
OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT109-1	076E07	MS-012				99-12-27 03-02-19	

Fig. 15. Package outline SOT109-1 (SO16)

Dual retriggerable monostable multivibrator with reset

TSSOP16: plastic thin shrink small outline package; 16 leads; body width 4.4 mm

SOT403-1



OUTLINE		REFER	EUROPEAN	ISSUE DATE			
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE	
SOT403-1		MO-153				99-12-27 03-02-18	

Fig. 16. Package outline SOT403-1 (TSSOP16)

Dual retriggerable monostable multivibrator with reset

DHVQFN16: plastic dual in-line compatible thermal enhanced very thin quad flat package; no leads; 16 terminals; body 2.5 x 3.5 x 0.85 mm SOT763-1

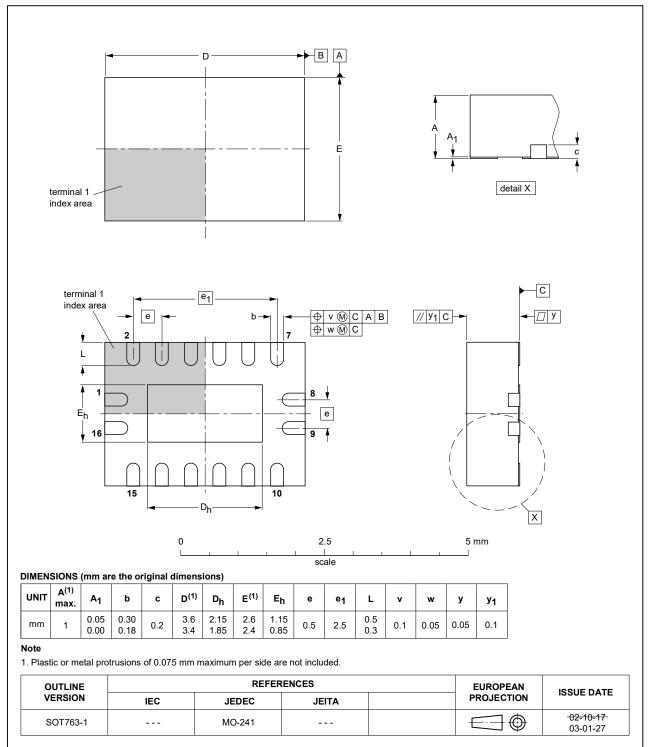


Fig. 17. Package outline SOT763-1 (DHVQFN16)

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Dual retriggerable monostable multivibrator with reset

13. Abbreviations

Table 10. Abbreviations

Acronym	Description			
CMOS	Complementary Metal Oxide Semiconductor			
DUT	Device Under Test			

14. Revision history

Table 11. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes					
74LV123 v.9	20210913	Product data sheet	-	74LV123 v.8					
Modifications:	Nexperia. Legal texts h Type numbe Section 1 an	this data sheet has been redesigned to comply with the identity guidelines of ave been adapted to the new company name where appropriate. 74LV123DB (SOT338-1/SSOP16) removed. 8 Section 2 updated. 9 updated. 9 updated to the new company name where appropriate.							
74LV123 v.8	20160304	Product data sheet	-	74LV123 v.7					
Modifications:	Type numbe	Type numbers 74LV123N (SOT38-4) removed.							
74LV123 v.7	20111212	Product data sheet	-	74LV123 v.6					
Modifications:	Legal pages	Legal pages updated.							
74LV123 v.6	20110826	Product data sheet	-	74LV123 v.5					
74LV123 v.5	20071108	Product data sheet	-	74LV123 v.4					
74LV123 v.4	20070919	Product specification	-	74LV123 v.3					
74LV123 v.3	20030313	Product specification	-	74LV123 v.2					
74LV123 v.2	19980420	Product specification	-	74LV123 v.1					
74LV123 v.1	19970204	Product specification	-	-					

Dual retriggerable monostable multivibrator with reset

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
- The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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