Single retriggerable monostable multivibrator; Schmitt trigger inputs

Rev. 6 — 2 November 2018

Product data sheet

1. General description

The 74LVC1G123 is a single retriggerable monostable multivibrator with Schmitt trigger inputs. Output pulse width is controlled by three methods:

- 1. The basic pulse is programmed by selection of an external resistor (R_{EXT}) and capacitor (C_{EXT}).
- 2. Once triggered, the basic output pulse width may be extended by retriggering the gated active LOW-going edge input (Ā) or the active HIGH-going edge input (B). By repeating this process, the output pulse period (Q = HIGH) can be made as long as desired. Alternatively an output delay can be terminated at any time by a LOW-going edge on input CLR, which also inhibits the triggering.
- **3.** An internal connection from CLR to the input gates makes it possible to trigger the circuit by a HIGH-going signal at input CLR.

Inputs can be driven from either 3.3 V or 5 V devices. This feature allows the use of these devices as translators in a mixed 3.3 V and 5 V environment. Schmitt trigger inputs, makes the circuit highly tolerant to slower input rise and fall times.

This device is fully specified for partial power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing the damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 1.65 V to 5.5 V
- High noise immunity
- ± 24 mA output drive (V_{CC} = 3.0 V)
- CMOS low power consumption
- DC triggered from active HIGH or active LOW inputs
- · Retriggerable for very long pulses up to 100 % duty factor
- Direct reset terminates output pulse
- · Schmitt trigger on all inputs
 - Complies with JEDEC standard:
 - JESD8-7 (1.65 V to 1.95 V)
 - JESD8-5 (2.3 V to 2.7 V)
 - JESD8-B/JESD36 (2.7 V to 3.6 V)
- Power-on-reset on outputs
- Latch-up performance exceeds 100 mA
- Direct interface with TTL levels
- Inputs accept voltages up to 5.5 V
- ESD protection:
 - HBM JESD22-A114F exceeds 2000 V
 - MM JESD22-A115-A exceeds 200 V
 - CDM JESD22-C101E exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

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3. Ordering information

Table	1.	Ordering	information
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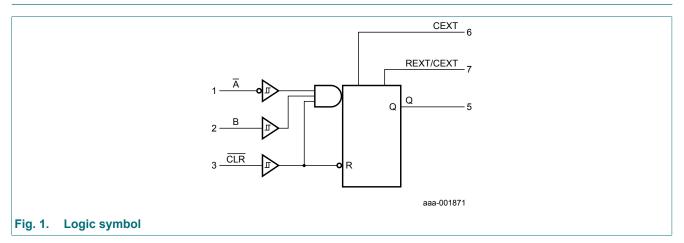
Type number	Package	Package						
	Temperature range	Name	Description	Version				
74LVC1G123DP	-40 °C to +125 °C	TSSOP8	plastic thin shrink small outline package; 8 leads; body width 3 mm; lead length 0.5 mm	SOT505-2				
74LVC1G123DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	SOT765-1				
74LVC1G123GT	-40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 x 1.95 x 0.5 mm	SOT833-1				
74LVC1G123GF	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 x 1 x 0.5 mm	SOT1089				
74LVC1G123GN	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.2 x 1.0 x 0.35 mm	SOT1116				
74LVC1G123GS	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 x 1.0 x 0.35 mm	SOT1203				

4. Marking

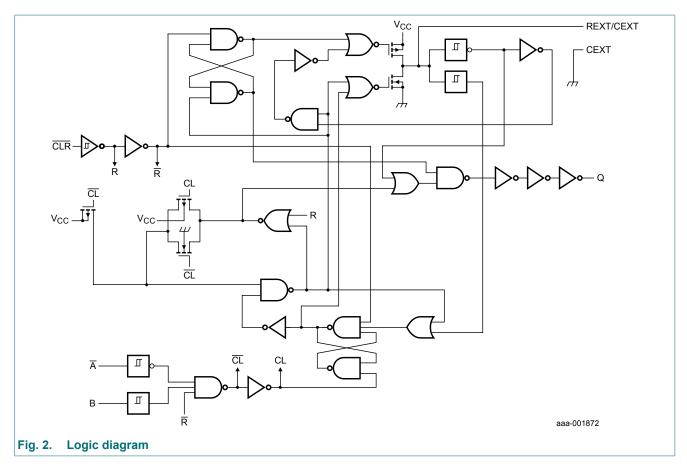
Table 2. Marking codes	
Type number	Marking code[1]
74LVC1G123DP	Y3
74LVC1G123DC	Y3
74LVC1G123GT	Y3
74LVC1G123GF	Y3
74LVC1G123GN	Y3
74LVC1G123GS	Y3

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram

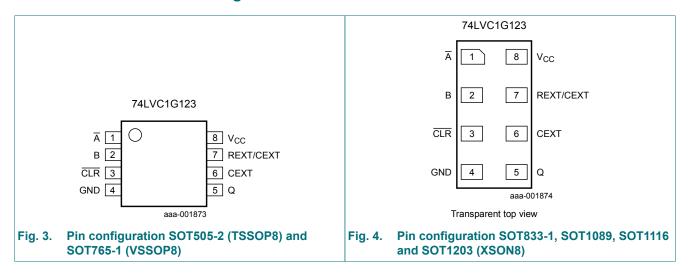


Single retriggerable monostable multivibrator; Schmitt trigger inputs



6. Pinning information

6.1. Pinning



74LVC1G123

6.2. Pin description

Table 3. Pin descripti	on	
Symbol	Pin	Description
Ā	1	negative-edge triggered input
В	2	positive-edge triggered input
CLR	3	direct reset LOW and positive-edge triggered input
GND	4	ground (0 V)
Q	5	active HIGH output
CEXT	6	external capacitor connection
REXT/CEXT	7	external resistor and capacitor connection
V _{CC}	8	supply voltage

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level; X = don't care; $\uparrow = LOW$ -to-HIGH transition; $\downarrow = HIGH$ -to-LOW transition; $\prod = one HIGH$ level output pulse.

Input			Output
CLR	Ā	В	Q
L	Х	Х	L
Х	Н	Х	L[1]
Х	Х	L	L[1]
Н	L	1	Л
Н	Ļ	Н	Л
1	L	Н	Л

[1] If the monostable was triggered before this condition was established, the pulse continues as programmed.

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		-0.5	+6.5	V
VI	input voltage	[-0.5	+6.5	V
Vo	output voltage	Active mode	-0.5	V _{CC} + 0.5	V
		Power-down mode [1]	-0.5	+6.5	V
I _{IK}	input clamping current	V ₁ < 0 V	-50	-	mA
I _{ОК}	output clamping current	$V_{\rm O}$ < 0 V or $V_{\rm O}$ > $V_{\rm CC}$	-	±50	mA
lo	output current	$V_{O} = 0 V \text{ to } V_{CC}$	-	±50	mA
I _{CC}	supply current		-	100	mA
I _{GND}	ground current		-100	-	mA
T _{stg}	storage temperature		-65	+150	°C

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Symbol	Parameter	Conditions	Min	Max	Unit
P _{tot}	total power dissipation	$T_{amb} = -40 \ ^{\circ}C \ to \ +125 \ ^{\circ}C$ [3]	-	300	mW

[1] The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] When $V_{CC} = 0 V$ (Power-down mode), the output voltage can be 5.5 V in normal operation.

[3] For TSSOP8 package: above 55 °C the value of P_{tot} derates linearly with 2.5 mW/K. For VSSOP8 package: above 110 °C the value of P_{tot} derates linearly with 8 mW/K. For XSON8 packages: above 118 °C the value of P_{tot} derates linearly with 7.8 mW/K.

9. Recommended operating conditions

Table 6. Operating conditions

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CC}	supply voltage		1.65	5.5	V
VI	input voltage		0	5.5	V
V _O	output voltage	Active mode	0	V _{CC}	V
		Power-down mode	0	5.5	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V_{CC} = 1.65 V to 5.5 V	-	1	ms/V

10. Static characteristics

Table 7. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Typ[1]	Max	Unit
T _{amb} = -4	0 °C to +85 °C	1	I	II.		
V _{OH}	HIGH-level	$V_{I} = V_{T+} \text{ or } V_{T-}$				
	output voltage	I_{O} = -100 µA; V_{CC} = 1.65 V to 5.5 V	V _{CC} - 0.1	-	-	V
		I _O = -4 mA; V _{CC} = 1.65 V	1.2	-	-	V
		I _O = -8 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I _O = -12 mA; V _{CC} = 2.7 V	2.2	-	-	V
		I _O = -24 mA; V _{CC} = 3.0 V	2.4	-	-	V
		I _O = -32 mA; V _{CC} = 4.5 V	3.8	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{T+} \text{ or } V_{T-}$				
		I _O = 100 μA; V _{CC} = 1.65 V to 5.5 V	-	-	0.1	V
		I _O = 4 mA; V _{CC} = 1.65 V	-	-	0.45	V
		I _O = 8 mA; V _{CC} = 2.3 V	-	-	0.3	V
		I _O = 12 mA; V _{CC} = 2.7 V	-	-	0.4	V
		I _O = 24 mA; V _{CC} = 3.0 V	-	-	0.55	V
		I _O = 32 mA; V _{CC} = 4.5 V	-	-	0.55	V
l _l	input leakage current	V_{I} = 5.5 V or GND; V_{CC} = 0 V to 5.5 V	-	-	±2	μA
I _{OFF}	power-off leakage current	$V_1 \text{ or } V_0 = 5.5 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±2	μA

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Symbol	Parameter	Conditions	Min	Typ[1]	Мах	Unit
I _{CC}	supply current	V _I = 5.5 V or GND;				
		Quiescent; V_{CC} = 1.65 V to 5.5 V; I_O = 0 A	-	0.1	10	μA
		Active state; $R_{EXT}/C_{EXT} = 0.5V_{CC}$				
		V _{CC} = 1.65 V	-	-	80	μA
		V _{CC} = 2.3 V	-	-	130	μA
		V _{CC} = 3 V	-	-	240	μA
		V _{CC} = 4.5 V	-	-	400	μA
		V _{CC} = 5.5 V	-	-	650	μA
CI	input capacitance		-	2.0	-	pF
T _{amb} = -4	40 °C to +125 °C	1	I.			
V _{OH}	HIGH-level	$V_{I} = V_{T+}$ or V_{T-}				
	output voltage	I_{O} = -100 µA; V_{CC} = 1.65 V to 5.5 V	V _{CC} - 0.1	-	-	V
		I _O = -4 mA; V _{CC} = 1.65 V	1.2	-	-	V
		I _O = -8 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I _O = -12 mA; V _{CC} = 2.7 V	2.2	-	-	V
		I _O = -24 mA; V _{CC} = 3.0 V	2.4	-	-	V
		I _O = -32 mA; V _{CC} = 4.5 V	3.8	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{T+} \text{ or } V_{T-}$				
		I_{O} = 100 µA; V_{CC} = 1.65 V to 5.5 V	-	-	0.1	V
		I _O = 4 mA; V _{CC} = 1.65 V	-	-	0.45	V
		I _O = 8 mA; V _{CC} = 2.3 V	-	-	0.3	V
		I _O = 12 mA; V _{CC} = 2.7 V	-	-	0.4	V
		I _O = 24 mA; V _{CC} = 3.0 V	-	-	0.55	V
		I _O = 32 mA; V _{CC} = 4.5 V	-	-	0.55	V
l _l	input leakage current	V_1 = 5.5 V or GND; V_{CC} = 0 V to 5.5 V	-	-	±10	μA
I _{OFF}	power-off leakage current	$V_1 \text{ or } V_0 = 5.5 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±10	μA
I _{CC}	supply current	V _I = 5.5 V or GND;				
		Quiescent; V_{CC} = 1.65 V to 5.5 V; I_O = 0 A	-	-	20	μA
		Active state; R _{EXT} /C _{EXT} = 0.5V _{CC}				
		V _{CC} = 1.65 V	-	-	80	μA
		V _{CC} = 2.3 V	-	-	130	μA
		V _{CC} = 3 V	-	-	240	μA
		V _{CC} = 4.5 V	-	-	400	μA
		V _{CC} = 5.5 V	-	-	650	μA

[1] All typical values are measured at T_{amb} = 25 °C.

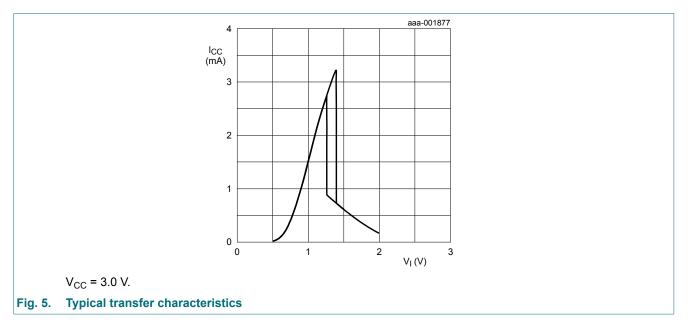
Table 8. Transfer characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 17.

Symbol	Parameter	Conditions	-40	°C to +8	5 °C	-40 °C to	o +125 °C	Unit
			Min	Typ[1]	Max	Min	Max	
V _{T+}	positive-going	Ā, B and CLR input; see Fig. 5						
	threshold voltage	V _{CC} = 1.65 V to 1.95 V	0.72	0.98	1.22	0.71	1.22	V
		V _{CC} = 2.3 V to 2.7 V	0.97	1.26	1.52	0.97	1.52	V
		V _{CC} = 3.0 V to 3.6 V	1.20	1.58	1.90	1.20	1.90	V
		V _{CC} = 4.5 V to 5.5 V	1.74	2.27	2.75	1.74	2.78	V
V _{T-}	negative-going threshold voltage	A, B and CLR input; see Fig. 5						
		V _{CC} = 1.65 V to 1.95 V	0.56	0.81	1.04	0.56	1.04	V
		V _{CC} = 2.3 V to 2.7 V	0.83	1.09	1.33	0.82	1.33	V
		V _{CC} = 3.0 V to 3.6 V	1.08	1.40	1.70	1.08	1.72	V
		V _{CC} = 4.5 V to 5.5 V	1.61	2.07	2.53	1.61	2.57	V
V _H	hysteresis voltage	Ā, B and CLR input; (V _{T+} - V _{T-}); see <u>Fig. 5</u>						
		V _{CC} = 1.65 V to 1.95 V	61	170	295	54	295	mV
		V _{CC} = 2.3 V to 2.7 V	41	174	304	41	304	mV
		V _{CC} = 3.0 V to 3.6 V	40	183	319	40	319	mV
		V _{CC} = 4.5 V to 5.5 V	32	199	363	26	363	mV

[1] Typical values are measured at T_{amb} = 25 °C and V_{CC} = 1.8 V, 2.5 V, 3.3 V and 5.0 V respectively.

10.1. Waveform transfer characteristics



11. Dynamic characteristics

Table 9. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 17.

Symbol	Parameter	Conditions	-40	-40 °C to +85 °C			-40 °C to +125 °C	
				Typ[1]	Max	Min	Max	1
t _{pd}	propagation	\overline{A} , B to Q; see Fig. 6 [2]					
	delay	C _L = 15 pF;						
		V _{CC} = 1.65 V to 1.95 V	2.5	7.1	16.3	2.5	17.6	ns
		V _{CC} = 2.3 V to 2.7 V	1.9	-	10.3	1.9	11.2	ns
		V _{CC} = 2.7 V	1.9	-	8.5	1.9	9.3	ns
		V _{CC} = 3.0 V to 3.6 V	1.5	-	7.6	1.5	8.3	ns
		V _{CC} = 4.5 V to 5.5 V	1.2	-	5.3	1.2	5.8	ns
		$C_{L} = 30 \text{ pF or } C_{L} = 50 \text{ pF}$						
		V _{CC} = 1.65 V to 1.95 V	2.9	7.8	17.6	2.9	19.0	ns
		V _{CC} = 2.3 V to 2.7 V	2.2	-	11.3	2.2	12.3	ns
		V _{CC} = 2.7 V	2.7	-	10.5	2.7	11.4	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	-	9.5	2.0	10.3	ns
		V _{CC} = 4.5 V to 5.5 V	1.5	-	6.7	1.5	7.2	ns
		CLR to Q; see Fig. 6						
		C _L = 15 pF;						
		V _{CC} = 1.65 V to 1.95 V	3.0	6.9	16.2	3.0	17.4	ns
		V _{CC} = 2.3 V to 2.7 V	2.2	-	9.6	2.2	10.5	ns
		V _{CC} = 2.7 V	2.2	-	8.2	2.2	8.9	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	-	7.3	2.0	8.0	ns
		V _{CC} = 4.5 V to 5.5 V	1.5	-	5.1	1.5	5.5	ns
		$C_{L} = 30 \text{ pF or } C_{L} = 50 \text{ pF}$						
		V _{CC} = 1.65 V to 1.95 V	3.3	7.5	17.2	3.8	18.6	ns
		V _{CC} = 2.3 V to 2.7 V	2.5	-	10.3	2.0	11.2	ns
		V _{CC} = 2.7 V	2.8	-	9.3	2.8	10.2	ns
		V _{CC} = 3.0 V to 3.6 V	1.5	-	8.4	1.5	9.2	ns
		V _{CC} = 4.5 V to 5.5 V	1.5	-	6.0	1.5	6.6	ns
t _{pd}	propagation	CLR to Q (trigger); see Fig. 6]					
	delay	C _L = 15 pF;						
		V _{CC} = 1.65 V to 1.95 V	2.7	7.6	17.4	2.7	18.9	ns
		V _{CC} = 2.3 V to 2.7 V	2.1	-	11.0	2.1	12.0	ns
		V _{CC} = 2.7 V	2.1	-	9.2	2.1	10.0	ns
		V _{CC} = 3.0 V to 3.6 V	1.7	-	8.2	1.7	8.9	ns
		V _{CC} = 4.5 V to 5.5 V	1.4	-	5.9	1.4	6.4	ns
		$C_{L} = 30 \text{ pF or } C_{L} = 50 \text{ pF}$						
		V _{CC} = 1.65 V to 1.95 V	3.1	8.3	18.8	3.3	20.3	ns
		V _{CC} = 2.3 V to 2.7 V	2.5	-	12.0	2.5	13.1	ns
		V _{CC} = 2.7 V	2.8	-	11.1	2.8	12.1	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	-	10.1	2.0	11.0	ns
		V _{CC} = 4.5 V to 5.5 V	1.5	-	7.1	1.5	7.7	ns

Symbol	Parameter	Conditions	-40 °C to +85 °C			-40 °C to +125 °C		Unit	
				Typ[1]	Мах	Min	Max		
t _w	pulse width	input Ā LOW; B HIGH; see Fig. 6 and Fig. 7							
		V _{CC} = 1.65 V to 1.95 V	8.0	-	-	8.0	-	ns	
		V _{CC} = 2.3 V to 2.7 V	4.0	-	-	4.0	-	ns	
		V _{CC} = 2.7 V	3.0	-	-	3.0	-	ns	
		V _{CC} = 3.0 V to 3.6 V	3.0	-	-	3.0	-	ns	
		V _{CC} = 4.5 V to 5.5 V	2.5	-	-	2.5	-	ns	
		input CLR LOW; see Fig. 6 and Fig. 8							
		V _{CC} = 1.65 V to 1.95 V	8.0	-	-	8.0	-	ns	
		V _{CC} = 2.3 V to 2.7 V	4.0	-	-	4.0	-	ns	
		V _{CC} = 2.7 V	3.0	-	-	3.0	-	ns	
		V _{CC} = 3.0 V to 3.6 V	3.0	-	-	3.0	-	ns	
		V _{CC} = 4.5 V to 5.5 V	2.5	-	-	2.5	-	ns	
t _W	pulse width	output Q HIGH; see Fig. 6, Fig. 7 and Fig. 8; [3] R_{EXT} = 10 k Ω							
		C _{EXT} = 100 pF							
		V _{CC} = 1.65 V to 1.95 V	-	1.4	2.2	-	2.2	μs	
		V _{CC} = 2.3 V to 2.7 V	-	1.3	1.8	-	1.8	μs	
		V _{CC} = 2.7 V	-	1.2	1.8	-	1.8	μs	
		V _{CC} = 3.0 V to 3.6 V	-	1.2	1.8	-	1.8	μs	
		V _{CC} = 4.5 V to 5.5 V	-	1.2	1.8	-	1.8	μs	
		C _{EXT} = 0.01 μF [3]							
		V _{CC} = 1.65 V to 1.95 V	-	100	110	-	110	μs	
		V _{CC} = 2.3 V to 2.7 V	-	100	110	-	110	μs	
		V _{CC} = 2.7 V	-	100	110	-	110	μs	
		V _{CC} = 3.0 V to 3.6 V	-	100	110	-	110	μs	
		V _{CC} = 4.5 V to 5.5 V	-	100	110	-	110	μs	
		C _{EXT} = 0.1 μF [3]							
		V _{CC} = 1.65 V to 1.95 V	-	1.0	1.05	-	1.05	ms	
		V _{CC} = 2.7 V	-	1.0	1.05	-	1.05	ms	
		V _{CC} = 3.0 V to 3.6 V	-	1.0	1.05	-	1.05	ms	
		V _{CC} = 3.0 V to 3.6 V	-	1.0	1.05	-	1.05	ms	
		V _{CC} = 4.5 V to 5.5 V	-	1.0	1.05	-	1.05	ms	

Symbol	Parameter	Conditions	-40	-40 °C to +85 °C			-40 °C to +125 °C	
				Typ[1]	Мах	Min	Max	
t _{rtrig}	retrigger time	Ā, B; see <u>Fig. 7</u>						
		C _{EXT} = 100 pF; R _{EXT} = 5 kΩ						
		V _{CC} = 1.65 V to 1.95 V	-	174	-	-	-	ns
		V _{CC} = 2.3 V to 2.7 V	-	59	-	-	-	ns
		C _{EXT} = 100 pF; R _{EXT} = 1 kΩ						
		V _{CC} = 3.0 V to 3.6 V	-	32	-	-	-	ns
		V _{CC} = 4.5 V to 5.5 V	-	20	-	-	-	ns
		C _{EXT} = 100 μF; R _{EXT} = 5 kΩ						
		V _{CC} = 1.65 V to 1.95 V	-	14	-	-	-	ms
		V _{CC} = 2.3 V to 2.7 V	-	10	-	-	-	ms
		C _{EXT} = 100 μF; R _{EXT} = 1 kΩ						
		V _{CC} = 3.0 V to 3.6 V	-	10	-	-	-	ms
		V _{CC} = 4.5 V to 5.5 V	-	8	-	-	-	ms
R _{ext}	external resistance	see Fig. 11, Fig. 12 and Fig. 13						
		V _{CC} = 2.0 V	5	-	-	-	-	kΩ
		V _{CC} ≥ 3.0 V	1	-	-	-	-	kΩ
C _{ext}	external capacitance	V _{CC} = 5.0 V; see <u>Fig. 11</u> , <u>Fig. 12</u> and <u>Fig. 13</u>	-	-	-	-	-	pF
C _{PD}	power dissipation capacitance	$V_I = GND$ to V_{CC} ; $C_{EXT} = 0 \text{ pF}$;						
		R _{EXT} = 5 kΩ						
		V _{CC} = 1.8 V	-	35	-	-	-	pF
		V _{CC} = 2.5 V	-	35	-	-	-	pF
		R _{EXT} = 1 kΩ						
		V _{CC} = 3.3 V	-	27	-	-	-	pF
		V _{CC} = 5.0 V	-	29	-	-	-	pF

Single retriggerable monostable multivibrator; Schmitt trigger inputs

Typical values are measured at T_{amb} = 25 °C and V_{CC} = 1.8 V, 2.5 V, 3.3 V and 5.0 V respectively. [1]

[2]

 t_{pd} is the same as t_{PHL} and t_{PLH} For other R_{EXT} and C_{EXT} combinations see Fig. 11, Fig. 12 and Fig. 13. If $C_{EXT} > 10$ nF, the next formula is valid. [3]

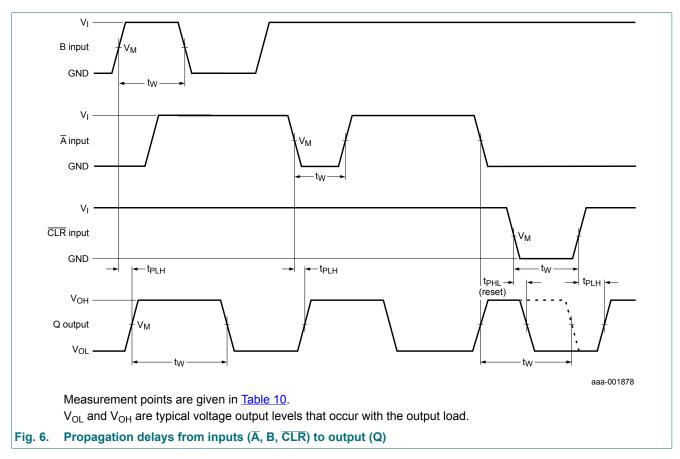
 t_W = K x R_{EXT} x C_{EXT}, where:

t_W = typical output pulse width in ns;

 R_{EXT} = external resistor in k Ω ;

 C_{EXT} = external capacitor in pF;

K = constant = 1; see Fig. 14 for typical "K" factor as function of V_{CC} .

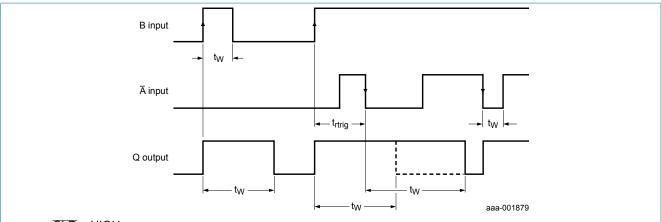


11.1. Waveforms, graphs and test circuit

Table 10. Measurement points

Supply voltage	Input	Output	
V _{cc}	V _M	V _M	
1.65 V to 1.95 V	0.5V _{CC}	0.5V _{CC}	
2.3 V to 2.7 V	0.5V _{CC}	0.5V _{CC}	
2.7 V	1.5 V	1.5 V	
3.0 V to 3.6 V	1.5 V	1.5 V	
4.5 V to 5.5 V	0.5V _{CC}	0.5V _{CC}	

Single retriggerable monostable multivibrator; Schmitt trigger inputs



 $\overline{\text{CLR}}$ = HIGH



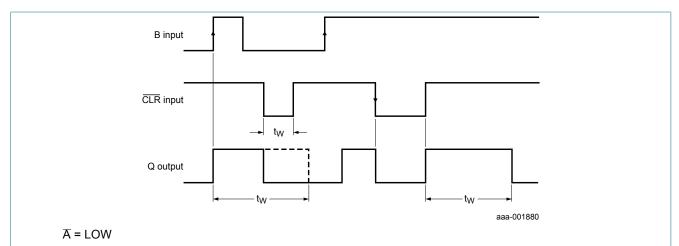
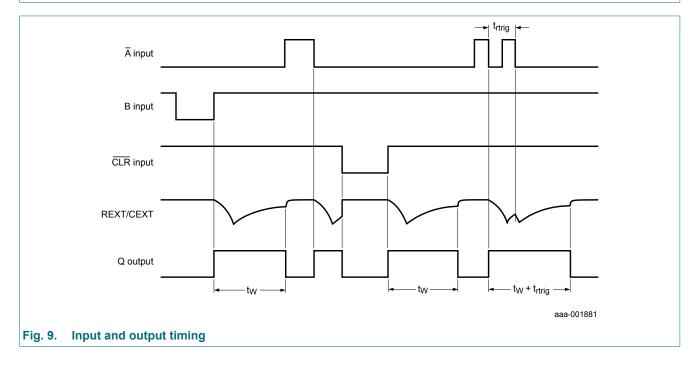
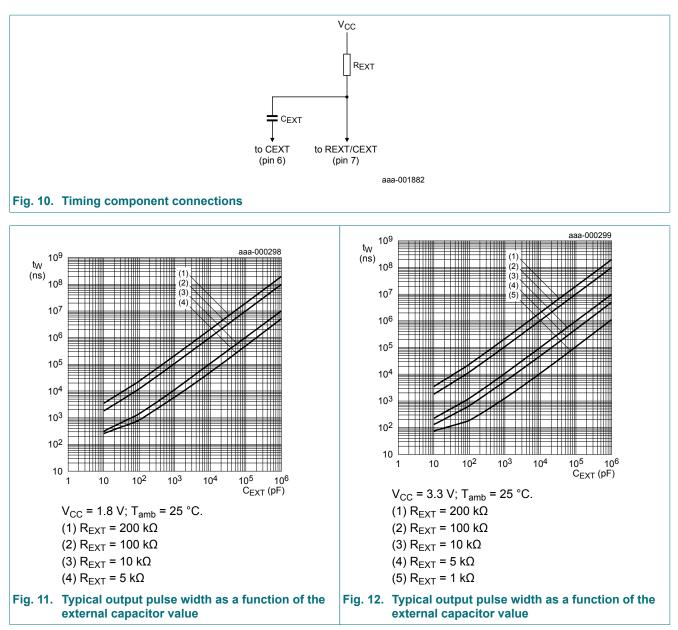
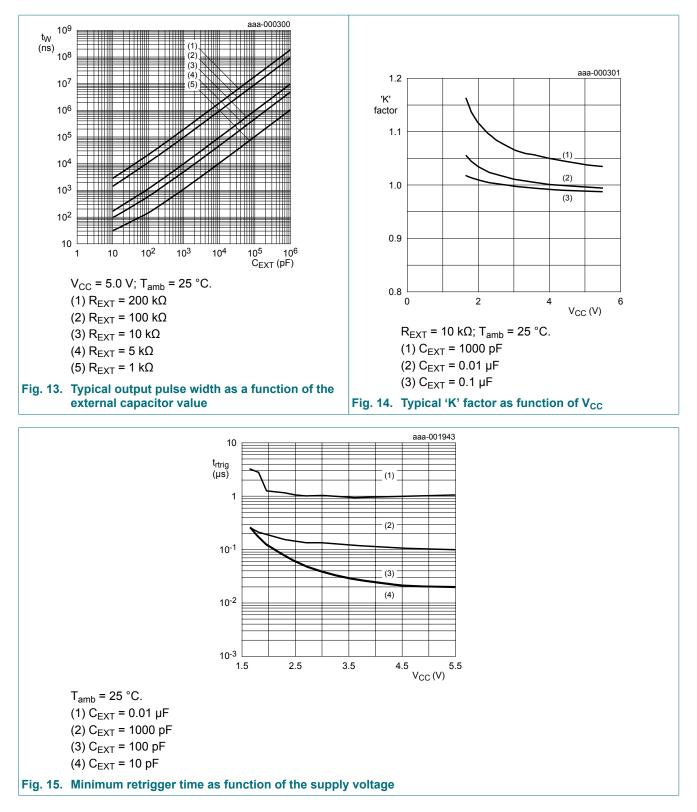


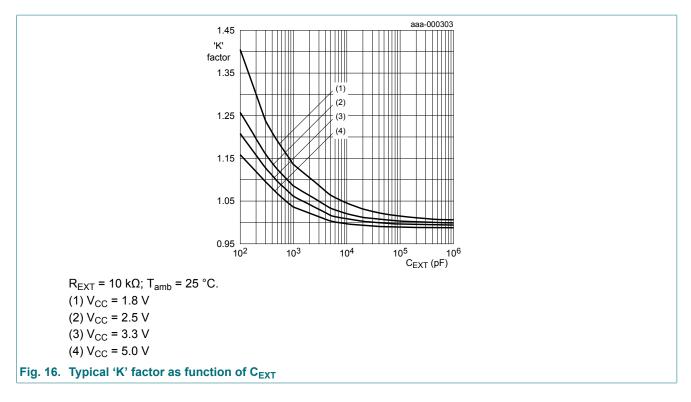
Fig. 8. Output pulse control using reset input CLR



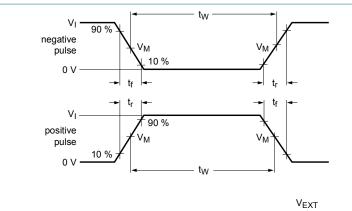
74LVC1G123

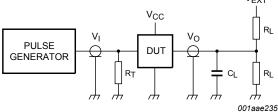






Single retriggerable monostable multivibrator; Schmitt trigger inputs





Test data is given in Table 11.

Definitions for test circuit:

R_L = Load resistance.

C_L = Load capacitance including jig and probe capacitance.

 R_T = Termination resistance should be equal to output impedance Z_o of the pulse generator.

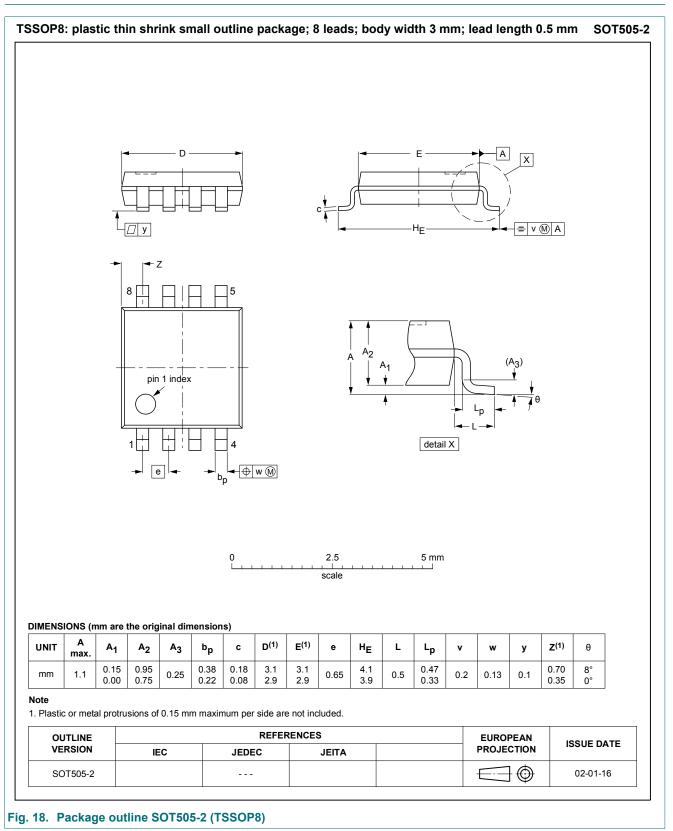
 V_{EXT} = Test voltage for switching times.

Fig. 17. Test circuit for measuring switching times

Table 11. Test data

Supply voltage	Input		Load		V _{EXT}	
V _{cc}	VI	t _r , t _f	CL	RL	t _{PLH} , t _{PHL}	
1.65 V to 1.95 V	V _{CC}	≤ 2.0 ns	15 pF	1 MΩ	open	
2.3 V to 2.7 V	V _{CC}	≤ 2.0 ns	15 pF	1 MΩ	open	
2.7 V	2.7 V	≤ 2.5 ns	15 pF	1 MΩ	open	
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	15 pF	1 MΩ	open	
4.5 V to 5.5 V	V _{CC}	≤ 2.5 ns	15 pF	1 MΩ	open	
1.65 V to 1.95 V	V _{CC}	≤ 2.0 ns	30 pF	1 kΩ	open	
2.3 V to 2.7 V	V _{CC}	≤ 2.0 ns	30 pF	500 Ω	open	
2.7 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open	
3.0 V to 3.6 V	2.7 V	≤ 2.5 ns	50 pF	500 Ω	open	
4.5 V to 5.5 V	V _{CC}	≤ 2.5 ns	50 pF	500 Ω	open	

12. Package outline



Single retriggerable monostable multivibrator; Schmitt trigger inputs

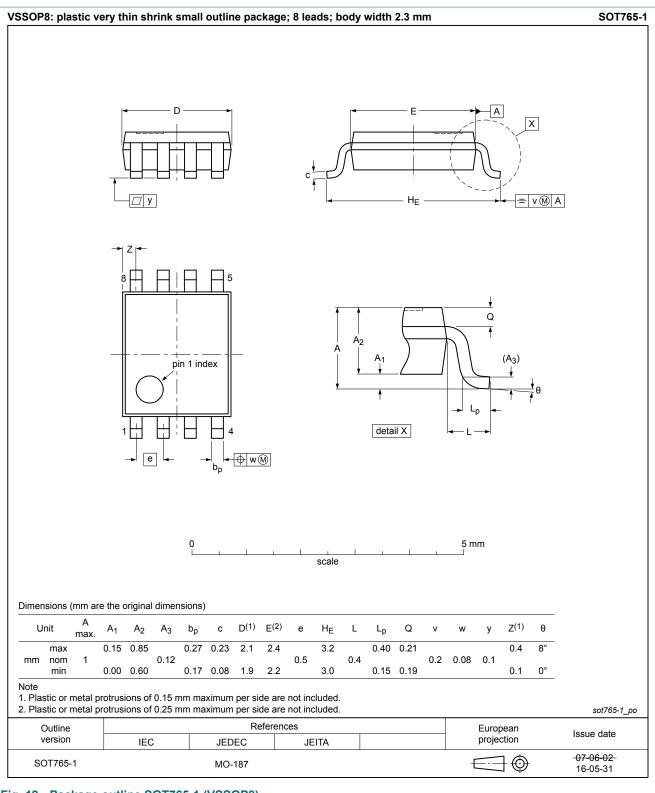
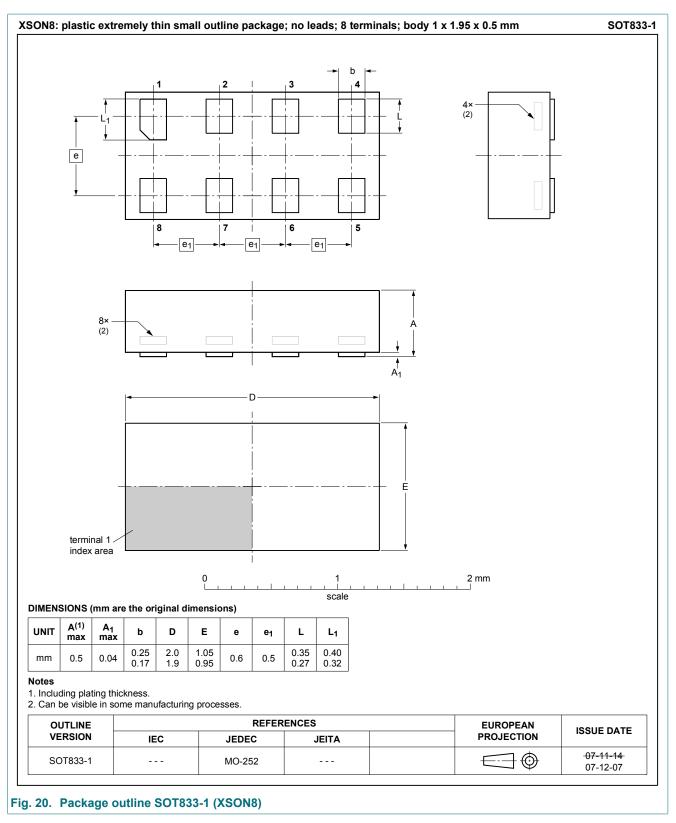
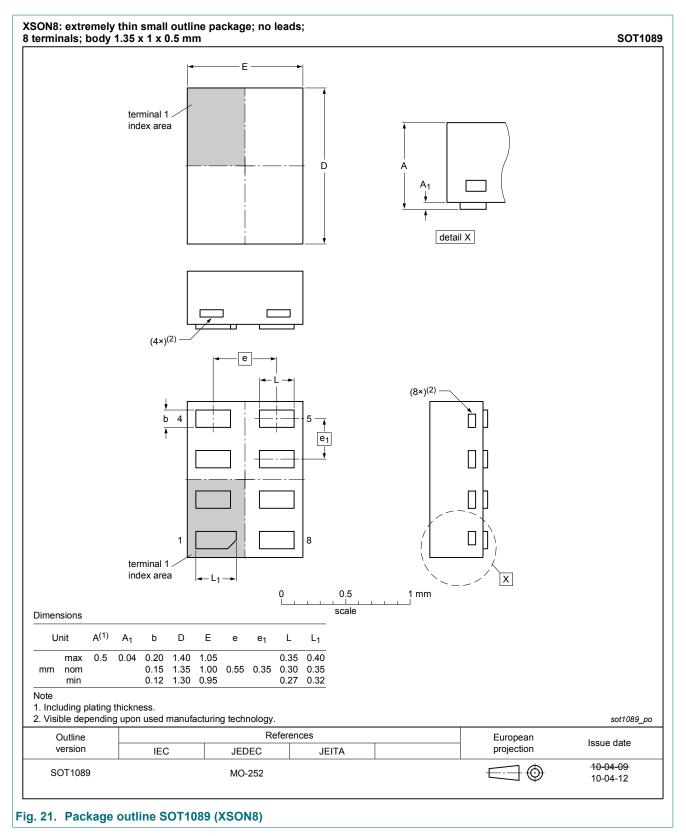


Fig. 19. Package outline SOT765-1 (VSSOP8)





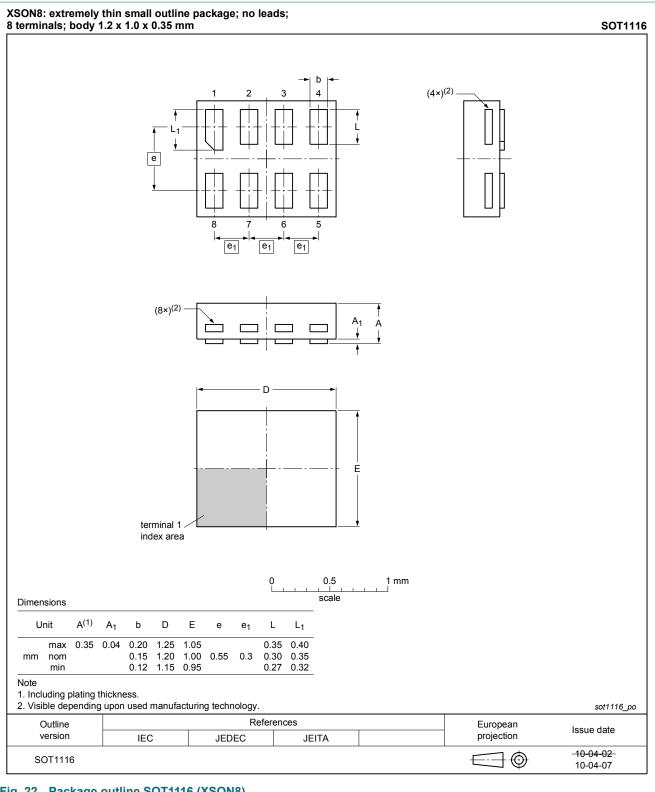
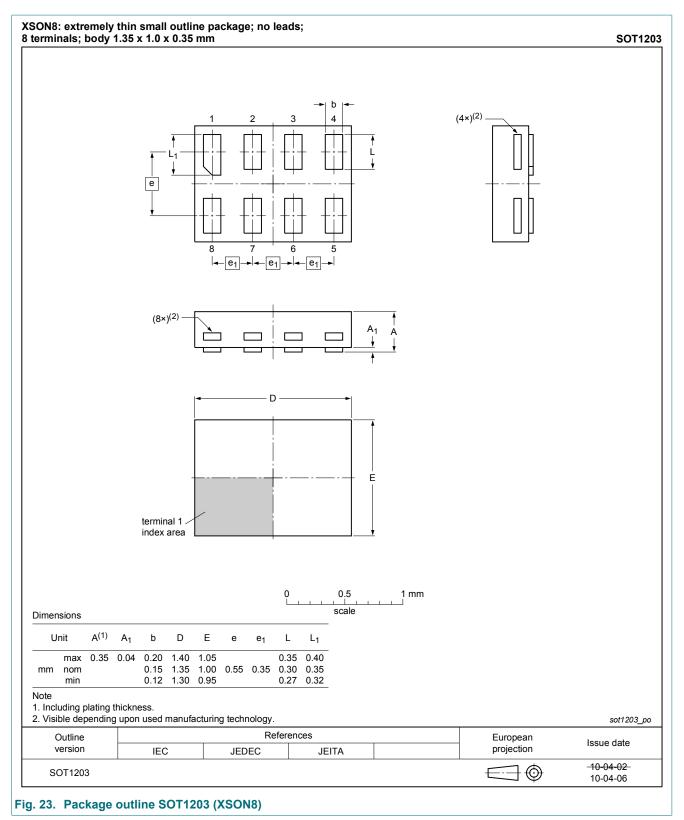


Fig. 22. Package outline SOT1116 (XSON8)



13. Abbreviations

Table 12. Abbre	Table 12. Abbreviations					
Acronym	Description					
CDM	Charged Device Model					
CMOS	Complementary Metal-Oxide Semiconductor					
DUT	Device Under Test					
ESD	ElectroStatic Discharge					
НВМ	Human Body Model					
ММ	Machine Model					
TTL	Transistor-Transistor Logic					

14. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
74LVC1G123 v.6	20181102	Product data sheet	-	74LVC1G123 v.5		
Modifications:	 The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. Legal texts have been adapted to the new company name where appropriate. Type numbers 74LVC1G123GD (SOT996-2/XSON8) removed. 					
74LVC1G123 v.5	20160614	Product data sheet	-	74LVC1G123 v.4		
Modifications:	• Fig. 19, packa	Fig. 19, package outline drawing for SOT765-1 has changed				
74LVC1G123 v.4	20131127	Product data sheet	-	74LVC1G123 v.3		
Modifications:	• 74LVC1G1230	74LVC1G123GM (XQFN8) removed.				
74LVC1G123 v.3	20130329	Product data sheet	-	74LVC1G123 v.2		
Modifications:	For type numb	For type number 74LVC1G123GD XSON8U has changed to XSON8.				
74LVC1G123 v.2	20120801	Product data sheet	-	74LVC1G123 v.1		
Modifications:	 V_{HYS} condition 	V _{HYS} conditions and limits corrected (errata).				
74LVC1G123 v.1	20120123	Product data sheet	-	-		

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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