

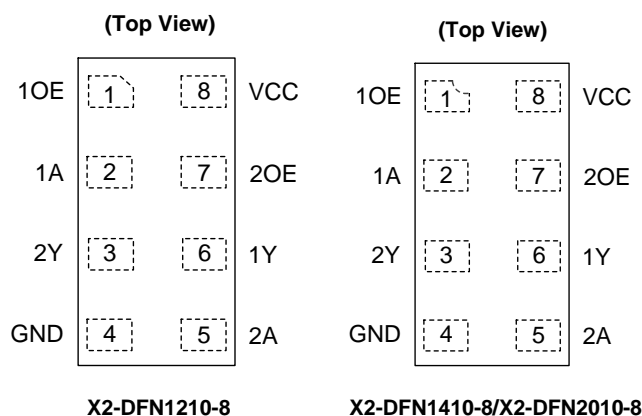
Description

The 74LVC2G126 is a dual buffer gate with 3-state outputs. The device is designed for operation over a power supply range of 1.65V to 5.5V. The device is fully specified for partial power down applications using I_{OFF}. The I_{OFF} circuitry disables the output, preventing damaging current backflow when the device is powered down.

Features

- Wide Supply Voltage Range from 1.65 to 5.5V
- ±24mA Output Drive at 3.3V
- CMOS Low Power Consumption
- I_{OFF} Supports Partial-Power-Down Mode Operation
- Inputs accept up to 5.5V
- Schmitt Trigger Action at all inputs makes the circuit tolerant for slower input rise and fall times. The hysteresis is typically 100mV at V_{CC} = 3.0V.
- ESD Protection Exceeds JESD 22
 - 2000-V Human Body Model (A114)
 - Exceeds 1000-V Charged Device Model (C101)
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative.**
<https://www.diodes.com/quality/product-definitions/>

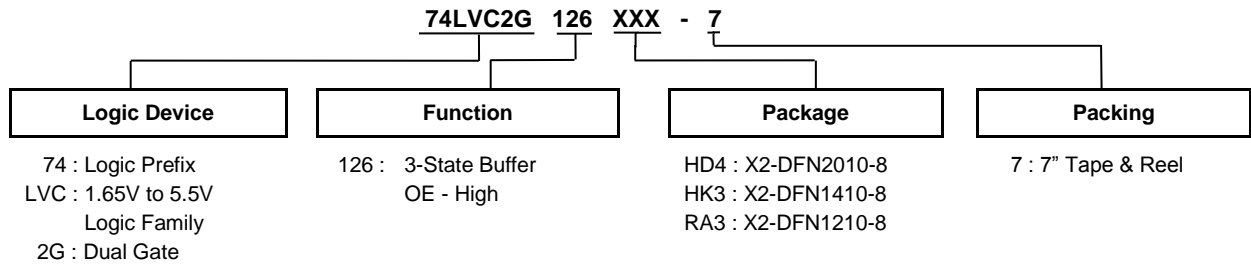
Pin Assignments



Applications

- Voltage Level Shifting
- General Purpose Logic
- Power Down Signal Isolation
- Wide Array of Products Such as:
 - PCs, Networking, Notebooks, Netbooks, PDAs
 - Tablet Computers, E-Readers
 - Computer Peripherals, Hard Drives, CD/DVD ROMs
 - TVs, DVDs, DVRs, Set Top Boxes
 - Cell Phones, Personal Navigation / GPS
 - MP3 Players, Cameras, Video Recorders

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

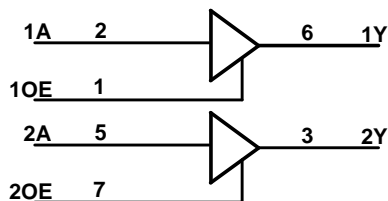
Ordering Information (Note 4)


Device	Package Code	Package (Note 5)	Package Size	7" Tape and Reel (Note 6)	
				Quantity	Part Number Suffix
74LVC2G126HD4-7	HD4	X2-DFN2010-8	1.95mm × 1.0mm × 0.4mm 0.5mm Lead Pitch	5,000/Tape & Reel	-7
74LVC2G126HK3-7	HK3	X2-DFN1410-8	1.35mm × 1.0mm × 0.35mm 0.4mm Lead Pitch	5,000/Tape & Reel	-7
74LVC2G126RA3-7	RA3	X2-DFN1210-8	1.2mm × 1.0mm × 0.35mm 0.3mm Lead Pitch	5,000/Tape & Reel	-7

- Notes: 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.
5. Pad layout as shown in Diodes Incorporated's package outline PDFs, which can be found on our website at <http://www.diodes.com/package-outlines.html>.
6. The taping orientation is located on our website at <https://www.diodes.com/assets/Packaging-Support-Docs/Ap02007.pdf>.

Pin Descriptions

Pin Name	Pin Number	Description
1OE	1	Output Enable for buffer 1
1A	2	Data Input
2Y	3	Data Output
GND	4	Ground
2A	5	Data Input
1Y	6	Data Output
2OE	7	Output Enable for buffer 2
VCC	8	Supply Voltage

Logic Diagram

Function Table

Inputs		Output
OE	A	Y
H	H	H
H	L	L
L	X	Z

Absolute Maximum Ratings (Notes 7 & 8)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	kV
ESD CDM	Charged Device Model ESD Protection	1	kV
V _{CC}	Supply Voltage	-0.5 to +6.5	V
V _I	Input Voltage	-0.5 to +6.5	V
V _O	Output Voltage - Active Mode	-0.5 to V _{CC} +0.5	V
	Output Voltage Power Down Mode	-0.5 to +6.5	V
I _{IK}	Input Clamp Current V _I < 0	-50	mA
I _{OK}	Output Clamp Current (V _O < 0 or V _O > V _{CC})	±50	mA
I _O	Continuous Output Current (V _O = 0 to V _{CC})	±50	mA
I _{CC}	Continuous Current Through V _{CC}	100	mA
I _{GND}	Continuous Current Through GND	-100	mA
T _J	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C

Notes: 7. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

8. Forcing the maximum allowed voltage could cause a condition exceeding the maximum current or conversely forcing the maximum current could cause a condition exceeding the maximum voltage. The ratings of both current and voltage must be maintained within the controlled range.

Recommended Operating Conditions (Note 9)

Symbol	Parameter	Min	Max	Unit	
V _{CC}	Operating Voltage	Operating	1.65	5.5	V
		Data Retention Only	1.5	—	
V _I	Input Voltage	0	5.5	V	
V _O	Output Voltage Active Mode	0	V _{CC}	V	
	Output Voltage Power-Down Mode	0	5.5		
I _{OH}	High-Level Output Current	V _{CC} = 1.65V	—	-4	mA
		V _{CC} = 2.3V	—	-8	
		V _{CC} = 2.7V	—	-12	
		V _{CC} = 3.0V	—	-16	
		V _{CC} = 4.5V	—	-24	
I _{OL}	Low-Level Output Current	V _{CC} = 1.65V	—	4	mA
		V _{CC} = 2.3V	—	8	
		V _{CC} = 2.7V	—	12	
		V _{CC} = 3.0V	—	16	
		V _{CC} = 4.5V	—	24	
Δt/ΔV	Input Transition Rise or Fall Rate	V _{CC} = 1.65V to 2.7V	—	20	ns/V
		V _{CC} = 2.7V to 5.5V	—	10	
T _A	Operating Free-Air Temperature	-40	+125	°C	

Note: 9. Unused inputs should be held at V_{CC} or Ground.

Electrical Characteristics (All typical values are at $T_A = +25^\circ\text{C}$.)

Symbol	Parameter	Test Conditions	V _{CC}	-40°C to +85°C			-40°C to +125°C		Unit
				Min	Typ	Max	Min	Max	
V _{IH}	High-Level Input Voltage	—	V _{CC} = 1.65V to 1.95V	0.65 × V _{CC}	—	—	0.65 × V _{CC}	—	V
			V _{CC} = 2.3V to 2.7V	1.7	—	—	1.7	—	
			V _{CC} = 2.7V to 3.6V	2.0	—	—	2.0	—	
			V _{CC} = 4.5V to 5.5V	0.7 × V _{CC}	—	—	0.7 × V _{CC}	—	
V _{IL}	Low-Level Input Voltage	—	V _{CC} = 1.65V to 1.95V	—	—	0.35 × V _{CC}	—	0.35 × V _{CC}	V
			V _{CC} = 2.3V to 2.7V	—	—	0.7	—	0.7	
			V _{CC} = 2.7V to 3.6V	—	—	0.8	—	0.8	
			V _{CC} = 4.5V to 5.5V	—	—	0.3 × V _{CC}	—	0.3 × V _{CC}	
V _{OH}	High-Level Output Voltage	I _{OH} = -100μA	1.65V to 5.5V	V _{CC} - 0.1	V _{CC}	—	V _{CC} - 0.1	—	V
		I _{OH} = -4mA	1.65V	1.2	1.53	—	0.95	—	
		I _{OH} = -8mA	2.3V	1.9	2.13	—	1.7	—	
		I _{OH} = -12mA	2.7V	2.2	2.5	—	1.9	—	
		I _{OH} = -16mA	3V	2.4	2.7	—	2.2	—	
		I _{OH} = -24mA		2.3	2.6	—	2.0	—	
		I _{OH} = -32mA	4.5V	3.8	4.1	—	3.4	—	
V _{OL}	Low-Level Output Voltage	I _{OL} = 100μA	1.65V to 5.5V	—	0	0.1	—	0.1	V
		I _{OL} = 4mA	1.65V	—	0.08	0.45	—	0.7	
		I _{OL} = 8mA	2.3V	—	0.14	0.3	—	0.45	
		I _{OL} = 12mA	2.7V	—	0.19	0.4	—	0.6	
		I _{OL} = 16mA	3V	—	0.25	0.4	—	0.6	
		I _{OL} = 24mA		—	0.37	0.55	—	0.8	
		I _{OL} = 32mA	4.5V	—	0.43	0.55	—	0.8	
I _I	Input Current	V _I = 5.5V or GND	0V to 5.5V	—	± 0.1	± 5	—	± 20	μA
I _{oz}	Z-State Leakage Current	V _I = V _{IH} or V _{IL} V _O = 5.5V or GND	3.6V	—	± 0.1	± 10	—	± 20	μA
I _{OFF}	Power Down Leakage Current	V _I or V _O = 5.5V	0V	—	± 0.1	± 10	—	± 20	μA
I _{CC}	Supply Current	V _I = 5.5V or GND I _O = 0A	1.65V to 5.5V	—	0.1	10	—	40	μA
ΔI _{CC}	Additional Supply Current	One input at V _{CC} - 0.6V Other inputs at V _{CC} or GND	2.3V to 5.5V	—	5	500	—	5,000	μA
C _I	Input Capacitance	V _I = V _{CC} or GND	3.3V	—	2.5	—	—	—	pF

Operating Characteristics

Parameter		Test Conditions	V _{CC} = 1.8V	V _{CC} = 2.5V	V _{CC} = 3.3V	V _{CC} = 5V	Unit
			Typ	Typ	Typ	Typ	
C _{pd}	Power Dissipation Capacitance	f = 10MHz output enabled	17	17	17	17	pF
		f = 10MHz output disabled	5	5	5	5	pF

Package Characteristics

Symbol	Parameter	Package	Test Conditions	Min	Typ	Max	Unit
θ _{JA}	Thermal Resistance Junction-to-Ambient	X2-DFN2010-8	(Note 10)	—	313	—	°C/W
		X2-DFN1410-8		—	321	—	
		X2-DFN1210-8		—	395	—	
θ _{JC}	Thermal Resistance Junction-to-Case	X2-DFN2010-8	(Note 10)	—	145	—	°C/W
		X2-DFN1410-8		—	166	—	
		X2-DFN1210-8		—	236	—	

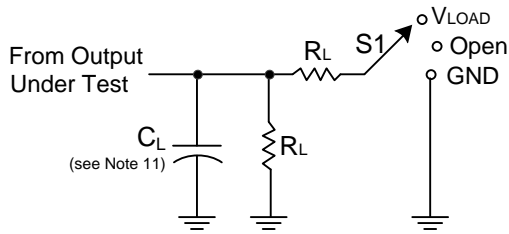
Note: 10. Test condition for each package type: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

Typical Values at T_A = +25°C and nominal voltages 1.8V, 2.5V, 2.7V, 3.3V, and 5.0V. See Figure 1.

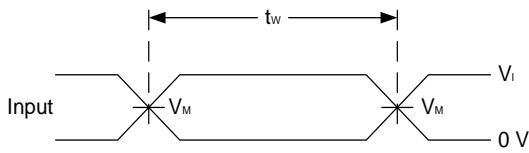
Parameter	From Input	To Output	V _{CC}	T _A = -40°C to +85°C			T _A = -40°C to +125°C		Unit
				Min	Typ	Max	Min	Max	
t _{pd}	A	Y	1.8V ± 0.15V	1.0	3.9	9.8	1.0	12.3	ns
			2.5V ± 0.2V	0.5	2.6	4.9	0.5	6.3	
			2.7V	1.0	2.8	4.7	1.0	5.9	
			3.3V ± 0.3V	0.5	2.4	4.4	0.5	5.4	
			5.0V ± 0.5V	0.5	1.9	3.9	0.5	4.0	
t _{en}	OE	Y	1.8V ± 0.15V	1.0	4.1	10.0	1.0	12.5	ns
			2.5V ± 0.2V	1.0	2.6	5.0	1.0	6.3	
			2.7V	1.0	2.8	4.7	1.0	5.9	
			3.3V ± 0.3V	1.0	2.4	4.1	1.0	5.1	
			5.0V ± 0.5V	0.5	1.8	3.4	0.5	3.9	
t _{dis}	OE	Y	1.8V ± 0.15V	1.0	3.3	12.6	1.0	15.4	ns
			2.5V ± 0.2V	0.5	1.9	5.7	0.5	7.5	
			2.7V	1.5	3.0	4.8	1.5	6.2	
			3.3V ± 0.3V	1.0	2.5	4.4	1.0	5.7	
			5.0V ± 0.5V	0.5	1.8	3.3	0.5	4.4	

Parameter Measurement Information

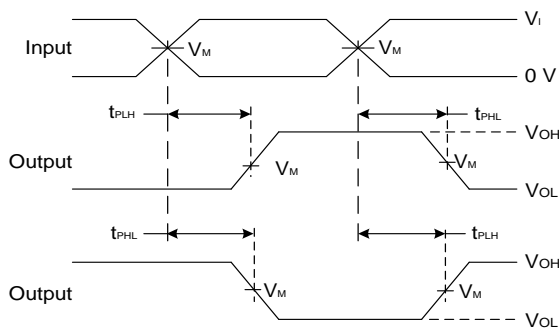


TEST (Notes 14, 15, 16)	S1
t_{PLH}/t_{PHL}	Open
t_{PLZ}/t_{PZL}	V_{LOAD}
t_{PHZ}/t_{PZH}	GND

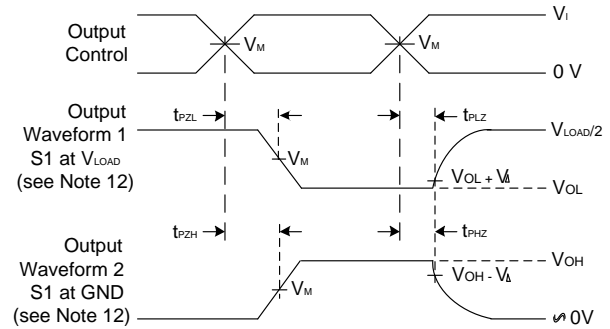
V_{CC}	Inputs		V_M	V_{LOAD}	C_L	R_L	V_{Δ}
	V_i	t_r/t_f					
$1.8V \pm 0.15V$	V_{CC}	$\leq 2ns$	$V_{CC}/2$	$2 \times V_{CC}$	30pF	1k Ω	0.15V
$2.5V \pm 0.2V$	V_{CC}	$\leq 2ns$	$V_{CC}/2$	$2 \times V_{CC}$	30pF	500 Ω	0.15V
2.7V	2.7V	$\leq 2.5ns$	1.5V	6V	50pF	500 Ω	0.3V
$3.3V \pm 0.3V$	3V	$\leq 2.5ns$	1.5V	6V	50pF	500 Ω	0.3V
$5V \pm 0.5V$	V_{CC}	$\leq 2.5ns$	$V_{CC}/2$	$2 \times V_{CC}$	50pF	500 Ω	0.3V



Voltage Waveform Pulse Duration (Note 13)



**Voltage Waveform Propagation Delay Times
Inverting and Non Inverting Outputs (Note 13)**



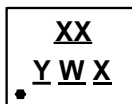
**Voltage Waveform Enable and Disable Times
Low and High Level Enabling**

Figure 1. Load Circuit and Voltage Waveforms

- Notes:
- Includes test lead and test apparatus capacitance.
 - All pulses are supplied at pulse repetition rate $\leq 10MHz$.
 - Inputs are measured separately one transition per measurement.
 - t_{PLZ} and t_{PHZ} are the same as t_{dis} .
 - t_{PZL} and t_{PZH} are the same as t_{en} .
 - t_{PLH} and t_{PHL} are the same as t_{pd} .

Marking Information

(Top View)



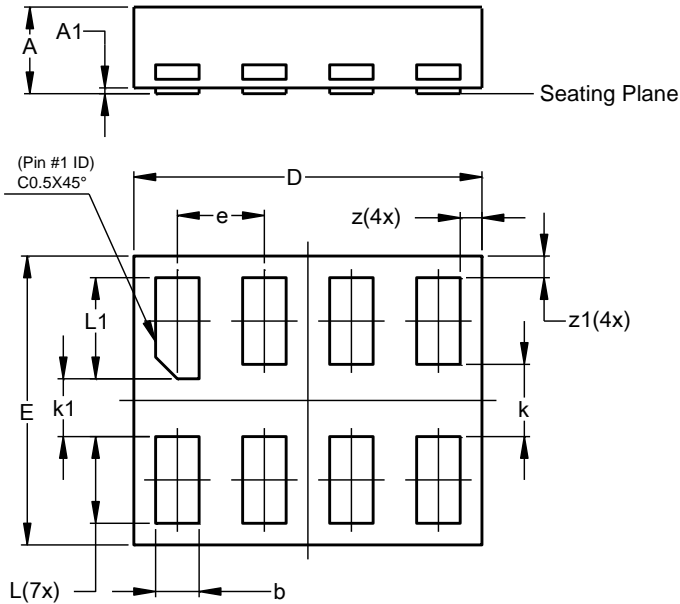
XX : Identification Code
Y : Year : 0~9
W : Week : A~Z : 1~26 week;
 a~z : 27~52 week; z represents
 52 and 53 week
X : Internal Code

Part Number	Package	Identification Code
74LVC2G126HD4-7	X2-DFN2010-8	9X
74LVC2G126HK3-7	X2-DFN1410-8	9Y
74LVC2G126RA3-7	X2-DFN1210-8	9Z

X2-DFN1210-8 Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X2-DFN1210-8

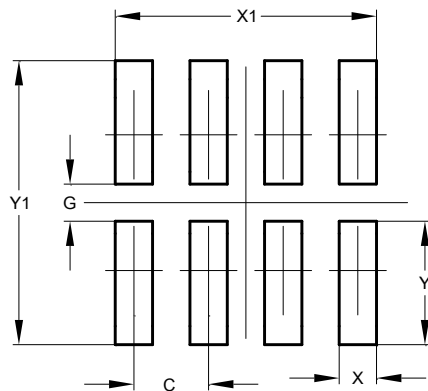


X2-DFN1210-8			
Dim	Min	Max	Typ
A	-	0.35	0.30
A1	0	0.03	0.02
b	0.10	0.20	0.15
D	1.15	1.25	1.20
E	0.95	1.05	1.00
e	-	-	0.30
k	-	-	0.25
k1	-	-	0.20
L	0.25	0.35	0.30
L1	0.30	0.40	0.35
z	0.050	0.100	0.075
z1	0.050	0.100	0.075
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X2-DFN1210-8

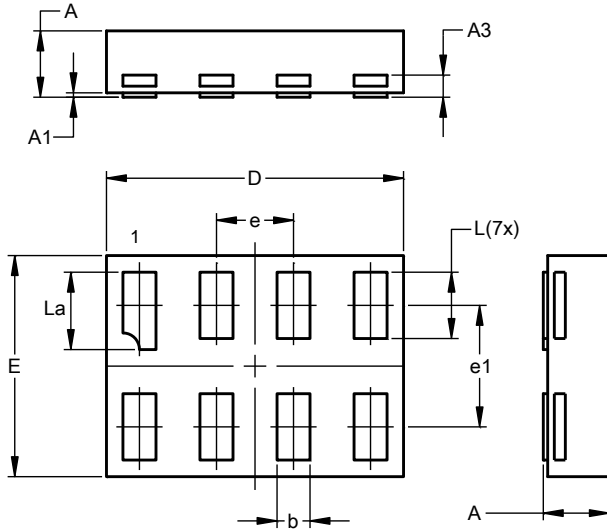


Dimensions	Value (in mm)
C	0.300
G	0.150
X	0.150
X1	1.050
Y	0.500
Y1	1.150

X2-DFN1410-8 Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X2-DFN1410-8

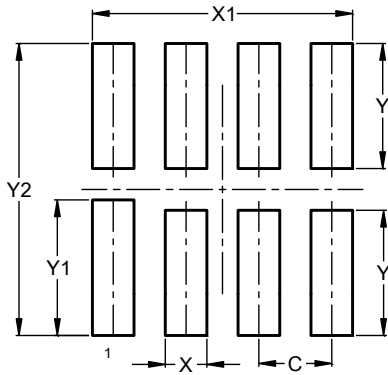


X2-DFN1410-8			
Dim	Min	Max	Typ
A	0.30	0.35	0.33
A1	0.00	0.03	0.02
A3	--	--	0.10
b	0.12	0.20	0.15
D	1.30	1.40	1.35
E	0.95	1.05	1.00
e	--	--	0.35
e1	--	--	0.55
L	0.27	0.35	0.30
L1	0.32	0.40	0.35
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X2-DFN1410-8

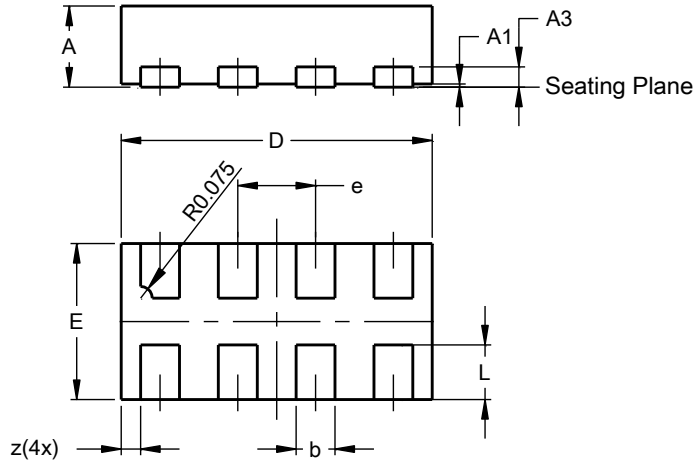


Dimensions	Value (in mm)
C	0.350
X	0.200
X1	1.250
Y	0.600
Y1	0.650
Y2	1.400

X2-DFN2010-8 Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X2-DFN2010-8

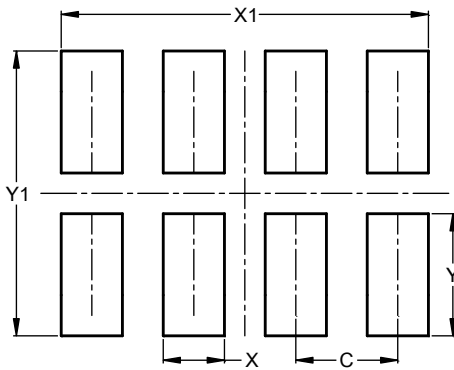


X2-DFN2010-8			
Dim	Min	Max	Typ
A	--	0.40	--
A1	0.00	0.05	0.02
A3	--	--	0.13
b	0.20	0.30	0.25
D	1.950	2.05	2.00
E	0.95	1.05	1.00
e	--	--	0.50
L	0.30	0.40	0.35
z	--	--	0.125
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

X2-DFN2010-8



Dimensions	Value (in mm)
C	0.500
X	0.300
X1	1.800
Y	0.600
Y1	1.400