8MPP2 SERIES MEMS I²C DIGITAL LOW PRESSURE SENSOR

Introduction

The 8MPP2 low pressure sensor utilizes Sensata's field proven automotive Micro-Electro-Mechanical-Systems (MEMS) technology that outputs fully conditioned pressure values via the digital I²C bus. The 8MPP2 features best-in-class performance including high accuracy, low power consumption, and long-term stability in a convenient package for mounting and electrical connection.

Compatible with natural gas, air and other gaseous media, the 8MPP2 is an atmospheric pressure reference sensor that accurately measures pressure in the 0-1 psig to 0-5 psig range. Typical applications are in the natural gas network including valves, pipelines, meters, etc.



Features

• Field proven MEMS sensor technology

Sensata

Technologies

- Digital output I²C
- Low power consumption (8 µA @1Hz)
- Compatible with natural gas
- Small and robust plastic package
- REACH/RoHS/CE compliant

Applications

- Valves
- Natural gas pipelines
- Gas meters



Electrical

Supply Voltage	2.7/3.3/5 VDC ±10%
Output	Digital I ² C, 12 bit scaled up to 14 bit
Average Supply Current	8 μAaverage @1Hz Sample Rate
Active Supply Current	2.5mA max
Output Response Time	3.2ms (max), power up to output valid
Overvoltage Protection	6 VDC
Reverse Voltage	-0.3 VDC
Short Circuit Protection	Yes
EMC	EN61000-4 Level 2
ESD	4kV
Radiated Immunity	IEC 61000-4-20; 50V/m level
Electrical Connection	4 position FFC connector, gold terminals
Pressure Ranges	0-1 to 0-5 psig (0 - 69 to 0 – 345 mbar)
Typical Operating Range	0.15-1.0 psig (10 - 69 mbar)

Physical

Proof Pressure	10 psig (690 mbar)					
Burst Pressure	70 psig (4.8 bar)					
Random Vibration	0.5G, 5-150Hz					
Mechanical Shock	100G					
Drop (any Axis)	1m					
Humidity	71C, 95%RH					
Media Compatibility	Dry Air, Natural Gas, Toluene, Benzene, Iso-octane, Xylene, Stoddard Solution					
Sealing O-Ring	O-ring is supplied by customer with correct material for material compatibility. O-ring should be sized in accordance with ISO 3601-109					

Performance

Accuracy @ 25°C (BFSL) (1)	+/-1.0%F\$ ⁽³⁾ , 25°C
Total Error Band Accuracy ⁽²⁾	+/-2.0%FS ⁽³⁾ , -40° to 85°C
Aging drift	<0.5 %FS ⁽³⁾
Pressure Reference	Vented/True Gauge
Operating Temperature	-40° to +85°C
Storage Temperature	-40° to +125°C

⁽¹⁾Best fit straight line accuracy includes errors from non-linearity, non-repeatability, and hysteresis

⁽²⁾ Total error band accuracy includes errors from non-linearity, non-repeatability, hysteresis, temperature drift, zero offset, and full span offset ⁽³⁾ Full Scale Pressure = Pmax - Pmin

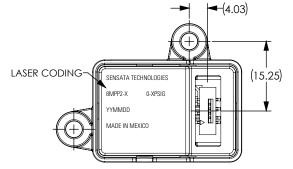


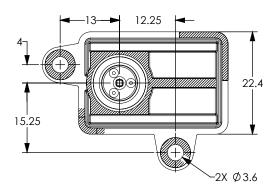
DIMENSIONS

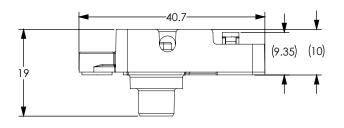
Dimensions are in millimeters (mm)

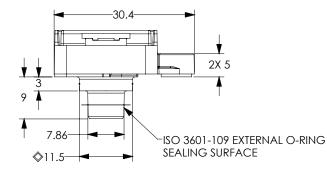
FS (Pressure) = Pmax – Pmin

*Other Media available upon consultation with Sensata



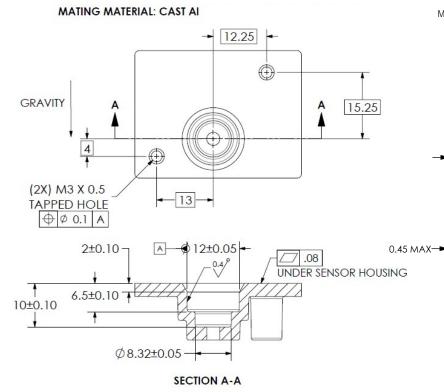




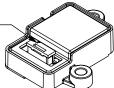




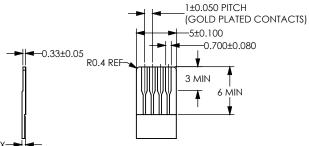
Mating Geometry



CONNECTOR TYPE: 4 POS FFC CONNECTOR PIN 1 LOCATION MANUFACTURER: AMPHENOL MANUFACTURER P/N: SFW4S-2STMAE1LF SINGLE SIDED GOLD CONTACTS



RECOMMENDED CABLE GEOMETRY



Connector Pinout

PIN	Output
1	SCL
2	SDA
3	VSUP
4	GND

HARDWARE INSTALLATION TORQUE: 7 IN-LB (1.20 N-mm) MAX



Example : 8MPP2-01-0x28-U-0

1 psig, 0x28 $\rm l^2C$ address, update mode, 0.5ms update rate

Part numbering for samples					1 3, 51			
	8MPP2	- 01	-	0x28		<u>s</u> -	0	
Family								
8MPP2								
Full Scale Pressure Range —								
01: 1 psig 05: 5 psig								
I ² C Address								
0x28 0x38 0x48 0x58 0x68 0x78								
Mode						J		
U: Update Mode S: Sleep Mode								
Update Rate (valid for Update I	Mode Only)						
0: 0.5 ms 1: 1.5 ms 2: 6.5 ms 3: 32.0 ms								

REACH

I²C COMMUNICATION

I ² C INTERFACE OVER TEMPERATURE RANGE								
OUTPUT								
RESOLUTION	12 BITS MIN							
UPDATE RATE (SEE NOTE 3)	1.5ms							
VOLTAGE	SYMBOL CONDITIONS		MIN	TYP	MAX	UNITS		
HIGH-LEVEL INPUT VOLTAGE	V	-	$0.8 \times V_{_{DD}}$	-	V _{DD}	V		
LOW-LEVEL INPUT VOLTAGE	V	-	-	-	$0.2 \times V_{DD}$	V		
HIGH-LEVEL OUTPUT VOLTAGE	V _{OH}	-	V _{DD} - 0.2	-	V _{DD}	V		
LOW-LEVEL OUTPUT VOLTAGE		-	0	-	0.2	V		
PARAMETER								
OUTPUT SINK CURRENT	I _{OL_SDA}	SDA @ V _{ol} , MAX	2.3	3.9	6.2	mA		
LOAD CAPACITANCE AT SDA		@400kHz	-	-	200	pF		
PULL-UP RESISTOR		-	0.5	1	50	kΩ		
INPUT CAPACITANCE	C _{PC_IN}	-	-	-	10	pF		

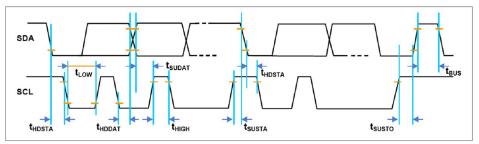
NOTES:

- THE SDA SIGNAL IS OPEN DRAIN. IT REQUIRES AN EXTERNAL PULL-UP SIZED TO MEET 1. THE SPECIFIED TIMING REQUIREMENTS LIMITS ON LOAD CAPACITANCE, PULL-UP RESISTOR, AND INPUT PIN CAPACITANCE
- 2
- ARE PROVIDED IN THE CASE WHERE MULTIPLE SLAVES ARE ON THE I²C B UPDATE RATE IS THE TIME INTERVAL BETWEEN NEW MEASUREMENTS WHEN THE 3

SENSOR IS POWERED CONTINUOUSLY.

SCL IS INPUT ONLY. SENSOR IS A SLAVE ONLY AND DOES NOT CREATE ANY CLOCK 4. STRETCHING OR MULTIMASTER.

TIMING DIAGRAM OVER TEMPERATURE RANGE:



NOTE: THERE ARE THREE ADJUSTMENTS TO THE I²C IMPLEMENTATION COMPARED WITH THE ORIGINAL I²C PROTOCOL:

- SENDING A START-STOP CONDITION WITHOUT ANY TRANSITIONS ON THE ٠ CLK LINE (NO CLOCK PULSES IN BETWEEN) CREATES A COMMUNICATION ERROR FOR THE NEXT COMMUNICATION, EVEN IF THE NEXT START CONDITION IS CORRECT AND THE CLOCK PULSE IS APPLIED. AN ADDITIONAL START CONDITION MUST BE SENT, WHICH RESULTS IN RESTORATION OF PROPER COMMUNICATION.
- THE RESTART CONDITION- A FALLING SDA EDGE DURING DATA TRANSMISSION WHEN THE CLK CLOCK LINE IS STILL HIGH- CREATES THE SAME SITUATION. THE NEXT COMMUNICATION FAILS, AND AN ADDITIONAL START CONDITION MUST BE SENT FOR THE CORRECT COMMUNICATION.
- A FALLING SDA EDGE IS NOT ALLOWED BETWEEN THE START CONDITION AND THE FIRST RISING SCL EDGE. IF USING AN I²C ADDRESS WITH THE FIRST BIT 0, SDA MUST BE HELD OW FROM THE START CONDITION THROUGH THE FIRST BIT.

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS
SCL clock frequency	f _{SCL}	100		400	kHz
Start condition hold time relative to SCL edge	t _{HDSTA}	0.1			μs
Minimum SCL clock low width 1)	t _{LOW}	0.6			μs
Minimum SCL clock high width 1)	t _{HIGH}	0.6			μs
Start condition setup time relative to SCL edge	t _{susta}	0.1			μs
Data hold time on SDA relative to SCL edge	t _{hddat}	0			μs
Data setup time on SDA relative to SCL edge	t _{SUDAT}	0.1			μs
Stop condition setup time on SCL	t _{susto}	0.1			μs
Bus free time between stop condition and start condition	t _{BUS}	2			μs

5. NO EXTERNAL CAPACITANCE NEEDED FOR Vs