



DIMMABLE LED CONTROLLER/DRIVER

Description

The DIODES™ AL1692 is a high performance, high power factor, high efficiency, and high current precision buck-boost and flyback dimmable LED controller/drivers for triac dimmable LED lamp applications. The AL1692 topology provides an accurate output current over wide line and load regulation. The wide switching frequency operates at boundary conduction mode (BCM) to ease EMI/EMC design and testing, to meet the latest regulatory standards.

The AL1692 controller with external MOSFET can support higher output power application, up to 25W. The AL1692-20C LED driver integrates with 600V/2A MOSFET. The AL1692 platform solutions can cover both 120Vac and 230Vac applications. The AL1692 has the built-in thermal fold-back protection trigger point to automatically reduce output current. Other protection features enhance LED lighting system's safety and reliability.

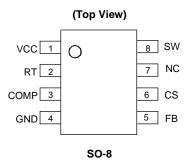
The AL1692 dimming curve is compliant with the NEMA SSL6 standard. The AL1692 applies to a wide range of dimmers, including leading edge and trailing edge dimmer, to achieve deep dimming down to 1%.

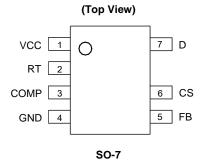
The AL1692 controller is available in SO-8 package. The AL1692-20C is available in SO-7 package.

Features

- Tight Current Sense Tolerance: ±3%
- Low Startup Current: 100µA Typical
- Low Operation Current: 210µA (Switching Frequency at 4kHz)
- Single Winding Inductor
- Wide Range of Dimmer Compatibility
- For Controller Power Can Drive up to 25W
- For MOSFET Options: 600V/2A
- NEMA SSL6 Dimming Curve Compliant
- Internal Protections
 - Undervoltage Lockout (UVLO)
 - Leading-Edge Blanking (LEB)
 - Cycle-by-Cycle Overcurrent Protection (OCP)
 - Output Open/Short Protection (OVP/OSP)
 - Thermal Foldback Protection (TFP)
 - Overtemperature Protection (OTP)
- SO-8 (Controller) and SO-7 (With MOSFET) Packages
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Pin Assignments





Applications

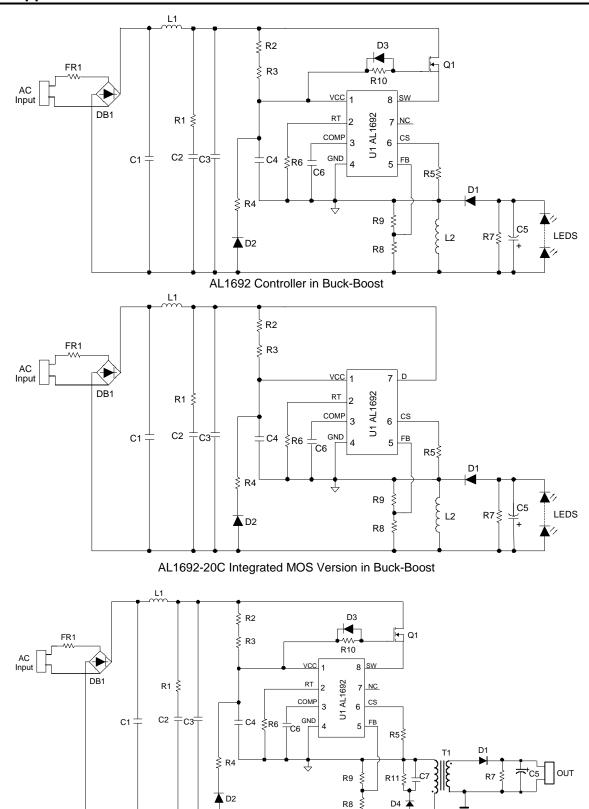
- Mains dimmable LED lamps
- Offline LED power supply drivers

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



Typical Application Circuits



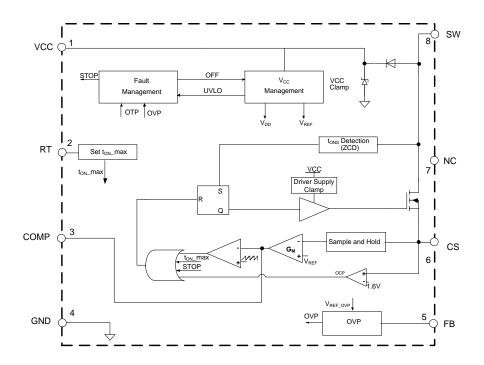
AL1692 Controller in Fly-Back



Pin Descriptions

Pin Number		Pin Name	Function	
SO-8	SO-7	Pin Name	runction	
1	1	VCC	Power supply voltage	
2	2	RT	Resistor set the system's maximum toN	
3	3	COMP	Compensation for current control	
4	4	GND	Ground	
5	5	FB	Feedback for LED open protection voltage	
6	6	CS	Current sensing	
7	7		Not connected	
	,	D (SO-7)	Drain of the internal high voltage MOSFET	
8	_	SW	Source driver of Switch	

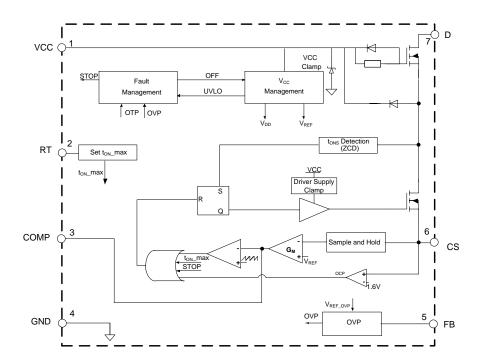
Functional Block Diagram



AL1692 Controller



Functional Block Diagram (continued)



AL1692-20C with MOSFET



Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.) (Note 4)

Symbol	Parameter	Rating	Unit
Vcc	Power Supply Voltage	18	V
Vsw	Voltage on SW Pin (AL1692) (Note 5)	20	V
V _D	Voltage on Drain Pin (AL1692-20C)	600	V
IDS	Continuous Drain Current T _C = +25°C (AL1692-20C)	2	Α
Vcs	Voltage on CS Pin	-0.3 to 7	V
VRT	Voltage on RT Pin	-0.3 to 7	V
V _{FB}	Voltage on FB Pin	-0.3 to 7	V
TJ	Operating Junction Temperature	-40 to +150	°C
Tstg	Storage Temperature	-65 to +150	°C
TLEAD	Lead Temperature (Soldering, 10 seconds)	+260	°C
D-	SO-8 Power Dissipation (T _A = +50°C) (Note 6)	0.96	W
P _D	SO-7 Power Dissipation (T _A = +50°C) (Note 6)	0.8	W
0	SO-8 Thermal Resistance (Junction to Ambient) (Note 6)	104	°C/W
θJA	SO-7 Thermal Resistance (Junction to Ambient) (Note 6)	123	°C/W
0	SO-8 Thermal Resistance (Junction to Case) (Note 6)	6.6	°C/W
θις	SO-7 Thermal Resistance (Junction to Case) (Note 6)	19	°C/W
	ESD (Human Body Model)	2,000	V
_	ESD (Machine Model)	200	V

Notes:

Recommended Operating Conditions (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
TA	Ambient Temperature (Note 7)	-40	+105	°C

7. The device may operate normally at +125°C ambient temperature under the condition not trigger temperature protection. Note:

^{4.} Stresses greater than those listed under Absolute Maximum Ratings can cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to *Absolute Maximum Ratings* for extended periods can affect device reliability. All voltages unless otherwise stated are measured with respect

^{5.} SW pin can withstand pulse voltage up to 25V with duration of 300ns.
6. Device mounted on 1" x 1" FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.



Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Symbol	Parameter	Condition	Min	Тур	Max	Unit
UVLO	•	1		•		l
V _{TH} (ST)	Startup Voltage	_	_	14.5	_	V
Vopr (min)	Minimal Operating Voltage	After Turn On	_	8.5	_	V
VCC_CLAMP	Vcc Clamp Voltage	Icc = 1mA	_	15.5	_	V
Standby Current			•			
Ist	Startup Current	Vcc = Vth (st)-0.5V Before Startup	_	100	_	μА
ICC (OPR)	Operating Current	Switching Frequency at 4kHz	_	210	_	μΑ
Source Driver			1	I	l	L
Rds(on)LV	Internal Low Voltage MOSFET On- State Resistance (Note 8)		_	1	_	Ω
High Voltage and Super-	Junction MOSFET					
RDS(ON)HV	Drain-Source On-State Resistance	AL1692-20C	_	4	5.5	Ω
VDS	Drain-Source Breakdown Voltage	AL1692-20C	600	_	_	V
I _{DSS}	Drain-Source Leakage Current	AL1692-20C	_	_	1	μΑ
RT						
Vrt_ref	Reference Voltage of RT Pin	_	_	0.5	_	V
Current Sense						
Vcs_clamp	CS Clamp Voltage	_	_	1.6	_	V
VREF	Internal Current Loop Control Reference	_	0.388	0.4	0.412	V
ton_min	Minimum toN	_	_	550	_	ns
ton_max	Maximum toN	$R_T = 51k\Omega$ $V_{COMP} = 4V$	_	5.4	_	μs
toff_min	Minimum toff (Note 8)	_	_	4	_	μs
toff_max	Maximum toff	_	_	290	_	μs
FB						
V _{FB}	Feedback Voltage	_	3.76	4.0	4.24	V
lfB	Feedback Pin Input Leakage Current	V _{FB} = 2V	_	4	_	μA
Error Amplifier						
G _М	Gm Trans-Conductance	_	_	25	_	μA/V
Isource	Amplifier Source Current	Vcs = 0V	_	10		μΑ
Isink	Amplifier Sink Current	V _{CS} = 1.5V	_	28	_	μΑ
Thermal Foldback and O	Thermal Foldback and Overtemperature Protection (OTP)					
TFOLD	Thermal Foldback (Note 8)	_	_	+145	_	°C
	Thermal Shutdown (Notes 8 & 9)			+160		°C

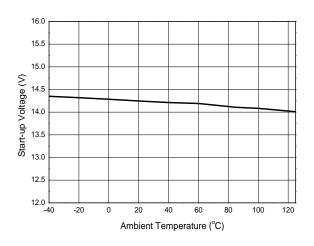
Notes:

^{8.} These parameters, although guaranteed by design, are not tested in production.9. The device will latch off when OTP happens, recovered after power cycle and the device won't operate normally at this temperature.

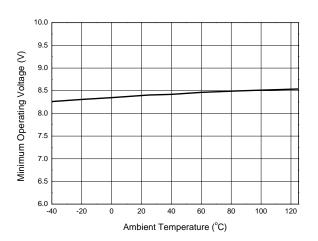


Performance Characteristics (Note 10)

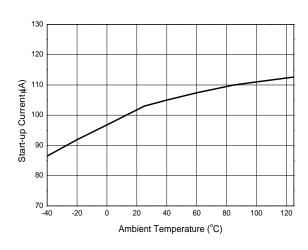
Start-up Voltage vs. Ambient Temperature



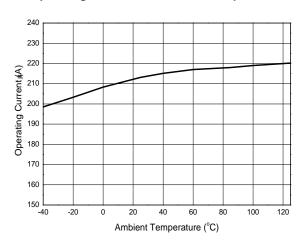
Minimum Operating Voltage vs. Ambient Temperature



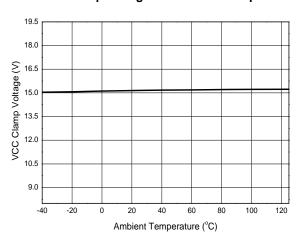
Start-up Current vs. Ambient Temperature



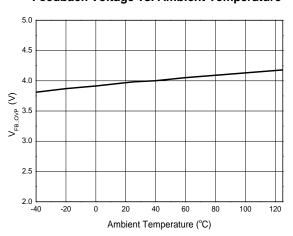
Operating Current vs. Ambient Temperature



VCC Clamp Voltage vs. Ambient Temperature



Feedback Voltage vs. Ambient Temperature



Note: 10. These electrical characteristics are tested under DC condition. The ambient temperature is equal to the junction temperature of the device.



Functional Description and Application Information

Operation

The AL1692 is a single stage, single winding, high efficiency, and high power factor dimmable LED driver controller/drivers for triac dimmable LED lamp applications. The AL1692 controller with an external MOSFET can support larger power application up to 25W. The AL1692-20C LED driver with 600V/2A internal MOSFET can suit for both 120V_{AC} and 230V_{AC} application.

The AL1692 adopts source-driver technique to decrease the system operating current. It uses a novel method to detect the tops time which results in the removal for the need of an auxiliary winding. The AL1692 operates at boundary conduction mode (BCM) which can ease EMI design and achieve high efficiency. High power factor (HPF) is achieved by using constant on-time mode; coupled with a closed loop of constant current control, the AL1692 achieves good line and load regulation.

Startup and Supply Voltage

Before startup, the V_{CC} capacitor C4 is charged by the startup resistors (R2, R3) from the high voltage mains. When the startup voltage is reached, the AL1692 starts switching. During normal operation, the V_{CC} supply is provided by startup resisters (R2, R3) and the output voltage (V_{OUT}) rectified by one diode (D2). In this way the system can provide V_{CC} supply at low dimming angle.

The AL1692 has an internal VCC clamp voltage (typical 15.5V), which is limited by one internal active Zener diode.

When VCC voltage drops to below the $V_{OPR\ (MIN)}$, switching is stopped. So the device can operate normally when the voltage on VCC pin is between $V_{OPR\ (MIN)}$ and VCC clamp voltage.

Protections

Undervoltage Lockout (UVLO)

When the voltage on the VCC pin drops to below V_{OPR (MIN)}, the IC stops switching. The IC can restart when the voltage on VCC exceeds the startup voltage (V_{TH (ST)}).

Leading-Edge Blanking (LEB)

To prevent false detection of the peak current of the inductor, a blanking time following switch-on is designed. When the internal switch turns on, a short current spike can occur because of the capacitive discharge of the voltage over the drain and source. It is disregarded during the LEB time (ton MIN).

Cycle-by-Cycle Overcurrent Protection (OCP)

The AL1692 has a built-in peak current detector. It is triggered when the voltage on CS pin reaches the peak level V_{CS_CLAMP}. The R5 is connected to the CS pin to sense the current of the inductor. The maximum peak current (I_{PEAK(MAX)}) of the inductor can be calculated as below:

$$I_{PEAK(MAX)} = \frac{V_{CS_CLAMP}}{R5} \tag{1}$$

The detection circuit is activated after the LEB time. When the detection circuit senses the CS voltage is higher than 1V, the IC will turn off the switching to limit the output current. It automatically provides protection for the maximum LED current during operation. A propagation delay exists between overcurrent detection and actual source-switch off, so the actual peak current is a little higher than the OCP level set by the R5.

Overvoltage Protection and Output-Open Protection (OVP)

The output voltage is sensed by the FB pin, which provides an overvoltage protection (OVP) function. When the output is open or large transient happens, the output voltage will exceed the rated value (R8, R9). When the voltage exceeds VFB, the overvoltage is triggered and the IC will discharge Vcc. When the Vcc is below the UVLO threshold voltage, IC will restart and the Vcc capacitor is charged again by startup resistance. If the overvoltage condition still exists, the system will work in hiccup mode.

Output-Short Protection (OSP)

When LED is shorted, the device cannot detect the topic time, and the device controls the system operation at 4kHz low frequency.

AL1692 Document number: DS39008 Rev. 3 - 2



Functional Description and Application Information (continued)

Thermal Foldback Protection (TFP)

AL1692 has a thermal foldback protection (TFP) function and adopts self-adaptive control method, which can prevent the system from breaking down caused by high temperature. The overheating temperature is set at +145°C typical, when the junction temperature of the IC is higher than +145°C typical, the device will linearly decrease the internal reference voltage to decrease the output current. As a result of this feature, the device can control the system's output power at high ambient temperature, to control the quantity of heat of the system. This enhances the safety of the system at high temperature.

Thermal foldback waveform is shown below:

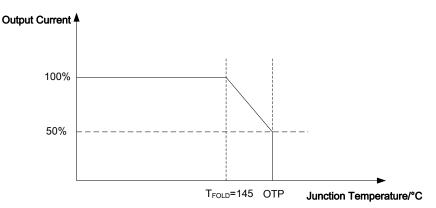


Figure 1. Thermal Foldback Waveform

Overtemperature Protection (OTP)

The AL1692 has overtemperature protection (OTP) function. When the junction temperature reach to +160°C typical, the IC will trigger an overtemperature protection, which causes the device to shut down and latched condition. Once OTP is triggered, the system needs to be resumed after the system's AC source supply has been reset and powered up.

Design Parameters

Setting the Current Sense Resistor R5

The AL1692 adopts boundary conduction mode, the output current is calculated as below,

$$I_{O_MEAN} = \frac{1}{\pi} \cdot \int_{0}^{\pi} \frac{1}{2} \cdot I_{PEAK} \cdot \frac{t_{OFF}}{t_{ON} + t_{OFF} + t_{DELAY}} dt$$
 (2)

Where,

IPEAK is the peak current of the inductance.

ton is the internal MOSFET on time.

toff is the freewheel diode D1 conduction time.

tDELAY is typical 0.4µs.

The AL1692 is a closed loop constant current control with the relationship between output current and current sense voltage follows this equation,

$$V_{REF} = \frac{1}{\pi} \cdot \int_{0}^{\pi} I_{PEAK} \cdot R5 \cdot \frac{t_{OFF}}{t_{ON} + t_{OFF} + t_{DELAY}} dt$$

$$(3)$$

Where,

VREF is the internal reference, typical 0.4V.

R5 is the current sense resistor.

So we can get the output current equation as below,

$$I_{O_MEAN} = \frac{1}{2} \cdot \frac{V_{REF}}{R5} \tag{4}$$



Functional Description and Application Information (continued)

Inductance Selection (L2)

In buck-boost structure, the peak current of the inductance can be calculated as below,

$$I_{PEAK} = \frac{\pi \cdot V_{REF}}{R5 \cdot \int_{0}^{\pi} \sin(\theta) \cdot \frac{\sqrt{2} \cdot V_{IN_RMS} \cdot \sin(\theta)}{\sqrt{2} \cdot V_{IN_RMS} \cdot \sin(\theta) + Vo} d\theta}$$
(5)

Where,

VIN_RMS is the input voltage's RMS value.

Vo is the system output voltage.

The AL1692 controls the system operating at boundary conduction mode which results in its operating frequency not being constant. To set the minimum switching frequency fmin at the crest of the minimum AC input.

$$L2 = \frac{\sqrt{2}V_{IN_RMS} \cdot V_O}{I_{PEAK} \cdot (\sqrt{2}V_{IN_RMS} + V_O) \cdot f_{MIN}}$$
(6)

According to the Faraday's Law, the winding number of the inductance can be calculated by:

$$N_{L2} = \frac{L2 \cdot I_{PEAK}}{A_e \cdot B_m} \tag{7}$$

Where,

Ae is the core effective area.

 $B_{\mbox{\scriptsize m}}$ is the maximum magnetic flux density.

ton MAX Setting

In order to get a good dimmer compatibility and a good dimming depth, the device sets a ton_{MAX} by one external resistor R_T (R6). And the ton_{MAX} time has the below equation:

$$t_{ON_MAX} = \frac{3.3 \cdot C_{REF}}{\frac{V_{RT_REF}}{10 \cdot R6} + 0.33uA}$$
(8)

Where,

VRT_REF is the internal RT pin 0.5V's reference.

CREF is the internal 1.5pF capacitor.

Dimming Control

The AL1692 is a closed loop control device; the dimming function is realized by t_{ON_MAX} limited when dimmer is connected in. When the dimmer is at the largest conduction angle, the device still has the adjustability to control the output current constant before COMP voltage is adjusted to the maximum 4V, so for most of the dimmer, the output current is almost the same with the no dimmer condition at the largest conduction angle. If the conduction angle is decreased, the COMP pin voltage will continue to increase quickly till to the maximum level (typical 4V), the device will output t_{ON_MAX} to limit system's output current. The t_{ON_MAX} is set by RT pin connected with one resistor, so the dimming depth can be adjusted by RT resistor (R6).

Before the AL1692 enters t_{ON_MAX} mode, it keeps the output current constant the same as no dimmer condition. When it enters t_{ON_MAX} mode, we can get the following equation:

$$I_{PEAK_DIM} = \frac{V_{IN_RMS} \cdot Sin(\theta) \cdot t_{ON_MAX}}{L2}$$
 (9)



Functional Description and Application Information (continued)

From the buck-boost output current equation, we can get the output current when dimming:

$$I_{O}(\theta) = \begin{cases} \frac{1}{2} \cdot \frac{V_{REF}}{R5} & \text{if } t_{ON} < t_{ON_MAX} \\ \frac{1}{\pi} \int_{0}^{\alpha} \frac{1}{2} \cdot I_{PEAK_DIM} \cdot \frac{\sqrt{2}V_{IN_RMS} \cdot Sin(\theta)}{\sqrt{2}V_{IN_RMS} \cdot Sin(\theta) + V_{O}} d\theta & \text{else} \end{cases}$$

$$(10)$$

Where,

 α is the dimmer conduction angle.

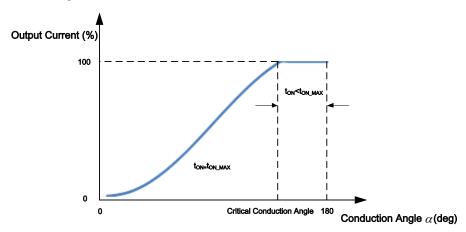


Figure 2. Dimming Curve

Dimmer Compatibility

Passive Bleeder Design

The passive bleeder is designed to supply latching and holding current to eliminate dimmer misfire and flicker.

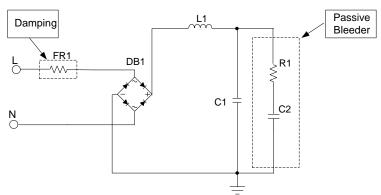


Figure 3. LED Driver Schematic with Passive Bleeder

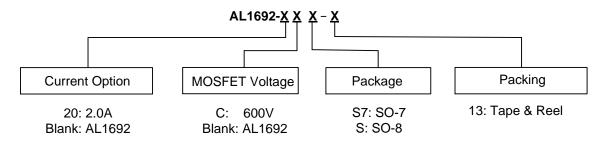
The passive bleeder includes a capacitor (C2, in hundreds of nF) to provide latching current. A resistor (R1) is necessary to dampen the current spike. Because a large C2 will affect the PF, THD and efficiency, the value of the capacitor (C2) should be selected accordingly. Generally, 100nF/400V to 330nF/400V is recommended. R1 is used to limit the latching current. If R1 is too large, the latching current is not enough and the TRIAC dimmer will misfire causing LED flicker. If R1 is too small, it will result in greater power dissipation. Generally speaking, a 200Ω to $2k\Omega$ resistor is selected for R1.

Passive Damping Design

FR1 is the damper for reducing the spike current caused by quick charging of C2 at firing. In general, FR1 is selected from 20Ω to 100Ω for low line like $120V_{AC}$ application, and 51Ω to 200Ω for high line like $230V_{AC}$ application.



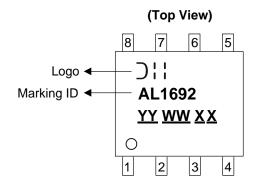
Ordering Information



Part Number	Dort Number Cuffix	Dankaga Cada	Dookono	Packing	
Fait Number	nber Part Number Suffix Package Code Pa		Package	Qty.	Carrier
AL1692S-13	-13	S	SO-8	4000	Tape & Reel
AL1692-20CS7-13	-13	S7	SO-7	4000	Tape & Reel

Marking Information

SO-8



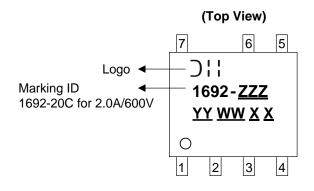
 \underline{YY} : Year (ex: 22 = 2022)

WW: Week: 01 to 52; 52

represents week 52 and 53

 $\underline{X}\underline{X}$: Internal Code

SO-7



<u>YY</u>: Year (ex: 22 = 2022)

<u>WW</u>: Week: 01 to 52; 52

represents week 52 and 53

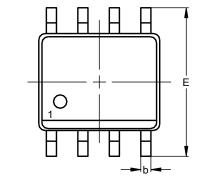
XX: Internal Code

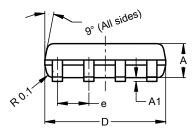


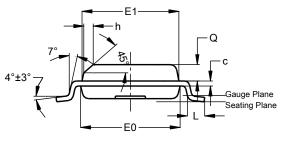
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SO-8

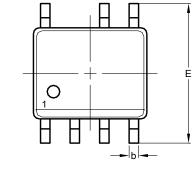


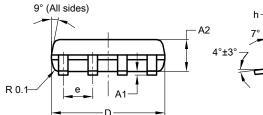


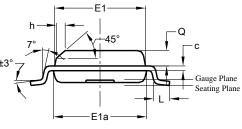


SO-8					
Dim	Min	Max	Тур		
Α	1.40	1.50	1.45		
A1	0.10	0.20	0.15		
b	0.30	0.50	0.40		
С	0.15	0.25	0.20		
D	4.85	4.95	4.90		
Е	5.90	6.10	6.00		
E1	3.80	3.90	3.85		
E0	3.85	3.95	3.90		
е			1.27		
h	-		0.35		
L	0.62	0.82	0.72		
ø	0.60	0.70	0.65		
All Dimensions in mm					

(2) Package Type: SO-7







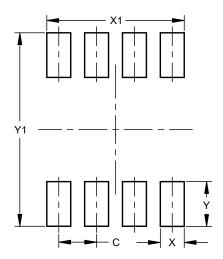
	SO-7						
Dim	Min	Max	Тур				
A2	1.40	1.50	1.45				
A1	0.10	0.20	0.15				
b	0.30	0.50	0.40				
С	0.15	0.25	0.20				
D	4.85	4.95	4.90				
Е	5.90	6.10	6.00				
E1	3.80	3.90	3.85				
E1a	3.85	3.95	3.90				
е	1	-	1.27				
h	-	-	0.35				
L	0.62	0.82	0.72				
Q	0.60	0.70	0.65				
All Dimensions in mm							



Suggested Pad Layout

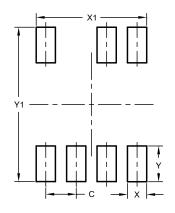
Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SO-8



Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Υ	1.505
Y1	6.50

(2) Package Type: SO-7



Dimensions	Value (in mm)
C	1.270
Х	0.802
X1	4.612
Y	1.505
Y1	6.500

Mechanical Data

- Moisture Sensitivity: Level 1 per JESD22-A113
- Terminals: Finish Matte Tin Plated Leads, Solderable per M2003 JESD22-B102 @3
- Weight
 - SO-8: 0.079 grams (Approximate)
 - SO-7: 0.077 grams (Approximate)