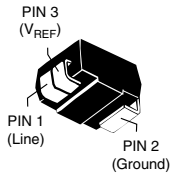
Battrax® Series Positive/Negative - Modified DO-214

HF RoHS

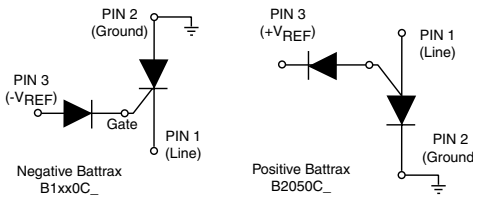
Agency Approvals

Agency	Agency File Number
UL	E133083

Pinout Designation



Schematic Symbol



Description

The Battrax® series offers programmable SIDACtor® overvoltage protection devices for SLIC applications. This series is offered in a negative Battrax version and a positive Battrax version. The B1xx0C_ is for a $-V_{REF}$ supply and the B2050C_ is for a $+V_{REF}$ supply. Designed using an SCR and a gate diode, the B1xx0C_ Battrax begins to conduct at $|-V_{REF}| + |-1.2\ V|$ while the B2050C_ Battrax begins to conduct at $|+V_{REF}| + |1.2\ V|$.

Features and Benefits

- Low voltage overshoot
- Low on-state voltage
- Does not degrade surge capability after multiple surge events within limit.
- Fails short circuit when surged in excess of ratings
- Robust surge current ratings
- Gate triggered tracking devices
- Pb-free E3 means 2nd level interconnect is Pb-free and the terminal finish material is tin(Sn) (IPC/JEDEC J-STD-609A.01)

Applicable Global Standards

- TIA-968-A
- TIA-968-B
- ITU K.20/21 Enhanced Level*
- ITU K.20/21 Basic Level
- GR 1089 Inter-building*
- GR 1089 Intra-building
- IEC 61000-4-5
- YD/T 1082
- YD/T 993
- YD/T 950

*A-rated parts require series resistance

Electrical Characteristics

Part Number	Marking	V_{DRM} @I_{DRM}=5µA	V_S @100V/µs	I_H	I_S	I_T	V_T @I_T=2.2 Amps	Capacitance*	
		V min	V max	mA min	mA max	A max	V max	pF	
								Min	Max
B1100CALRP	B10A	$I-V_{REF}I + I-1.2VI$	$I-V_{REF}I + I-10VI$	100	100	2.2	4	30	200
B1160CALRP	B16A	$I-V_{REF}I + I-1.2VI$	$I-V_{REF}I + I-10VI$	160	100	2.2	4	30	200
B1200CALRP	B12A	$I-V_{REF}I + I-1.2VI$	$I-V_{REF}I + I-10VI$	200	100	2.2	4	30	200
B2050CALRP	B25A	$I+V_{REF}I + I1.2VI$	$I+V_{REF}I + I10VI$	5	50	2.2	4	20	200
B1100CCLRP	B10C	$I-V_{REF}I + I-1.2VI$	$I-V_{REF}I + I-10VI$	100	100	2.2	4	30	200
B1160CCLRP	B16C	$I-V_{REF}I + I-1.2VI$	$I-V_{REF}I + I-10VI$	160	100	2.2	4	30	200
B1200CCLRP	B12C	$I-V_{REF}I + I-1.2VI$	$I-V_{REF}I + I-10VI$	200	100	2.2	4	30	200
B2050CCLRP	B25C	$I+V_{REF}I + I1.2VI$	$I+V_{REF}I + I10VI$	5	50	2.2	4	20	200

Notes:
- Absolute maximum ratings measured at T_A= 25°C (unless otherwise noted).
- Devices are uni-directional
- All electrical characteristics shown are defined from Tip (pin 1) to Ground (pin 2), and Ring (pin 1) to Ground (pin 2)
- V_{REF} Max Value for the negative Battrax is -200 V.
- V_{REF} Max Value for the positive Battrax is 110 V.
* Off-state capacitance (C_O) is measured across pins 1 & 2 at 1 MHz with a 2V bias.

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Surge Ratings

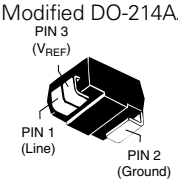
Series	I_{PP}									I_{TSM} 50/60 Hz	di/dt
	0.2x310 [1] 0.5x700 [2]	2x10 [1] 2x10 [2]	8x20 [1] 1.2x50 [2]	10x160 [1] 10x160 [2]	10x560 [1] 10x560 [2]	5x320 [1] 9x720 [2]	10x360 [1] 10x360 [2]	10x1000 [1] 10x1000 [2]	5x310 [1] 10x700 [2]		
	A min	A min	A min	A min	A min	A min	A min	A min	A min	A min	A/µs max
A	20	150	150	90	50	75	75	45	75	20	500
C	50	500	400	200	150	200	175	100	200	50	500

Notes:

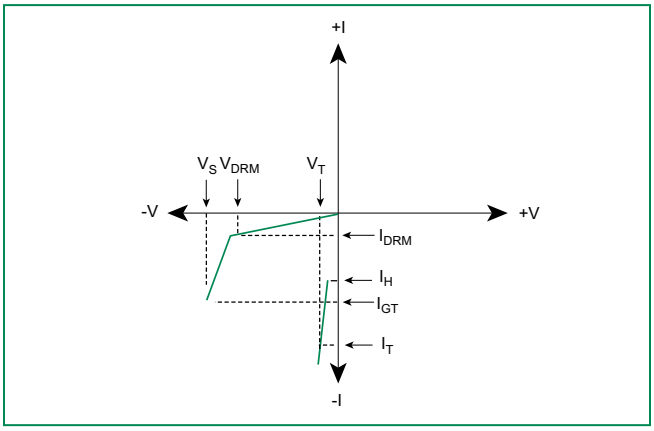
1 Current waveform in µs
2 Voltage waveform in µs

- Peak pulse current rating (I_{PP}) is repetitive and guaranteed for the life of the product that remains in thermal equilibrium.
- I_{PP} ratings applicable over temperature range of -40°C to +85°C (I_{PP} rating assumes V_{REF} equals +/- 48 V)
- The device must initially be in thermal equilibrium with -40°C ≤ T_J ≤ +150°C

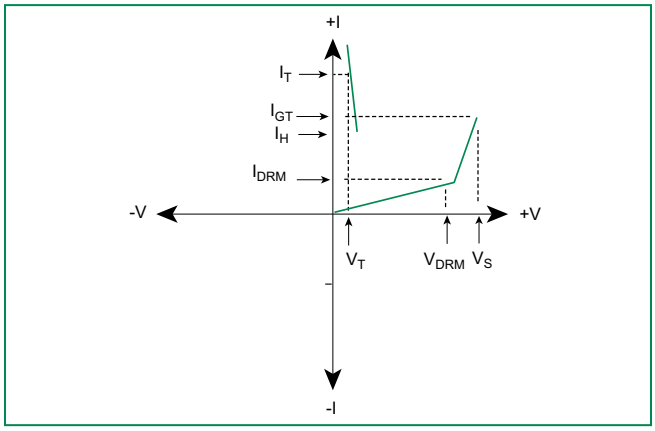
Thermal Considerations

Package	Symbol	Parameter	Value	Unit
Modified DO-214AA 	T_J	Operating Junction Temperature Range	-40 to +150	°C
	T_S	Storage Temperature Range	-65 to +150	°C
	$R_{\theta JA}$	Thermal Resistance: Junction to Ambient	85	°C/W

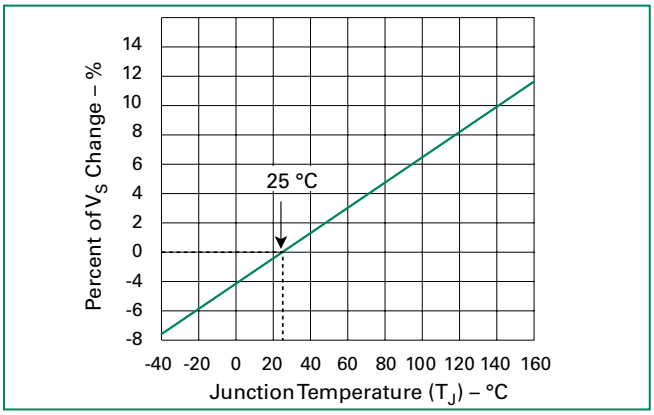
V-I Characteristics - Negative Battrax



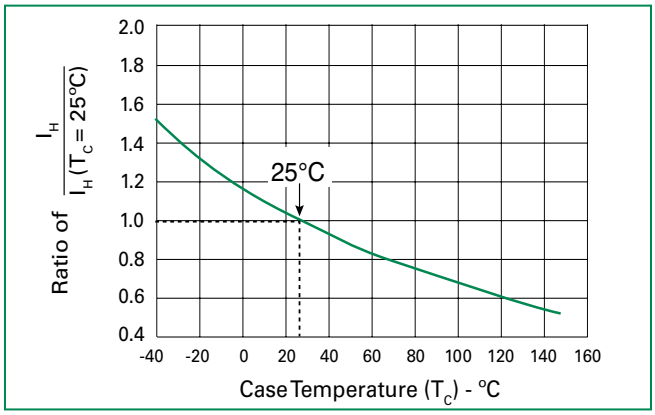
V-I Characteristics - Positive Battrax



Normalized V_S Change vs. Junction Temperature



Normalized DC Holding Current vs. Case Temperature



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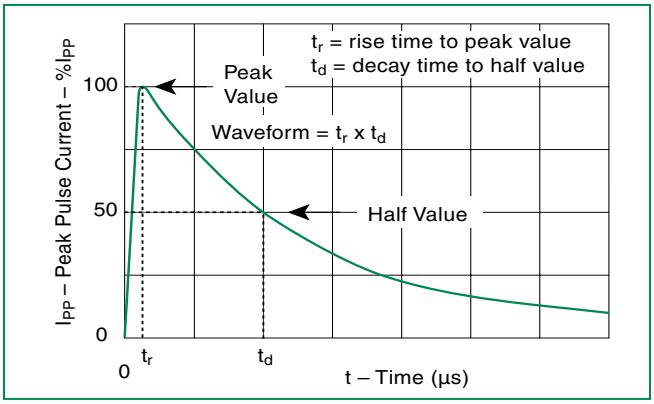
SIDACtor® Protection Thyristors
SLIC Protection

t_r x t_d Pulse Waveform



Environmental Specifications

High Temp Voltage Blocking	80% Rated V_{REF} Max. (V_{DC} Peak) +125°C or +150°C, 504 or 1008 hrs. MIL-STD-750 (Method 1040) JEDEC, JESD22-A-101
Temp Cycling	-65°C to +150°C, 15 min. dwell, 10 up to 100 cycles. MIL-STD-750 (Method 1051) EIA/JEDEC, JESD22-A104
Biased Temp & Humidity	52 V_{DC} (+85°C) 85%RH, 504 up to 1008 hrs. EIA/JEDEC, JESD22-A-101
High Temp Storage	+150°C 1008 hrs. MIL-STD-750 (Method 1031) JEDEC, JESD22-A-101
Low Temp Storage	-65°C, 1008 hrs.
Thermal Shock	0°C to +100°C, 5 min. dwell, 10 sec. transfer, 10 cycles. MIL-STD-750 (Method 1056) JEDEC, JESD22-A-106
Autoclave (Pressure Cooker Test)	+121°C, 100%RH, 2atm, 24 up to 168 hrs. EIA/JEDEC, JESD22-A-102
Resistance to Solder Heat	+260°C, 30 secs. MIL-STD-750 (Method 2031)
Moisture Sensitivity Level	85%RH, +85°C, 168 hrs., 3 reflow cycles (+260°C Peak). JEDEC-J-STD-020, Level 1

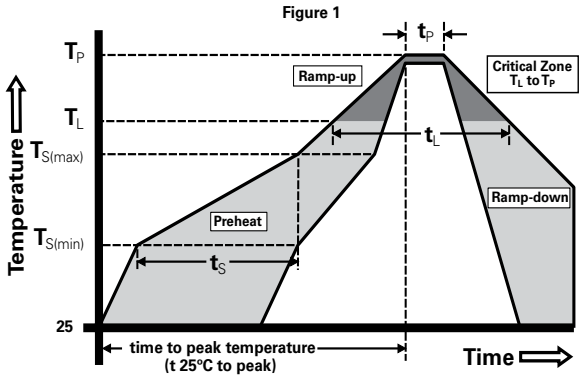
Physical Specifications

Lead Material	Copper Alloy
Terminal Finish	100% Matte-Tin Plated
Body Material	UL recognized epoxy meeting flammability classification 94V-0

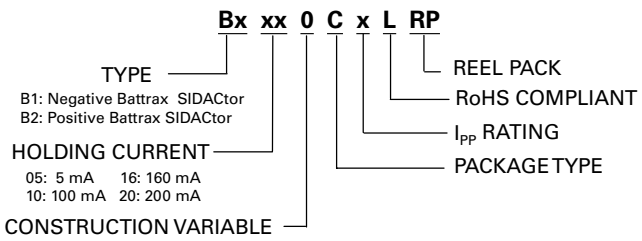
Soldering Parameters

Reflow Condition		Pb-Free assembly (see Fig. 1)
Pre Heat	-Temperature Min ($T_{s(min)}$)	+150°C
	-Temperature Max ($T_{s(max)}$)	+200°C
	-Time (Min to Max) (t_s)	60-180 secs.
Average ramp up rate (Liquidus Temp (T_L) to peak)		3°C/sec. Max.
$T_{S(max)}$ to T_L - Ramp-up Rate		3°C/sec. Max.
Reflow	-Temperature (T_L) (Liquidus)	+217°C
	-Temperature (t_L)	60-150 secs.
Peak Temp (T_P)		+260(+0/-5)°C
Time within 5°C of actual Peak Temp (t_p)		30 secs. Max.
Ramp-down Rate		6°C/sec. Max.
Time 25°C to Peak Temp (T_P)		8 min. Max.
Do not exceed		+260°C

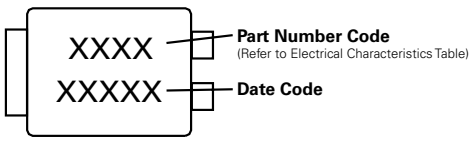
Figure 1



Part Numbering



Part Marking



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