EPD Extension Kit Gen2 (EXT2)

The EXT2 board is a new generation of extension board to drive most of Pervasive Displays' e-Paper displays (EPD) including iTC and eTC driver types of EPD.

It is designed to kick-start EPD development with your microcontrollers. There are onboard headers can be bridged to any of your development kit and product. It has 20 to 40 pins socket at the rear side to stack on Texas Instruments (TI)'s LaunchPad directly.

Visit *www.pervasivedisplays.com/kits/ext2_kit* for more details.

Key Features

- Support driving EPD with eTC and iTC driver IC embedded Aurora and Spectra material film
- On board driving circuit as a starter kit to easily develop EPD application with your product
- Direct socket to stack on TI LaunchPads to get various libraries and technical supports
- Open driving source code on TI Code Composer Studio™ IDE
- Free powerful utility to test and demonstration with EPD
- On board 8M bits Flash memory and temperature sensor
- Provide bridging cable and header to connect with your product and any evaluation kit
- Provide touch panel and light guide connector to support EPD integration

Kit Contents

EPD Extension Board Gen2 (EXT2 board) * 1 piece

90 degrees 20 pins header * 1 piece	Bridging cable * 1 piece

Pin Assignment of EXT2 header

Jumper	Pin No.	Name	Description
J4, J5	1	VCC	Target supply voltage
	2	GUARD2	TFT Breakage detection (option)
	3	VPP	Power Supply for OTP Programming
	4	UART-TX	LaunchPad default pin
	5	TS_SDA_MCU	I^{2}C data for external temperature sensor (GPIO)
	6	TS_SCL_MCU	I^{2}C clock for external temperature sensor (GPIO)
	7	SPI_CLK	Clock for SPI
	8	BUSY	COG busy pin (GPIO)
	9	D/C	Data/Command control for iTC (GPIO)
	10	/RESET	Reset signal. Low enable (GPIO)
J5, J6	11	PANEL_ON	COG driver power control pin (GPIO)
	12	DISCHARGE	EPD discharge when EPD power off (GPIO)
	13	BORDER_CONTROL (GUARD1)	Border control pin for eTC (GPIO) Breakage detection (option)
	14	SPI_MISO	Serial output from EPD to host MCU
	15	SPI_MOSI	Serial input from host MCU to EPD
	16	RST/SBWTDIO	LaunchPad default pin
	17	TEST/SBWTCK	LaunchPad default pin
	18	/FLASH_CS	On board flash chip select (GPIO)
	19	/EPD_CS	Chip Select. Low enable (GPIO)
	20	GND	Ground