

45 V, 100 mA NPN general-purpose transistor Rev. 2 — 8 September 2021

**Product data sheet** 

### 1. General description

NPN general-purpose transistor in an ultra small DFN1110D-3 (SOT8015) leadless Surface-Mounted Device (SMD) plastic package with side-wettable flanks.

#### Table 1. Product overview

Type number	Package	PNP complement:	
	Nexperia	JEDEC	
BC847AQB	SOT8015	MO-340BA	BC857AQB
BC847BQB			BC857BQB
BC847CQB			BC857CQB

### 2. Features and benefits

- High power dissipation capability •
- Suitable for Automatic Optical Inspection (AOI) of solder joint
- Smaller footprint compared to conventional leaded SMD packages
- Low package height of 0.5 mm
- AEC-Q101 qualified

### 3. Applications

- General-purpose switching and amplification
- Space restricted applications •

### 4. Quick reference data

#### Table 2. Quick reference data

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base	-	-	45	V
I <sub>C</sub>	collector current		-	-	100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms	-	-	200	mA
h <sub>FE</sub>	h <sub>FE</sub> DC current gain					
	BC847AQB	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA	110	-	220	
	BC847BQB		200	-	450	
	BC847CQB		420	-	800	

# nexperia

### 5. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	В	base		С
2	E	emitter	3	
3	С	collector		B [x
				É
				sym021
			Transparent top view	

### 6. Ordering information

#### Table 4. Ordering information

Type number	Package						
	Name	Description	Version				
BC847AQB	DFN1110D-3	plastic leadless extremely thin small outline package with	SOT8015				
BC847BQB		side-wettable flanks (SWF); 3 terminals; 0.65 mm pitch; body: 1.1 x 1.0 x 0.48 mm					
BC847CQB	-						

### 7. Marking

Table 5. Marking					
Type number	Marking code				
BC847AQB	A2				
BC847BQB	A3				
BC847CQB	A4				

### 8. Limiting values

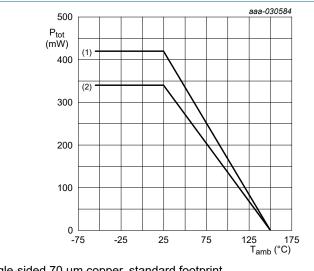
#### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

T<sub>amb</sub> = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter	open emitter		50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	6	V
I <sub>C</sub>	collector current			-	100	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	200	mA
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	100	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1]	-	340	mW
			[2]	-	420	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature				150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided 35 µm copper; tin-plated and standard footprint.
 Device mounted on an FR4 PCB; single-sided 70 µm copper; tin-plated and standard footprint.



(1) FR4 PCB; single-sided 70 µm copper, standard footprint (2) FR4 PCB; single-sided 35 µm copper, standard footprint

Fig. 1. Power derating curves DFN1110D-3 (SOT8015)

### 9. Thermal characteristics

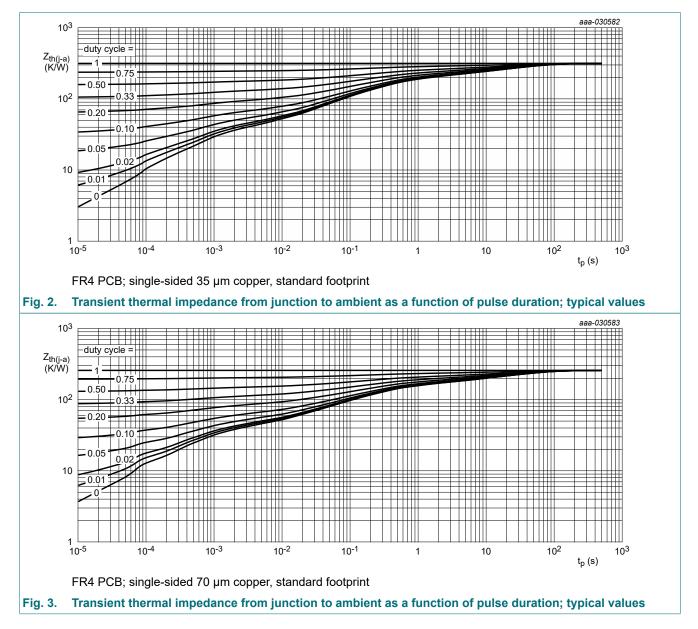
#### Table 7. Thermal characteristics

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	[1]	-	-	368	K/W
			[2]	-	-	298	K/W

[1] Device mounted on an FR4 PCB; single-sided 35 µm copper; tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB; single-sided 70 µm copper; tin-plated and standard footprint.



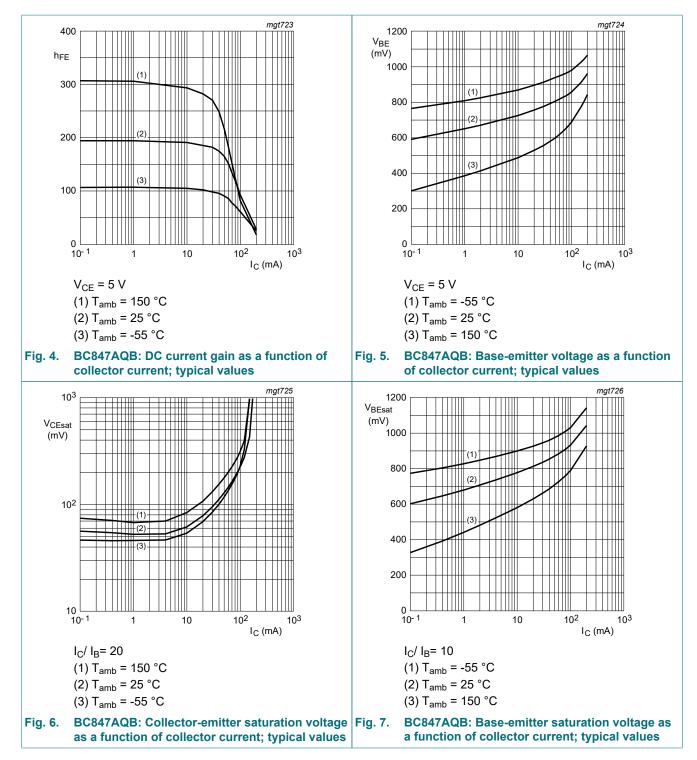
### 10. Characteristics

#### **Table 8. Characteristics**

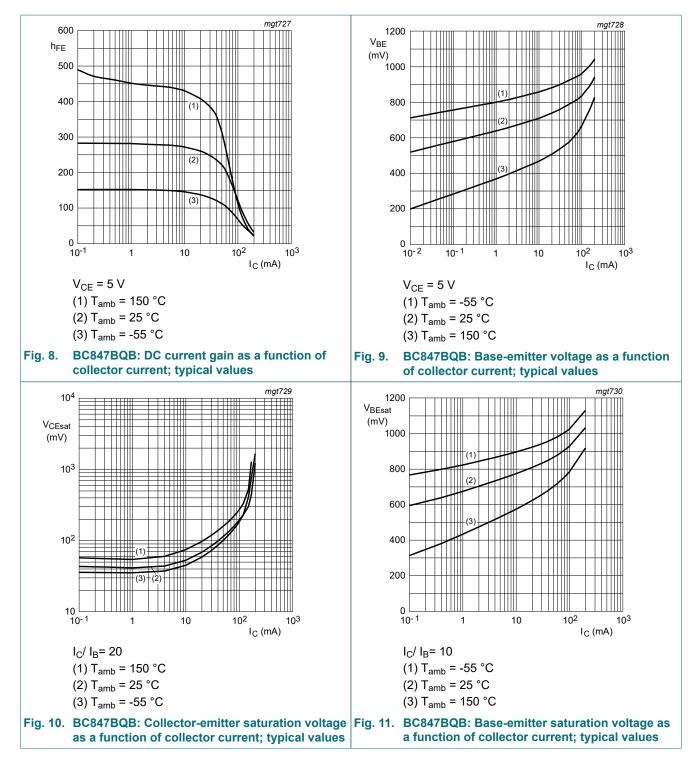
 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = 100 μA; I <sub>E</sub> = 0 A		50	-	-	V
V <sub>(BR)CES</sub>	collector-emitter peak voltage	I <sub>C</sub> = 2 mA; I <sub>E</sub> = 0 A			-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>E</sub> = 100 μA; I <sub>C</sub> = 0 A			-	-	V
I <sub>CBO</sub>	collector-base cut-off	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A		-	-	15	nA
	current	V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	5	μA
I <sub>EBO</sub>	emitter-base cut-off current	V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A	-	-	100	nA	
h <sub>FE</sub>	DC current gain			1			
BC847AQB		V <sub>CE</sub> = 5 V; I <sub>C</sub> = 2 mA		110	-	220	
BC847BQ	BC847BQB				-	450	
BC847CQB				420	-	800	
V <sub>CEsat</sub> collector-emitter		I <sub>C</sub> = 10 mA; I <sub>B</sub> = 0.5 mA		-	-	200	mV
	saturation voltage	I <sub>C</sub> = 100 mA; I <sub>B</sub> = 5 mA	[1]	-	-	400	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = 5 V ; I <sub>C</sub> = 2 mA	[2]	580	-	700	mV
		V <sub>CE</sub> = 5 V ; I <sub>C</sub> = 10 mA	[2]	-	-	770	mV
V <sub>BEsat</sub>	base-emitter saturation	I <sub>C</sub> = 10 mA ; I <sub>B</sub> = 0.5 mA		-	760	-	mV
	voltage	I <sub>C</sub> = 100 mA ; I <sub>B</sub> = 5 mA	[1]	-	900	-	mV
f <sub>T</sub>	transition frequency	V <sub>CE</sub> = 5 V; I <sub>C</sub> = 10 mA; f = 100 MHz		100	-	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = 10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	-	1.5	pF
C <sub>e</sub>	emitter capacitance	V <sub>EB</sub> = 0.5 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	11	-	pF
NF	noise figure	$V_{CE}$ = 5 V; I <sub>C</sub> = 200µA; R <sub>S</sub> = 2 kΩ; f = 1 kHz; B = 200 Hz		-	-	10	dB

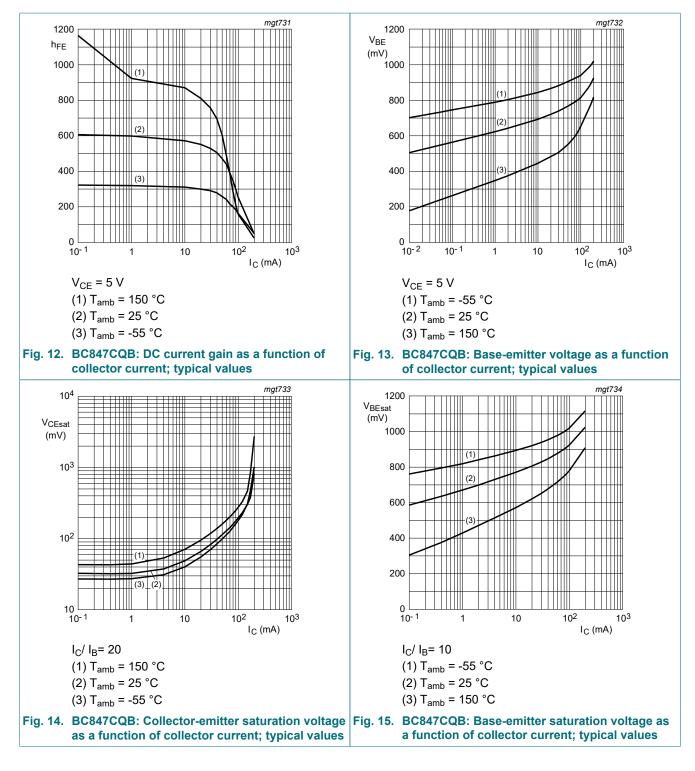
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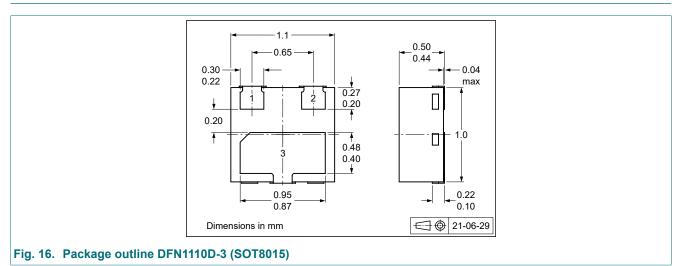


### **11. Test information**

#### **Quality information**

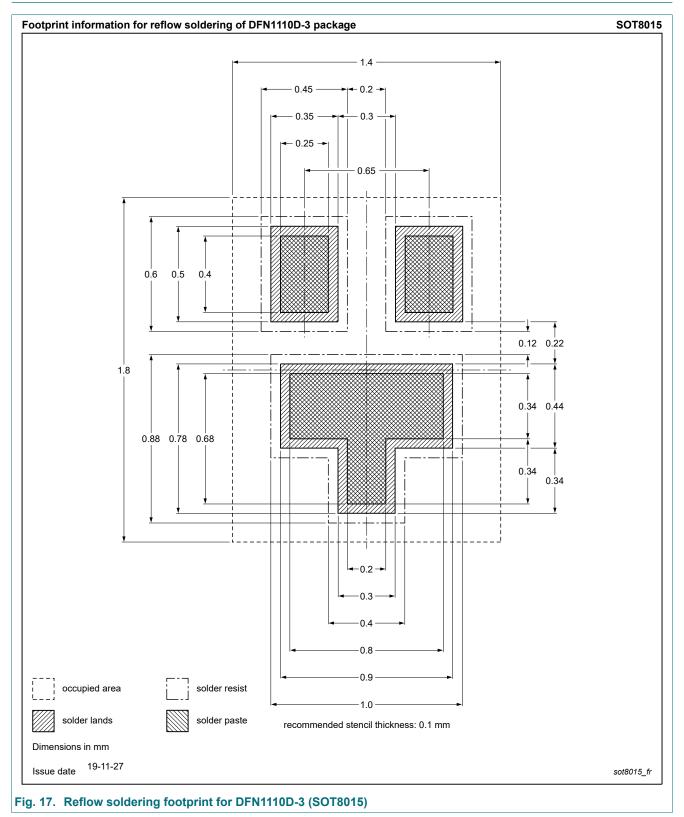
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

### 12. Package outline



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### 13. Soldering



### 14. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
BC847XQB_SER v.2	20210908	BC847XQB_SER v.1					
Modifications:	<ul> <li>Pinning: Simplified outline replaced</li> <li>Characteristics: Conditions corrected at C<sub>e</sub></li> <li>Package outline: Drawing replaced by minimized version</li> </ul>						
BC847XQB_SER v.1	20200124	Product data sheet	-	-			

BC847XQB\_SER

### 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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