Product data sheet

1. General description

NPN/NPN matched double transistor in a SOT363 (SC-88) very small Surface-Mounted Device (SMD) plastic package. The transistors are fully isolated internally.

PNP/PNP complement: BMC857BS Matched version of: BC847BS

2. Features and benefits

- Current gain matching
- Base-emitter voltage matching
- Drop-in replacement for standard double transistors
- AEC-Q101 qualified

3. Applications

- Current mirror
- · Differential amplifier

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor			•		'		
V _{CEO}	collector-emitter voltage	open base		-	-	45	V
I _C	collector current			-	-	100	mA
h _{FE}	DC current gain	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C		200	290	450	
Per device			•		'		
h _{FE1} /h _{FE2}	DC current gain matching	$V_{CE} = 5 \text{ V}; I_{C} = 2 \text{ mA}; T_{amb} = 25 \text{ °C}$	[1]	0.9	1	-	
V _{BE1} -V _{BE2}	base-emitter voltage matching		[2]	-	-	2	mV

- [1] The smaller of the two values is taken as the numerator.
- [2] The smaller of the two values is subtracted from the larger value.



NPN/NPN matched double transistor

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E1	emitter TR1	Пе Пе Пи	C1 B2 E2
2	B1	base TR1	6 5 4	
3	C2	collector TR2		(TR1) TR2)
4	E2	emitter TR2		
5	B2	base TR2		
6	C1	collector TR1	TSSOP6 (SOT363)	sym020

6. Ordering information

Table 3. Ordering information

Type number	Package						
	Name	Description	Version				
BCM847BS		plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	<u>SOT363</u>				

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
BCM847BS	M1%

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit		
Per transisto	Per transistor							
V _{CBO}	collector-base voltage	open emitter		-	50	V		
V _{CEO}	collector-emitter voltage	open base		-	45	V		
V _{EBO}	emitter-base voltage	open collector		-	6	V		
Ic	collector current			-	100	mA		
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	200	mA		
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	200	mW		
Per device	·		·					
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1] [2]	-	300	mW		
Tj	junction temperature			-	150	°C		
T _{amb}	ambient temperature			-65	150	°C		
T _{stg}	storage temperature			-65	150	°C		

^[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

^[2] Reflow soldering is the only recommended soldering method.

NPN/NPN matched double transistor

9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit	
Per transistor	Per transistor							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	625	K/W	
Per device	Per device							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1] [2]	-	-	416	K/W	

^[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

10. Characteristics

Table 7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
Per transistor	•						
I _{CBO}	collector-base cut-off	V _{CB} = 30 V; I _E = 0 A; T _{amb} = 25 °C		-	-	15	nA
	current	$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A}; T_j = 150 ^{\circ}\text{C}$		-	-	5	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A; T _{amb} = 25 °C		-	-	100	nA
h _{FE}	DC current gain	$V_{CE} = 5 \text{ V}; I_{C} = 10 \mu\text{A}; T_{amb} = 25 ^{\circ}\text{C}$		-	250	-	
		V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C		200	290	450	
V _{CEsat}	collector-emitter	I_C = 10 mA; I_B = 0.5 mA; T_{amb} = 25 °C		-	50	200	mV
	saturation voltage	I _C = 100 mA; I _B = 5 mA; T _{amb} = 25 °C		-	200	400	mV
V _{BEsat}	base-emitter saturation	I_C = 10 mA; I_B = 0.5 mA; T_{amb} = 25 °C	[1]	-	760	-	mV
	voltage	I _C = 100 mA; I _B = 5 mA; T _{amb} = 25 °C	[1]	-	910	-	mV
V _{BE}	base-emitter voltage	V _{CE} = 5 V; I _C = 2 mA; T _{amb} = 25 °C	[2]	610	660	710	mV
		V _{CE} = 5 V; I _C = 10 mA; T _{amb} = 25 °C	[2]	-	-	770	mV
C _c	collector capacitance	$V_{CB} = 10 \text{ V}; I_E = 0 \text{ A}; i_e = 0 \text{ A}; f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$		-	-	1.5	pF
C _e	emitter capacitance	$V_{EB} = 0.5 \text{ V}; I_{C} = 0 \text{ A}; i_{c} = 0 \text{ A};$ $f = 1 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$		-	11	-	pF
f _T	transition frequency	$V_{CE} = 5 \text{ V}; I_{C} = 10 \text{ mA}; f = 100 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$		100	250	-	MHz
NF	noise figure	V_{CE} = 5 V; I_{C} = 0.2 mA; R_{S} = 2 k Ω ; f = 10 Hz to 15.7 kHz; T_{amb} = 25 °C		-	2.8	-	dB
		V_{CE} = 5 V; I_{C} = 0.2 mA; R_{S} = 2 k Ω ; f = 1 kHz; B = 200 Hz; T_{amb} = 25 °C		-	3.3	-	dB
Per device	•						,
h _{FE1} /h _{FE2}	DC current gain matching	$V_{CE} = 5 \text{ V}; I_{C} = 2 \text{ mA}; T_{amb} = 25 ^{\circ}\text{C}$	[3]	0.9	1	-	
V _{BE1} -V _{BE2}	base-emitter voltage matching		[4]	-	-	2	mV

^[1] V_{BEsat} decreases by about 1.7 mV/K with increasing temperature.

BCM847BS

^[2] Reflow soldering is the only recommended soldering method.

^[2] V_{BE} decreases by about 2 mV/K with increasing temperature.

^[3] The smaller of the two values is taken as the numerator.

^[4] The smaller of the two values is subtracted from the larger value.

NPN/NPN matched double transistor

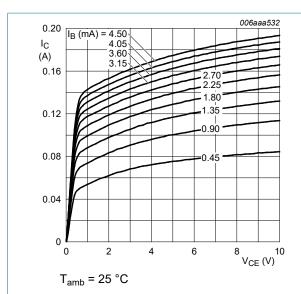
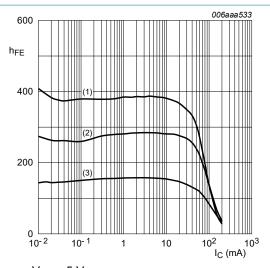


Fig. 1. Collector current as a function of collectoremitter voltage; typical values



V_{CE} = 5 V (1) T_{amb} = 100 °C (2) T_{amb} = 25 °C (3) T_{amb} = -55 °C

Fig. 2. DC current gain as a function of collector current; typical values

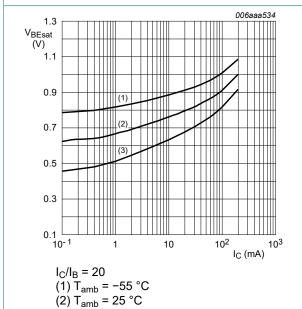
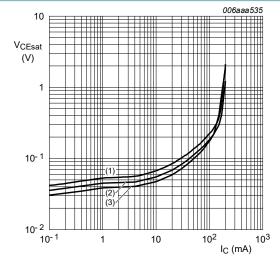


Fig. 3. Base-emitter saturation voltage as a function of collector current; typical values

(3) $T_{amb} = 100 \, ^{\circ}C$



 $I_{\rm C}/I_{\rm B} = 20$ (1) $T_{\rm amb} = 100~{\rm ^{\circ}C}$ (2) $T_{\rm amb} = 25~{\rm ^{\circ}C}$ (3) $T_{\rm amb} = -55~{\rm ^{\circ}C}$

Fig. 4. Collector-emitter saturation voltage as a function of collector current; typical values

NPN/NPN matched double transistor

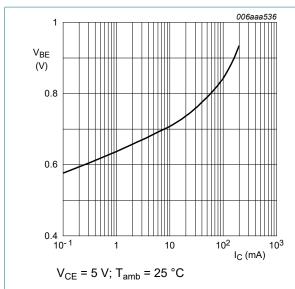


Fig. 5. Base-emitter voltage as a function of collector current; typical values

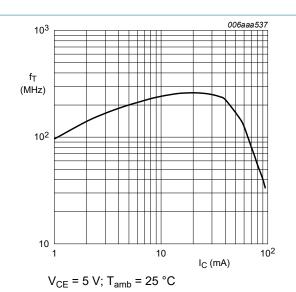


Fig. 6. Transition frequency as a function of collector current; typical values

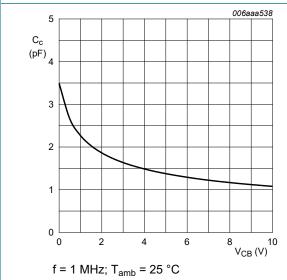
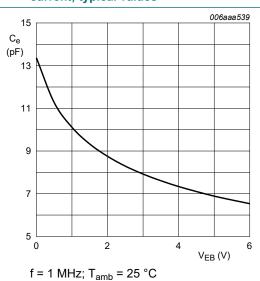
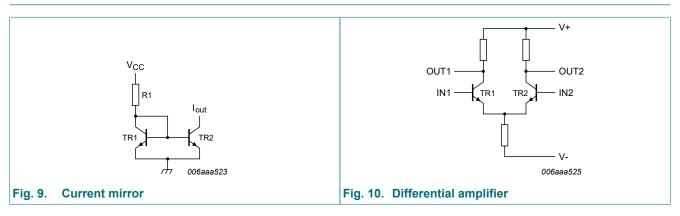


Fig. 7. Collector capacitance as a function of collectorbase voltage; typical values



8. Emitter capacitance as a function of emitterbase voltage; typical values

11. Application information



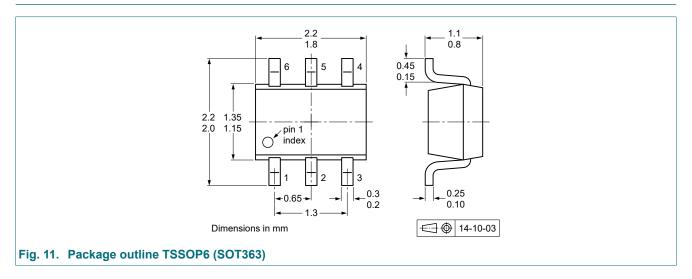
NPN/NPN matched double transistor

12. Test information

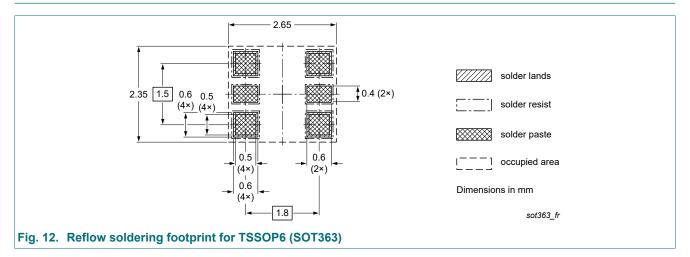
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

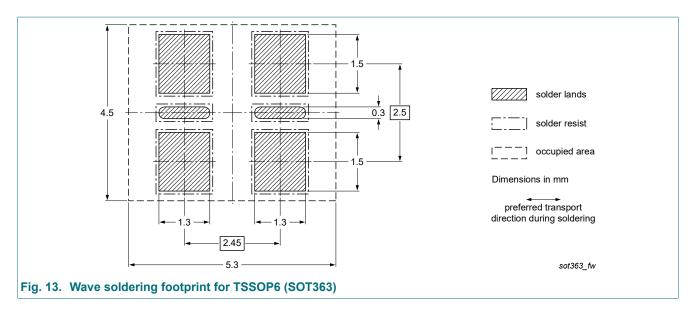
13. Package outline



14. Soldering



NPN/NPN matched double transistor



NPN/NPN matched double transistor

15. Revision history

Table 8. Revision history

Table 6. Revision history							
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes			
BCM847BS v.7	20221227	Product data sheet	-	BCM847BV_BS_DS_6			
Modifications:	Family data sheet spPacking information	litted to single type data removed.	sheets.				
BCM847BV_BS_DS_6		Product data sheet	-	BCM847BV_BS_DS_5			
BCM847BV_BS_DS_5		Product data sheet Product data sheet	-	BCM847BS_DS_4			
BCM847BS_DS_4		Product data sheet	-	BCM847BS_DS_3			
BCM847BS_DS_3		Product data sheet	-	BCM847BS_2			
BCM847BS_2		Product data sheet	-	BCM847BS_1			
BCM847BS_1		Product data sheet	-	-			

NPN/NPN matched double transistor

16. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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