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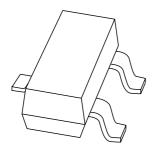
If you have any questions related to the data sheet, please contact our nearest sales office via e-mail or telephone (details via **salesaddresses@nexperia.com**). Thank you for your cooperation and understanding,

Kind regards,

Team Nexperia

DISCRETE SEMICONDUCTORS

DATA SHEET



BCW29; BCW30 PNP general purpose transistors

Product data sheet Supersedes data of 1999 Apr 13 2004 Jan 13



PNP general purpose transistors

BCW29; BCW30

FEATURES

• Low current (max. 100 mA)

• Low voltage (max. 32 V).

APPLICATIONS

• General purpose switching and amplification.

DESCRIPTION

PNP transistor in a SOT23 plastic package. NPN complements: BCW31 and BCW32.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾
BCW29	C1*
BCW30	C2*

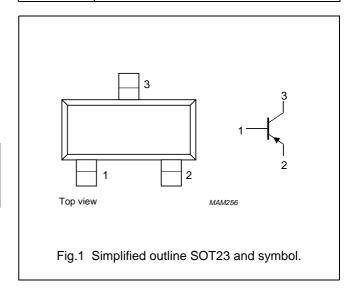
Note

1. * = p: Made in Hong Kong.

* = t : Made in Malaysia. * = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE	PACKAGE						
NUMBER	NAME	DESCRIPTION	VERSION				
BCW29	_	plastic surface mounted package; 3 leads	SOT23				
BCW30							

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-32	V
V _{CEO}	collector-emitter voltage	open base; I _C = −2 mA	_	-32	V
V _{EBO}	emitter-base voltage	open collector	_	-5	V
I _C	collector current (DC)		_	-100	mA
I _{CM}	peak collector current		_	-200	mA
I _{BM}	peak base current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
Tj	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

PNP general purpose transistors

BCW29; BCW30

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 $T_i = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	$I_E = 0$; $V_{CB} = -32 \text{ V}$	_	_	-100	nA
		$I_E = 0$; $V_{CB} = -32 \text{ V}$; $T_j = 100 ^{\circ}\text{C}$	_	_	-10	μΑ
I _{EBO}	emitter cut-off current	$I_C = 0; V_{EB} = -5 \text{ V}$	_	_	-100	nA
h _{FE}	DC current gain	$I_C = -10 \mu A; V_{CE} = -5 V$				
	BCW29		_	90	-	
	BCW30		_	150	_	
	DC current gain	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$				
	BCW29		120	-	260	
	BCW30		215	_	500	
V _{CEsat}	collector-emitter saturation	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	-80	-300	mV
	voltage	$I_C = -50 \text{ mA}; I_B = -2.5 \text{ mA}$	_	-150	_	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = -10 \text{ mA}; I_B = -0.5 \text{ mA}$	_	-720	_	mV
		$I_C = -50 \text{ mA}; I_B = -2.5 \text{ mA}$	_	-810	_	mV
V _{BE}	base-emitter voltage	$I_C = -2 \text{ mA}; V_{CE} = -5 \text{ V}$	-600	_	-750	mV
C _c	collector capacitance	$I_E = I_e = 0$; $V_{CB} = -10 \text{ V}$; $f = 1 \text{ MHz}$	_	4.5	_	pF
f _T	transition frequency	$I_C = -10 \text{ mA}; V_{CE} = -5 \text{ V}; f = 100 \text{ MHz}$	100	_	_	MHz
F	noise figure	$I_{C} = -200 \ \mu A; \ V_{CE} = -5 \ V; \ R_{S} = 2 \ k\Omega; \ f = 1 \ kHz; \ B = 200 \ Hz$	_	_	10	dB

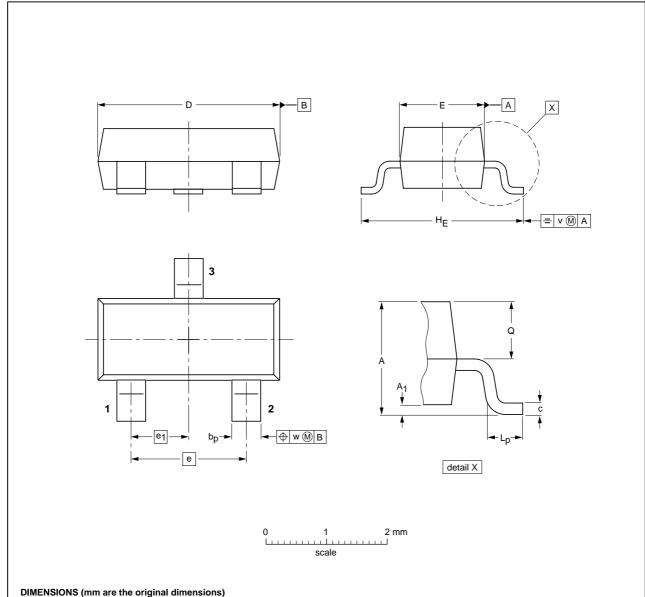
PNP general purpose transistors

BCW29; BCW30

PACKAGE OUTLINE

Plastic surface-mounted package; 3 leads

SOT23



DIMENS	ЮИЗ (П	ım are tı	ne origir	nai dime	nsions)	

UNIT	Α	A ₁ max.	bp	С	D	E	е	e ₁	HE	Lp	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE		REFER	EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-04-11-04 06-03-16

PNP general purpose transistors

BCW29; BCW30

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published
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