Product data sheet

1. General description

NPN medium power transistors in a SOT89 (SC-62) flat lead Surface-Mounted Device (SMD) plastic package.

2. Features and benefits

- High collector current capability I_C and I_{CM}
- Three current gain selections
- · High power dissipation capability
- AEC-Q101 qualified

3. Applications

- Linear voltage regulators
- MOSFET drivers
- Low-side switches
- · Battery-driven devices
- Power management
- Amplifiers

4. Quick reference data

Table 1. Quick reference data

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	45	V
I _C	collector current			-	-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms		-	-	2	А
h _{FE}	DC current gain						
	BCX54	V _{CE} = 2 V; I _C = 150 mA	[1]	63	-	250	
	BCX54-10		[1]	63	-	160	
	BCX54-16		[1]	100	-	250	

[1] pulsed; $t_p \le 300 \ \mu s$; $\delta \le 0.02$



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5. Pinning information

Table 2. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	Е	emitter		C
2	С	collector		в
3	В	base		13
			3 2 1	sym042

6. Ordering information

Table 3. Ordering information

Type number	Package					
	Name	Description	Version			
BCX54	SC-62	plastic surface-mounted package; exposed die pad for good heat	SOT89			
BCX54-10		transfer; 3 leads				
BCX54-16						

7. Marking

Table 4. Marking

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Type number	Marking code					
BCX54	BA					
BCX54-10	BC					
BCX54-16	BD					

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8. Limiting values

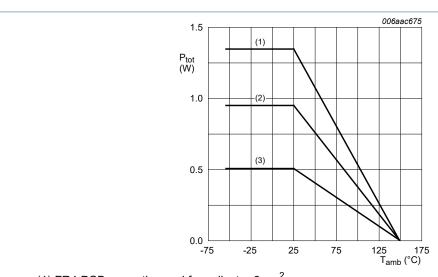
Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Conditions		Max	Unit
V _{CBO}	collector-base voltage	open emitter	open emitter		45	V
V _{CEO}	collector-emitter voltage	open base		-	45	V
V _{EBO}	emitter-base voltage	open collector		-	5	V
I _C	collector current			-	1	А
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	single pulse; t _p ≤ 1 ms		2	А
I _B	base current				0.3	Α
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms	single pulse; t _p ≤ 1 ms		0.3	А
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	0.50	W
			[2]	-	0.95	W
			[3]	-	1.35	W
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C

- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.
- Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm². Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 6 cm².



- (1) FR4 PCB, mounting pad for collector 6 cm²
- (2) FR4 PCB, mounting pad for collector 1 cm²
- (3) FR4 PCB, standard footprint

Fig. 1. Power derating curves SOT89

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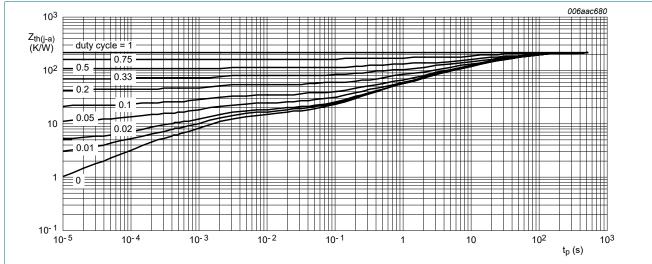
9. Thermal characteristics

Table 6. Thermal characteristics

 T_{amb} = 25 °C unless otherwise specified.

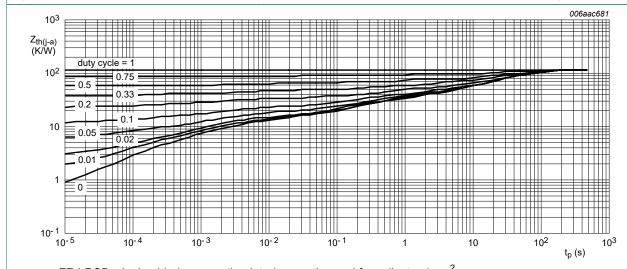
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	[1]	-	-	250	K/W
			[2]			132	K/W
			[3]			93	K/W
R _(j-sp)	thermal resistance from junction to solder point			-	-	16	K/W

- [1] Device mounted on an FR4 PCB; single-sided copper; tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm².
- [3] Device mounted on an FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 6 cm².



FR4 PCB; single-sided copper; tin-plated and standard footprint

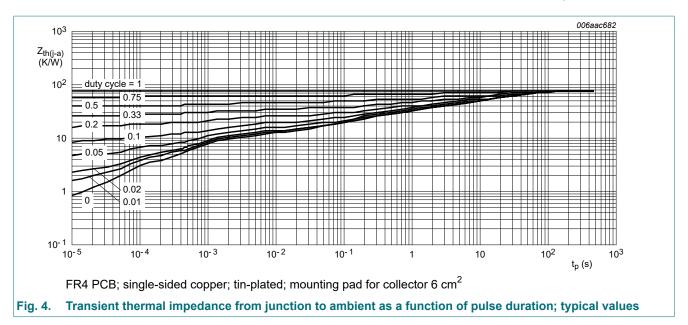
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB; single-sided copper; tin-plated; mounting pad for collector 1 cm²

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

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10. Characteristics

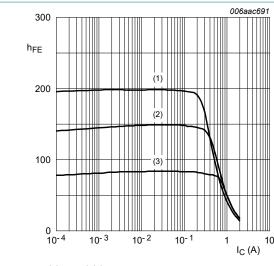
Table 7. Characteristics

 T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 μA; I _E = 0 A		45	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	$I_C = 2 \text{ mA}; I_B = 0 \text{ A}$			-	-	V
V _{(BR)EBO}	emitter-base breakdown voltage	I _E = 100 μA; I _C = 0 A	5	-	-	V	
I _{CBO}	collector-base	V _{CB} = 30 V; I _E = 0 A		-	-	100	nA
	cut-off current	V _{CB} = 30 V; I _E = 0 A; T _j = 150 °C		-	-	10	μΑ
I _{EBO}	emitter-base cut-off current	V _{EB} = 5 V; I _C = 0 A	V _{EB} = 5 V; I _C = 0 A		-	100	nA
h _{FE}	DC current gain		'		'	'	
	BCX54	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA		63	-	250	
		V _{CE} = 2 V; I _C = 500 mA		40	-	-	
	BCX54-10	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA		63	-	160	
		V _{CE} = 2 V; I _C = 500 mA	40		-	-	
	BCX54-16	V _{CE} = 2 V; I _C = 5 mA	[1]	63	-	-	
		V _{CE} = 2 V; I _C = 150 mA		100		250	
		V _{CE} = 2 V; I _C = 500 mA		40	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = 500 mA; I _B = 50 mA	[1]	-	-	0.5	V
V_{BE}	base-emitter voltage	V _{CE} = 2 V; I _C = 500 mA	[1]	-	-	1	V
C _c	collector capacitance	V _{CB} = 10 V; I _E = i _e = 0 A; f = 1 MHz		-	6	-	pF
f _T	transition frequency	V _{CE} = 5 V; I _C = 50 mA; f = 100 MHz		100	180	-	MHz

^[1] pulsed; $t_p \le 300 \ \mu s; \ \delta \le 0.02$

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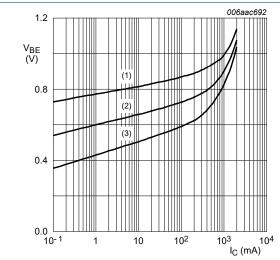


$$V_{CE} = 2 V$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(3)
$$T_{amb} = -55$$
 °C

Fig. 5. DC current gain as a function of collector current; typical values



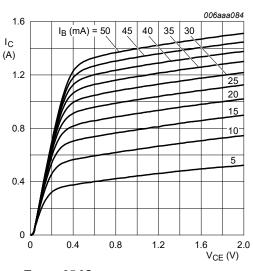
$$V_{CE} = 2 V$$

(1)
$$T_{amb} = -55$$
 °C

(2)
$$T_{amb}$$
 = 25 °C

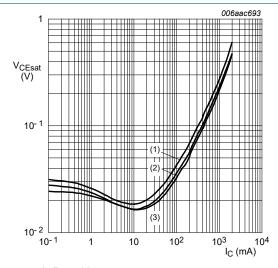
(3)
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



 T_{amb} = 25 °C

Fig. 6. Collector current as a function of collectoremitter voltage; typical values



$$I_{\rm C}/I_{\rm B} = 10$$

(1)
$$T_{amb} = 100 \, ^{\circ}C$$

(2)
$$T_{amb}$$
 = 25 °C

(3)
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

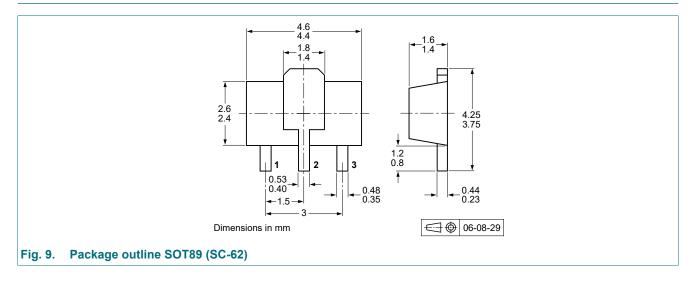
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11. Test information

11.1. Quality information

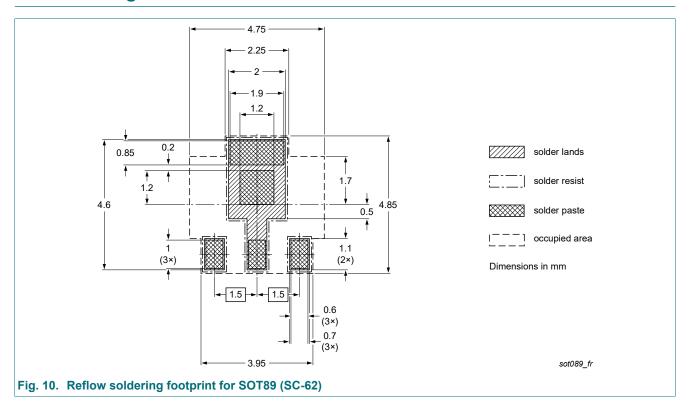
This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

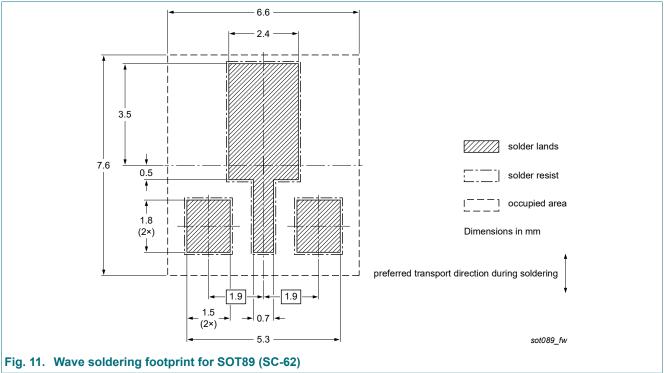
12. Package outline



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13. Soldering





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14. Revision history

Table 8. Revision history

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Document ID	Release date	Data sheet status	Change notice	Supersedes				
BCX54_SER v.9	20220106	Product data sheet	-	BCP54_BCX54_BC54PA v.8				
Modifications:	Data sheet separate	Data sheet separated into 3 data sheets						
BCP54_BCX54_BC54PA v.8	20111021	Product data sheet	-	BC635_BCP54_BCX54 v.7				
BC635_BCP54_BCX54 v.7	20070604	Product data sheet	-	BC635_BCP54_BCX54 v.6				
BC635_BCP54_BCX54 v.6	20050225	Product data sheet	CPCN200405 029	BC635_637_639 v.4 BCP54_55_56 v.5 BCP54_55_56 v.4				
BC635_637_639 v.4	20011010	Product specification	-	BC635_637_639 v.3				
BCX54_55_56 v.5	20030206	Product specification	-	BCX54_55_56 v.4				
BCX54_55_56 v.4	20011010	Product specification	-	BCX54_55_56 v.3				

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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