

BFU550A NPN wideband silicon RF transistor Rev. 1 – 13 January 2014

Product data sheet

1. Product profile

1.1 General description

NPN silicon RF transistor for high speed, low noise applications in a plastic, 3-pin SOT23 package.

The BFU550A is part of the BFU5 family of transistors, suitable for small signal to medium power applications up to 2 GHz.

1.2 Features and benefits

- Low noise, high breakdown RF transistor
- AEC-Q101 qualified
- Minimum noise figure (NF_{min}) = 0.6 dB at 900 MHz
- Maximum stable gain 18 dB at 900 MHz
- 11 GHz f_T silicon technology

1.3 Applications

- Applications requiring high supply voltages and high breakdown voltages
- Broadband amplifiers up to 2 GHz
- Low noise amplifiers for ISM applications
- ISM band oscillators

1.4 Quick reference data

Table 1. Quick reference data

$T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{CB}	collector-base voltage	open emitter		-	-	24	V
V_{CE}	collector-emitter voltage	open base		-	-	12	V
		shorted base		-	-	24	V
V_{EB}	emitter-base voltage	open collector		-	-	2	V
I _C	collector current			-	15	50	mA
P _{tot}	total power dissipation	$T_{sp} \le 87 \ ^{\circ}C$	<u>[1]</u>	-	-	450	mW
h _{FE}	DC current gain	$I_{C} = 15 \text{ mA}; V_{CE} = 8 \text{ V}$		60	95	200	
C _c	collector capacitance	V _{CB} = 8 V; f = 1 MHz		-	0.74	-	pF
f _T	transition frequency	I_{C} = 25 mA; V_{CE} = 8 V; f = 900 MHz		-	11	-	GHz



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Table 1.Quick reference datacontinued $T_{amb} = 25 \ ^{\circ}C$ unless otherwise specified							
Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
G _{p(max)}	maximum power gain	I_{C} = 15 mA; V_{CE} = 8 V; f = 900 MHz	[2] _	18	-	dB	
NF_{min}	minimum noise figure	I_{C} = 1 mA; V_{CE} = 8 V; f = 900 MHz; Γ_{S} = Γ_{opt}	-	0.6	-	dB	
P _{L(1dB)}	output power at 1 dB gain compression	I _C = 25 mA; V _{CE} = 8 V; Z _S = Z _L = 50 Ω; f = 900 MHz	-	13.5	-	dBm	

[1] T_{sp} is the temperature at the solder point of the collector lead.

If K > 1 then $G_{p(max)}$ is the maximum power gain. If K < 1 then $G_{p(max)}$ = MSG. [2]

Pinning information 2.

Table 2.	Discrete pinning		
Pin	Description	Simplified outline Graphic symb	ol
1	base		
2	emitter		;
3	collector		2
_		aaa-010458	ļ

Ordering information 3.

Table 3. **Ordering information**

Type number	nber Package		
	Name Description		Version
BFU550A	-	plastic surface-mounted package; 3 leads	SOT23
OM7961	-	Customer evaluation kit for BFU520A, BFU530A and BFU550A [1]	-

[1] The customer evaluation kit contains the following:

- a) Unpopulated RF amplifier Printed-Circuit Board (PCB)
- b) Unpopulated RF amplifier Printed-Circuit Board (PCB) with emitter degeneration
- c) Four SMA connectors for fitting unpopulated Printed-Circuit Board (PCB)
- d) BFU520A, BFU530A and BFU550A samples
- e) USB stick with data sheets, application notes, models, S-parameter and noise files

4. Marking

Table 4. Marking				
Type number	Marking	Description		
BFU550A	HW*	* = t : made in Malaysia		
		* = w : made in China		

5. Design support

Table 5.Available design supportDownload from the BFU550A product information	ation page on <u>h</u>	ttp://www.nxp.com.
Support item	Available	Remarks
Device models for Agilent EEsof EDA ADS	yes	Based on Mextram device model.
SPICE model	yes	Based on Gummel-Poon device model.
S-parameters	yes	
Noise parameters	yes	
Customer evaluation kit	yes	See Section 3 and Section 10.
Solder pattern	yes	
Application notes	yes	See Section 10.1 and Section 10.2.

6. Limiting values

Table 6.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V _{CB}	collector-base voltage	open emitter	-	30	V
V_{CE}	collector-emitter voltage	open base	-	16	V
		shorted base	-	30	V
V_{EB}	emitter-base voltage	open collector	-	3	V
I _C	collector current		-	80	mA
T _{stg}	storage temperature		-65	+150	°C
V _{ESD}	electrostatic discharge voltage	Human Body Model (HBM) According to JEDEC standard 22-A114E	-	±150	V
		Charged Device Model (CDM) According to JEDEC standard 22-C101B	-	±2	kV

7. Recommended operating conditions

Table 7.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{CB}	collector-base voltage	open emitter	-	-	24	V
V _{CE}	collector-emitter voltage	open base	-	-	12	V
		shorted base	-	-	24	V
V_{EB}	emitter-base voltage	open collector	-	-	2	V
I _C	collector current		-	-	50	mA
Pi	input power	Z _S = 50 Ω	-	-	10	dBm
Tj	junction temperature		-40	-	+150	°C
P _{tot}	total power dissipation	$T_{sp} \le 87 \ ^{\circ}C$	<u>[1]</u> _	-	450	mW

[1] T_{sp} is the temperature at the solder point of the collector lead.

8. Thermal characteristics

Table 8.	Thermal characteristics			
Symbol	Parameter	Conditions	Тур	Unit
$R_{th(j-sp)}$	thermal resistance from junction to solder point		<u>[1]</u> 140	K/W

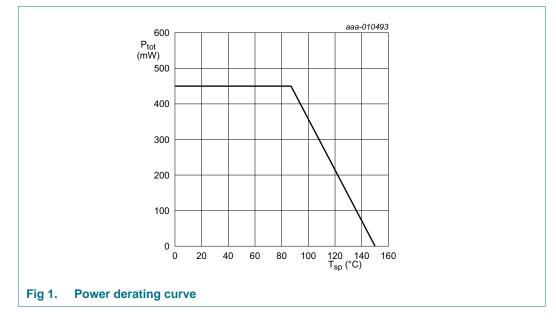
[1] T_{sp} is the temperature at the solder point of the collector lead.

 T_{sp} has the following relation to the ambient temperature $T_{amb}\!\!:$

 $T_{sp} = T_{amb} + P \times R_{th(sp-a)}$

With P being the power dissipation and $R_{th(sp-a)}$ being the thermal resistance between the solder point and ambient. $R_{th(sp-a)}$ is determined by the heat transfer properties in the application.

The heat transfer properties are set by the application board materials, the board layout and the environment e.g. housing.



9. Characteristics

Table 9. Characteristics

 $T_{amb} = 25$ °C unless otherwise specified

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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{(BR)CBO}	collector-base breakdown voltage	I _C = 100 nA; I _E = 0 mA	24	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	$I_{C} = 150 \text{ nA}; I_{B} = 0 \text{ mA}$	12	-	-	V
I _C	collector current		-	15	50	mA
I _{CBO}	collector-base cut-off current	$I_{E} = 0 \text{ mA}; V_{CB} = 8 \text{ V}$	-	<1	-	nA
h _{FE}	DC current gain	$I_{C} = 15 \text{ mA}; V_{CE} = 8 \text{ V}$	60	95	200	
Ce	emitter capacitance	V _{EB} = 0.5 V; f = 1 MHz	-	0.98	-	pF
C _{re}	feedback capacitance	$V_{CE} = 8 V; f = 1 MHz$	-	0.48	-	pF
Cc	collector capacitance	$V_{CB} = 8 V; f = 1 MHz$	-	0.74	-	pF
f _T	transition frequency	$I_{C} = 25 \text{ mA}; V_{CE} = 8 \text{ V}; \text{ f} = 900 \text{ MHz}$	-	11	-	GHz

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Symbol	Parameter	Conditions		Min	Тур	Max	Unit
G _{p(max)}	maximum power gain	f = 433 MHz; V _{CE} = 8 V	<u>[1]</u>				
		I _C = 1 mA		-	15	-	dB
		I _C = 15 mA		-	23.5	-	dB
		I _C = 25 mA		-	24	-	dB
		f = 900 MHz; V _{CE} = 8 V	<u>[1]</u>				
		I _C = 1 mA		-	12.5	-	dB
		I _C = 15 mA		-	18	-	dB
		I _C = 25 mA		-	18	-	dB
		f = 1800 MHz; V _{CE} = 8 V	<u>[1]</u>				
		I _C = 1 mA		-	10.5	-	dB
		I _C = 15 mA		-	12	-	dB
		I _C = 25 mA		-	12	-	dB
s ₂₁ ²	insertion power gain	f = 433 MHz; V _{CE} = 8 V					
		I _C = 1 mA		-	10	-	dB
		I _C = 15 mA		-	21	-	dB
		I _C = 25 mA		-	21.5	-	dB
		f = 900 MHz; V _{CE} = 8 V					
		I _C = 1 mA		-	8.5	-	dB
		I _C = 15 mA		-	15.5	-	dB
		I _C = 25 mA		-	15.5	-	dB
		f = 1800 MHz; V _{CE} = 8 V					
		I _C = 1 mA		-	5	-	dB
		I _C = 15 mA		-	10	-	dB
		I _C = 25 mA		-	10	-	dB
NF _{min}	minimum noise figure	f = 433 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}					
		I _C = 1 mA		-	0.5	-	dB
		I _C = 15 mA		-	0.9	-	dB
		I _C = 25 mA		-	1.2	-	dB
		f = 900 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}					
		I _C = 1 mA		-	0.6	-	dB
		I _C = 15 mA		-	1.0	-	dB
		I _C = 25 mA		-	1.3	-	dB
		f = 1800 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}					
		$I_{\rm C} = 1 \rm{mA}$		-	0.7	-	dB
		I _C = 15 mA		-	1.1	-	dB
		$I_{\rm C} = 25 \rm mA$		-	1.4	-	dB

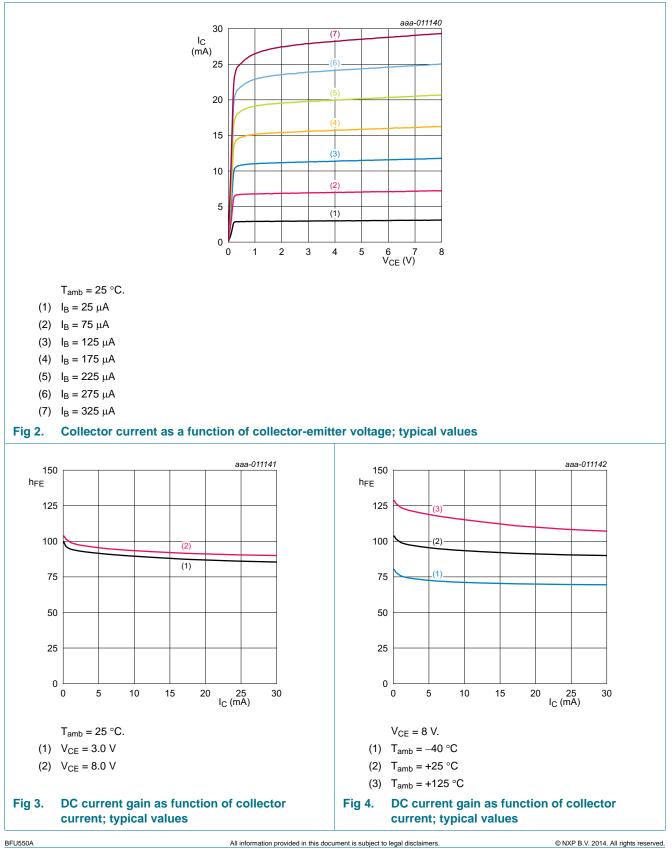
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
G _{ass}	associated gain	f = 433 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	22	-	dB
		I _C = 15 mA	-	22	-	dB
		I _C = 25 mA	-	22	-	dB
		f = 900 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	14.5	-	dB
		I _C = 15 mA	-	16	-	dB
		I _C = 25 mA	-	16	-	dB
		f = 1800 MHz; V_{CE} = 8 V; Γ_{S} = Γ_{opt}				
		I _C = 1 mA	-	8	-	dB
		I _C = 15 mA	-	10.5	-	dB
		I _C = 25 mA	-	10	-	dB
P _{L(1dB)}	output power at 1 dB gain compression	f = 433 MHz; V _{CE} = 8 V; Z _S = Z _L = 50 Ω				
		I _C = 15 mA	-	10	-	dBm
		I _C = 25 mA	-	13	-	dBm
		f = 900 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω				
		I _C = 15 mA	-	10.5	-	dBm
		I _C = 25 mA	-	13.5	-	dBm
		f = 1800 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω				
		I _C = 15 mA	-	11	-	dBm
		I _C = 25 mA	-	13	-	dBm
IP3 _o	output third-order intercept point	f_{1} = 433 MHz; f_{2} = 434 MHz; V_{CE} = 8 V; Z_{S} = Z_{L} = 50 Ω				
		I _C = 15 mA	-	20	-	dBm
		I _C = 25 mA	-	23	-	dBm
		f_1 = 900 MHz; f_2 = 901 MHz; V_{CE} = 8 V; Z_S = Z_L = 50 Ω				
		I _C = 15 mA	-	20	-	dBm
		I _C = 25 mA	-	23	-	dBm
		f_1 = 1800 MHz; f_2 = 1801 MHz; V _{CE} = 8 V; Z _S = Z _L = 50 Ω				
		I _C = 15 mA	-	21	-	dBm
		I _C = 25 mA	-	23	-	dBm

[1] If K > 1 then $G_{p(max)}$ is the maximum power gain. If K < 1 then $G_{p(max)}$ = MSG.

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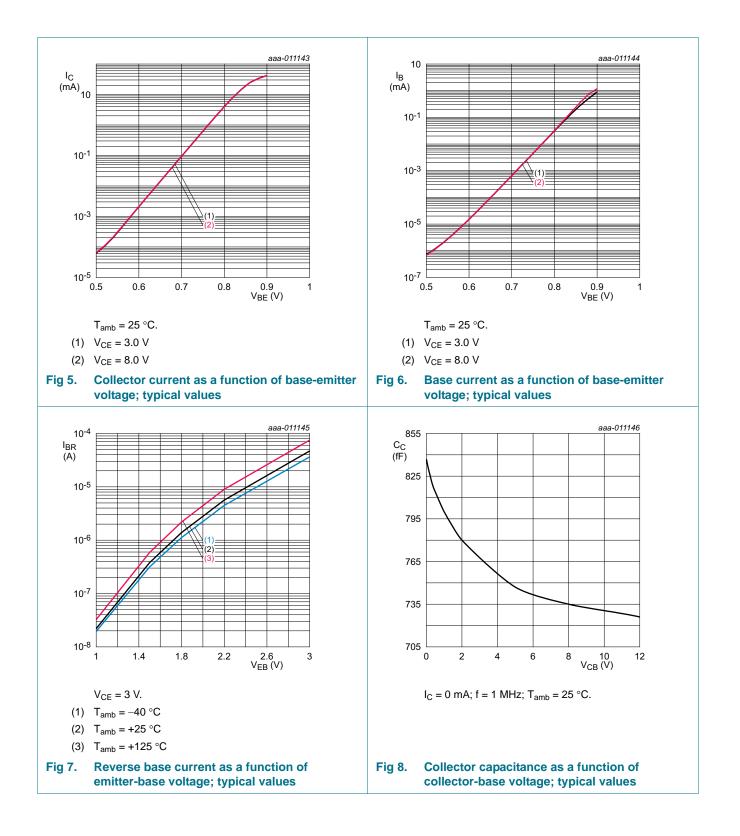
9.1 Graphs



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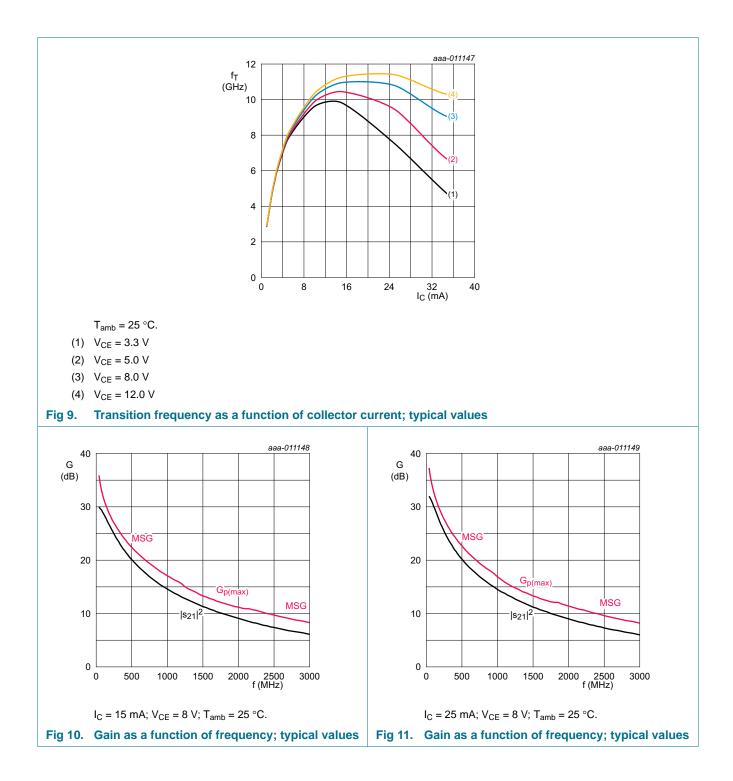
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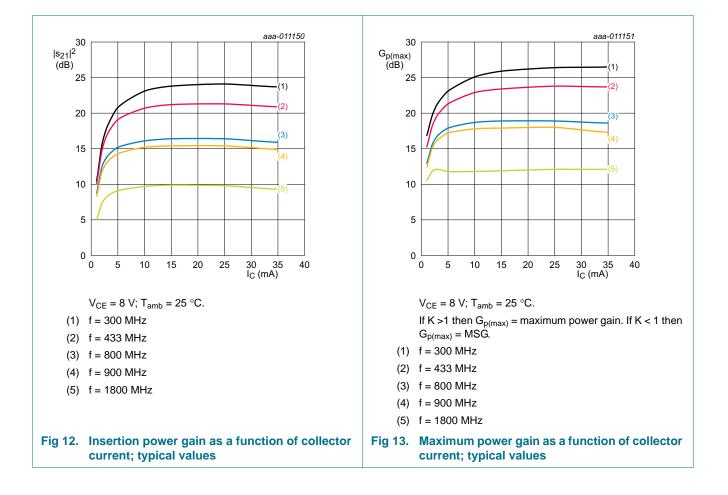
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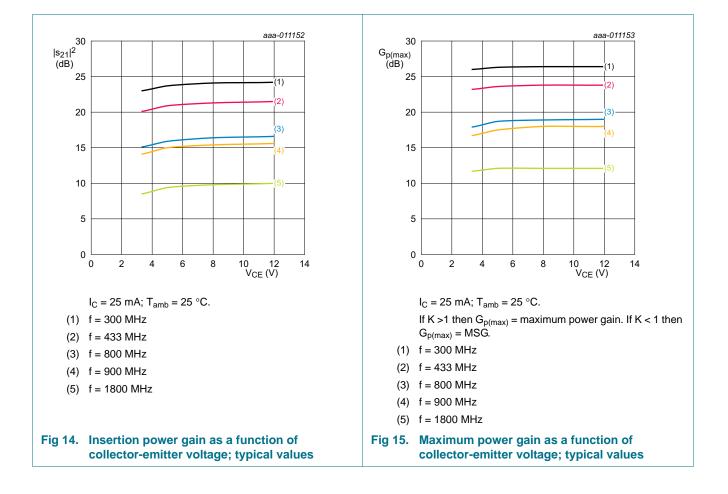
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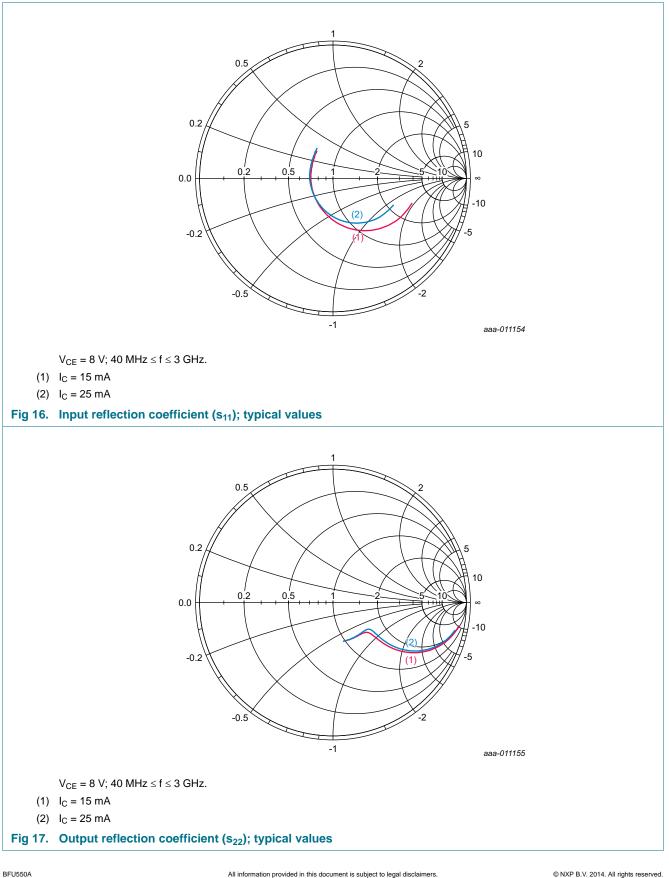
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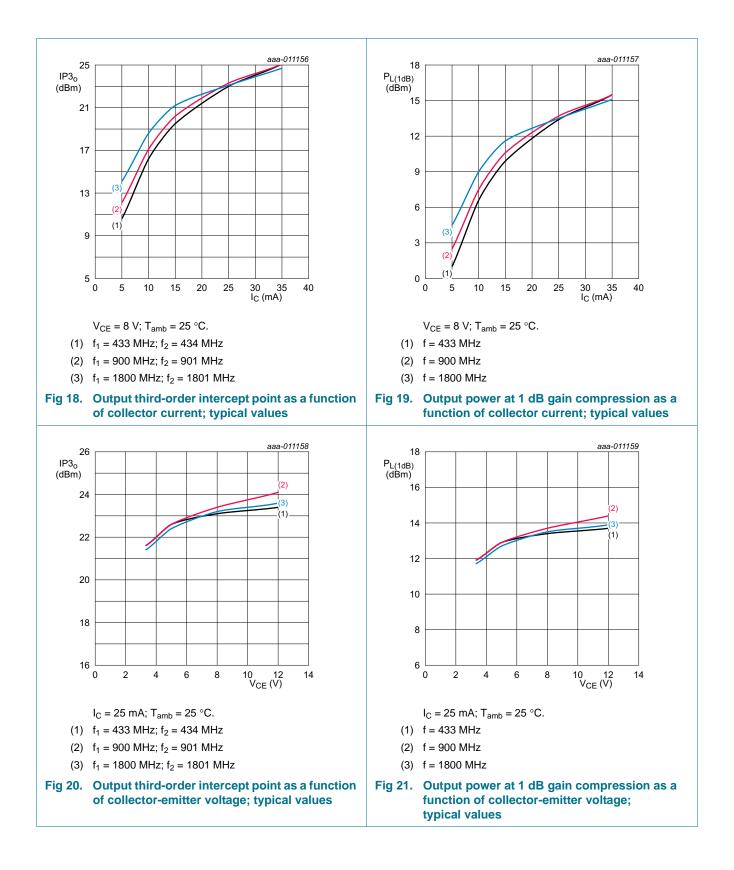
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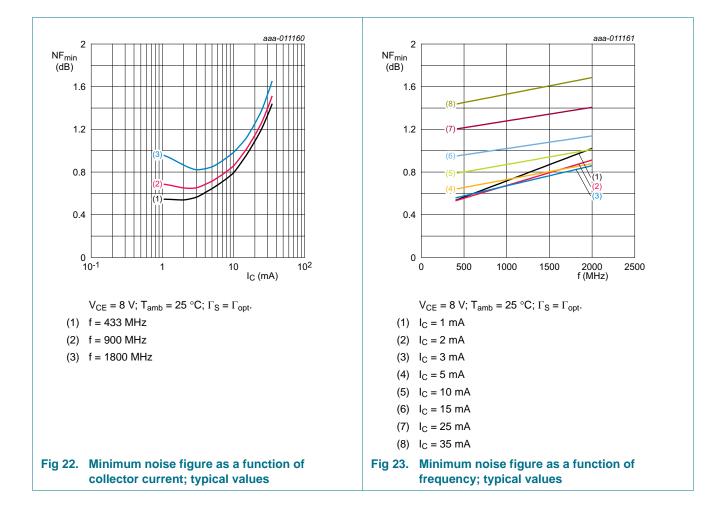
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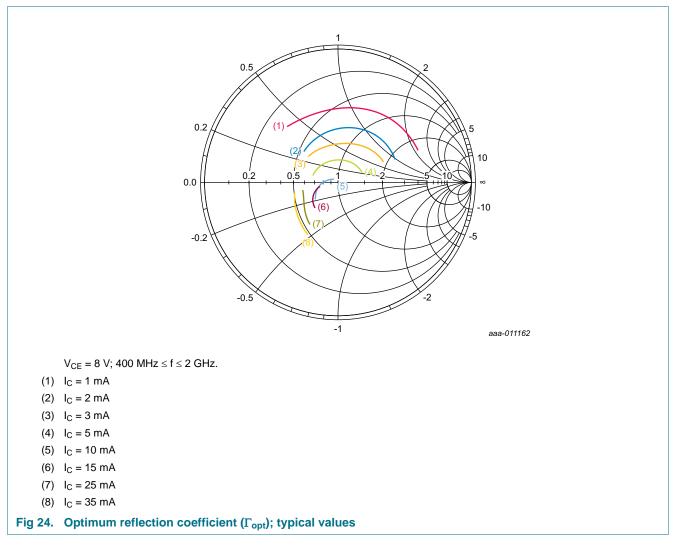
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NPN wideband silicon RF transistor



NPN wideband silicon RF transistor



10. Application information

More information about the following application example can be found in the application notes. See <u>Section 5 "Design support</u>".

The following application example can be implemented using the evaluation kit. See Section 3 "Ordering information" for the order type number.

The following application example can be simulated using the simulation package. See <u>Section 5 "Design support</u>".

10.1 Application example: 433 ISM band LNA

433 ISM band LNA, optimized for low noise.

More detailed information of the application example can be found in the application note: *AN11381*.

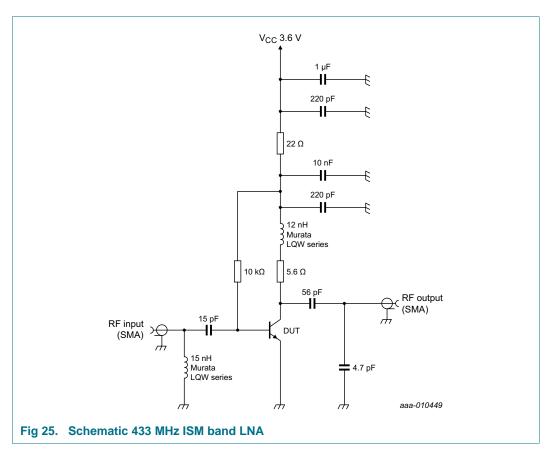


Table 10.Application performance data at 433 MHz $I_{CC} = 20 mA$; $V_{CC} = 3.6 V$

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$ s_{21} ^2$	insertion power gain		-	19	-	dB
NF	noise figure		-	1.3	-	dB
IP3 _o	output third-order intercept point	$f_1 = 433.1$ MHz; $f_2 = 433.2$ MHz; $P_i = -30$ dBm per carrier	-	19	-	dBm

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10.2 Application example: 866 ISM band LNA

866 ISM band LNA, optimized for low noise.

More detailed information of the application example can be found in the application note: *AN11382*.

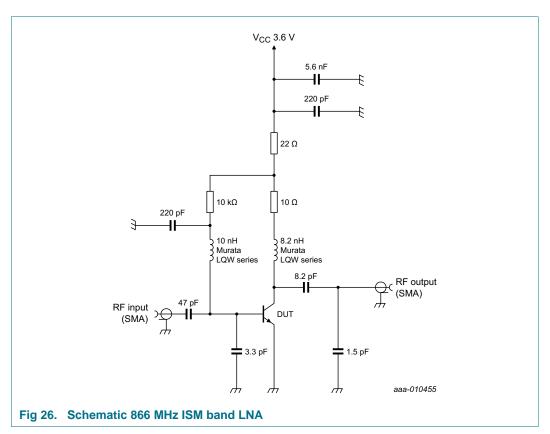


Table 11. Application performance data at 866 MHz

$I_{CC} = 20$	mA; V	$/_{CC} = 3$	8.6 V
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Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$ s_{21} ^2$	insertion power gain		-	13	-	dB
NF	noise figure		-	1.4	-	dB
IP3 _o	output third-order intercept point	$\label{eq:hardward} \begin{array}{l} f_1 = 866.1 \text{ MHz}; \ f_2 = 866.2 \text{ MHz}; \\ P_i = -30 \text{ dBm per carrier} \end{array}$	-	19	-	dBm

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11. Package outline

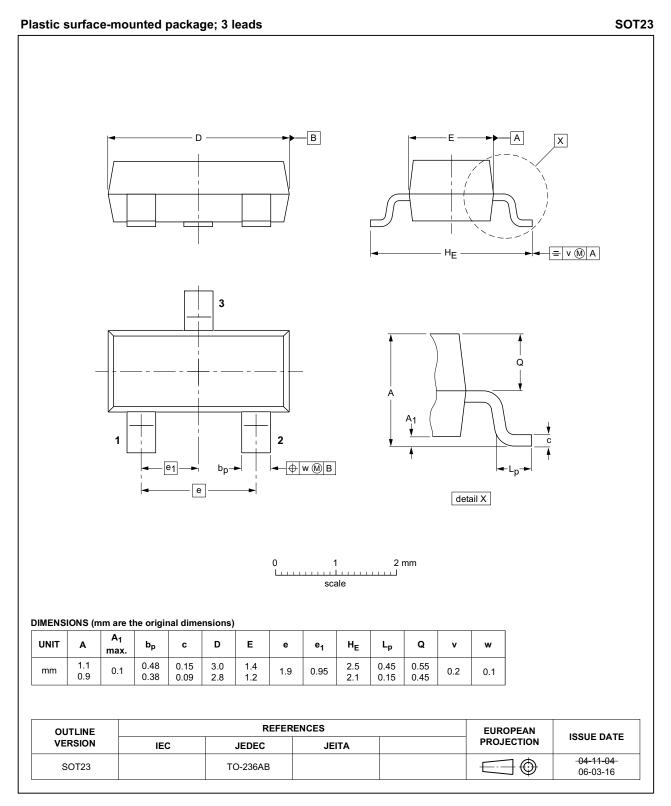


Fig 27. Package outline SOT23

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12. Handling information

CAUTION



This device is sensitive to ElectroStatic Discharge (ESD). Observe precautions for handling electrostatic sensitive devices.

Such precautions are described in the ANSI/ESD S20.20, IEC/ST 61340-5, JESD625-A or equivalent standards.

13. Abbreviations

Acronym	Description
AEC	Automotive Electronics Council
ISM	Industrial, Scientific and Medical
LNA	Low-Noise Amplifier
MSG	Maximum Stable Gain
NPN	Negative-Positive-Negative
SMA	SubMiniature version A

14. Revision history

Table 13. Revision hist	ory			
Document ID	Release date	Data sheet status	Change notice	Supersedes
BFU550A v.1	20140113	Product data sheet	-	-

15. Legal information

15.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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