What's Included

- XDS560v2 System Trace Emulator Pod
- System Trace Coax JTAG Cable
- MIPI 60-pin Buffer board
- Pin Converters (4): TI14, cTI20, ARM20 and TI60 targets
- USB Cable
- Quick Start Guide
- CDROM
- Warranty and Product Registration Information
- PoE Injector power supply and wall cords (with optional PoE Kit)
- Ethernet Cables (2) (with optional PoE Kit)

Blackhawk XDS560v2

JTAG Emulator with System Trace for Texas Instruments

Features

- Supports Code Composer Studio v4.2 or later and all Ctools-enabled devices
- Flexible 12" JTAG cable and buffer board with native 60-pin MIPI HSPT target connection
- Includes 20-pin compact TI (cTI), 14-pin standard TI, 20-pin ARM, and TI 60-pin trace pin converters
- Supports JTAG 1149.1 and 1149.7
- Auto-adaptive Test Clock (TCLK) up to 50MHz
- Auto-sensing target voltage range from 1.2v to 4.1v
- JTAG debug isolation mode, TCLK loop-back and boot-mode pins supported
- Supports 1-4 pin System Trace with 128 Mbytes of System Trace Buffer
- Up to 100MHz export clock compliant to MIPI STP
- Auto-compensating calibration for edge jitter, channel skew, and duty cycle
- Adaptive receiver for setup/hold times up to 1.5ns
- High-Speed USB 2.0 port and 10/100 Mbit Power over Ethernet (PoE) interfaces
- Supports Mobile Industry Processor Interface (MIPI)
- Supports System Trace Protocol (STP) and High-speed Parallel Trace (HSPT)
- Ten (10) status and activity LEDs

Supported Texas Instruments Devices

- OMAP™ Application Processors
- Sitara™
- Stellaris™
- DaVinci™ Video Processors
- C6000™ High Performance DSPs
- C6000™ Multi-Core DSPs
- C5000™ Low Power DSPs
- C28xxx Digital Control Processors
- ARM® Cortex™ A / R / M Microprocessors
- ARM® 9 / 11 Microprocessors
- 66AK2x multicore DSP + Arm® Processors
- Many More

TEXAS INSTRUMENTS
Design Network

The **Blackhawk XDS560v2 System Trace Emulator** (BH560v2) is based on the Texas Instruments XDS560v2 JTAG emulator reference design (XDS560v2). The XDS560v2 design is the next-generation of the high-performance XDS560-class technology that was first made available by Blackhawk with the USB560/LAN560 and XDS560 Trace.

The BH560v2 design includes a bus-powered, high-speed USB 2.0 host interface, 10/100 Mbit Power over Ethernet connection, state and activity LEDs, and a flexible ribbonized coax JTAG cable. The BH560v2 adds support for IEEE1149.7 and the system trace module (STM), an interface on TI heterogeneous multi-core (ARM + DSP) devices.

The JTAG cable comes with a buffer board that includes a native MIPI 60-pin connection and the unit ships with four (4) pin converters for connection to target boards with 14-pin TI, 20-pin compact TI, 20-pin ARM and 60-pin TI trace headers. Blackhawk offers a comprehensive debug interface utility, Bh560v2Config, that users can use to search for, setup, and test the BH560v2. The Bh560v2Config utility works with all Blackhawk XDS560v2 models via USB and Ethernet on Windows an Linux platforms.

Blackhawk Advanced JTAG Emulators are available from a worldwide network of industry resellers and distributors.

Ordering Information

Blackhawk Part Numbers:

BH-XDS-560v2-BP
BH-XDS-560v2-PoE (includes PoE Kit)

For more information, or to order this product online, please visit our website at www.blackhawk-dsp.com

Learn More: For more information about Blackhawk products, please visit **www.blackhawk-dsp.com**