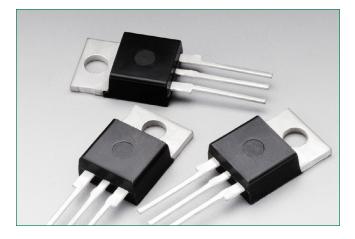
Thyristors Datasheet

Po

BTB08-600BW3G, BTB08-800BW3G Through Hole Radial – 800V



Additional Information







Resources

Accessories

Samples

Description

The BTB08 is designed for high performance full-wave AC control applications where high noise immunity and high commutating di/ dt are required.

Features

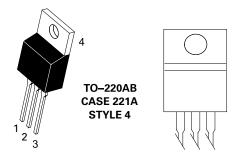
- Blocking Voltage to 800 V
- On-State Current Rating of 8 Amperes RMS at 25°C
- Uniform Gate Trigger Currents in Three Quadrants
- High Immunity to dV/dt 2000 V/µs minimum at 125°C

Functional Diagram

- Minimizes Snubber Networks for Protection
- Industry Standard TO-220AB Package
- High Commutating dl/dt 4 A/ms minimum at 125°C
- These are Pb-Free Devices



Pin Out





Maximum Ratings (TJ = 25°C unless otherwise noted)

Rating		Symbol	Value	Unit
Peak Repetitive Off-State Voltage (Note 1) (Gate Open, Sine Wave 50 to 60 Hz, $T_{J} = -40^{\circ}$ to 125°C)	BTB08-600BW3G BTB08-800BW3G	V _{drm} , V _{rrm}	600 800	V
On-State RMS Current (Full Cycle Sine Wave, 60 Hz, $T_c = 80$	0°C)	I _{T (RMS)}	18	А
Peak Non-Repetitive Surge Current (One Full Cycle Sine Wave, 60 Hz, T _c = 25°C)		I _{TSM}	90	А
Circuit Fusing Consideration (t = 10 ms)		l²t	36	A ² sec
Non-Repetitive Surge Peak Off-State Voltage ($T_1 = 25^{\circ}C$, t = 10 ms)		$V_{\rm DSM}/V_{\rm RSM}$	V _{DSM} /V _{RSM} +100	V
Peak Gate Current (T _j = 125°C, t = 20ms)		I _{GM}	4.0	W
Peak Gate Power (Pulse Width \leq 1.0 µs, T _c = 80°C)		P _{GM}	20	W
Average Gate Power ($T_{J} = 125^{\circ}C$)		P _{G(AV)}	1.0	W
Operating Junction Temperature Range		TJ	-40 to +125	°C
Storage Temperature Range		T _{stg}	-40 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

1. V_{point} and V_{point} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

Thermal Characteristics

Rating		Symbol	Value	Unit
Thermal Resistance,	Junction-to-Case (AC) Junction-to-Ambient	R _{ejc} R _{eja}	2.5 60	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 10 seconds		TL	260	°C

Electrical Characteristics - OFF ($TJ = 25^{\circ}C$ unless otherwise noted ; Electricals apply in both directions)

Characteristic		Symbol	Min	Тур	Мах	Unit
Peak Repetitive Blocking Current	T ₁ = 25°C	I _{DRM} ,	-	-	0.005	mA
$(V_{D} = V_{DRM} = V_{RRM}; \text{ Gate Open})$	T _ = 125°C	I	-	-	1.0	ША

Electrical Characteristics - ON ($TJ = 25^{\circ}C$ unless otherwise noted; Electricals apply in both directions)

Characteristic		Symbol	Min	Тур	Max	Unit
Forward On-State Voltage (Note 2) ($I_{TM} = \pm 11 \text{ A Peak}$)		V _{TM}	-	-	1.55	V
	MT2(+), G(+)		2.5	-	50	
Gate Trigger Current (Continuous dc) (V $_{\rm D}$ = 12 V, R $_{\rm L}$ = 30 Ω)	MT2(+), G(-)	I _{gt}	2.5	-	50	mA
	MT2(-), G(-)		2.5	-	50	
Holding Current ($V_{D} = 12 V$, Gate Open, Initiating Current = ±100 mA)		I _H	-	-	50	mA
	MT2(+), G(+)	I _L	_	-	70	mA
Latching Current ($V_p = 24 \text{ V}, I_g = 60 \text{ mA}$)	MT2(+), G(-)		_	-	90	
	MT2(-), G(-)		-	-	70	
	MT2(+), G(+)	V _{GT}	0.5	-	1.7	V
Gate Trigger Voltage ($V_D = 12 \text{ V}, \text{ R}_1 = 30 \Omega$)	MT2(+), G(-)		0.5	-	1.1	
	MT2(-), G(-)	-	0.5	-	1.1	
	MT2(+), G(+)	V _{gd}	0.2	-	_	
Gate Non–Trigger Voltage ($T_{J} = 125^{\circ}C$)	MT2(+), G(-)		0.2	-	_	V
	MT2(-), G(-)		0.2	_	_	

2. Indicates Pulse Test: Pulse Width \leq 2.0 ms, Duty Cycle \leq 2%.



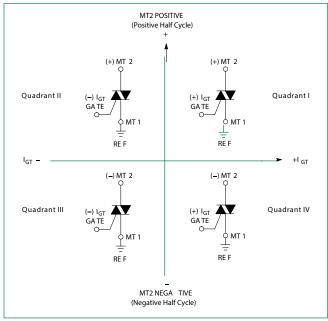
Dynamic Characteristics

Characteristic	Symbol	Min	Тур	Max	Unit
Rate of Change of Commutating Current, See Figure 10. (Gate Open, T _J = 125°C, No Snubber)	(dl/dt)c	4.0	-	-	A/ms
Critical Rate of Rise of On–State Current ($T_J = 125^{\circ}C$, f = 120 Hz, $I_G = 2 \times I_{GT}$, tr $\leq 100 \text{ ns}$)	dl/dt	-	_	50	A/µs
Critical Rate of Rise of Off-State Voltage ($V_D = 0.66 \times V_{DRM}$, Exponential Waveform, Gate Open, $T_J = 125^{\circ}$ C)	dV/dt	2000	-	-	V/µs

Voltage Current Characteristic of SCR

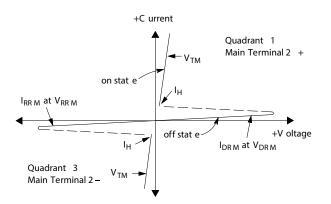
Symbol	Parameter
V _{DRM}	Peak Repetitive Forward Off State Voltage
I _{DRM}	Peak Forward Blocking Current
V _{RRM}	Peak Repetitive Reverse Off State Voltage
I _{BRM}	Peak Reverse Blocking Current
V _{TM}	Maximum On State Voltage
I _H	Holding Current

Quadrant Definitions for a Triac



All polarities are referenced to MT1.

With in-phase signals (using standard AC lines) quadrants I and III are used





BTB08-600BW3G, BTB08-800BW3G Through Hole Radial – 800V

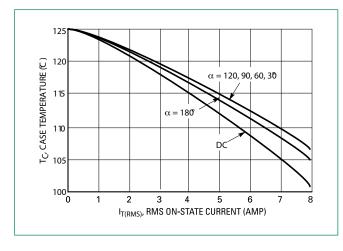
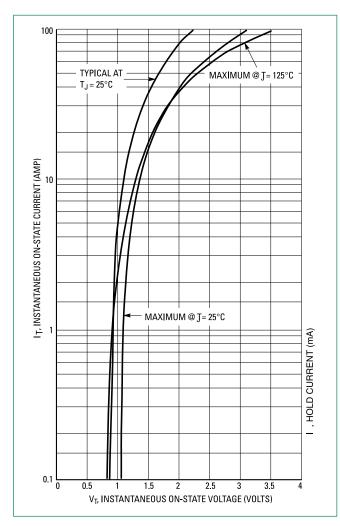


Figure 1. RMS Current Derating

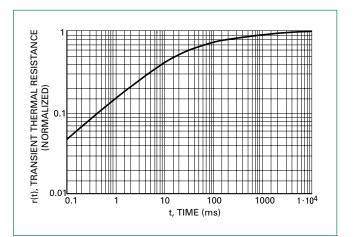




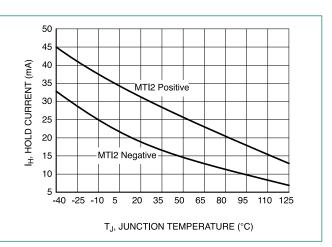
12 DC P_{AV} AVERAGE POWER (WATTS) 10 180% 8 120° 60° 2 90° = 30 2 0 3 4 5 7 8 2 6 0 I_{T(RMS)}, ON-STATE CURRENT (AMP)

Figure 2. On-State Power Dissipation

Figure 4. Thermal Response







BTB08-600BW3G, BTB08-800BW3G Through Hole Radial – 800V

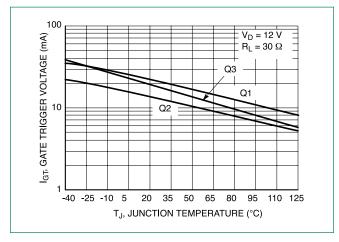


Figure 6. Typical Gate Trigger Current Variation

Figure 8. Critical Rate of Rise of Off-State Voltage (Exponential Waveform)

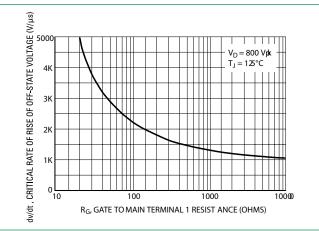


Figure 7. Typical Gate Trigger Voltage Variation

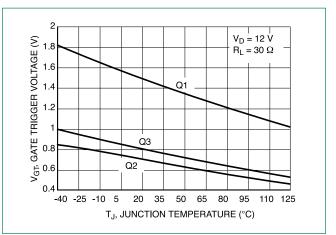
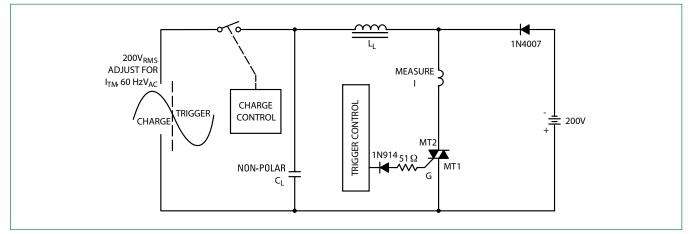


Figure 9. Simplified Test Circuit to Measure the Critical Rate of Rise of Commutating Current (di/dt)



Note: Component values are for verification of rated (di/dt)c. See AN1048 for additional information

