

# Metal Composite Power Inductor (Thin Film) Specification Sheet



# CIGT201610EH1R0MNE (2016 / EIA 0806)

#### APPLICATION

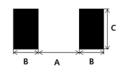
Smart phones, Tablet, Wearable devices, Power converter modules, etc.



# **FEATURES**

Small power inductor for mobile devices
Low DCR structure and high efficiency inductor for power circuits.
Monolithic structure for high reliability
Free of all RoHS-regulated substances
Halogen free

# RECOMMENDED LAND PATTERN



	Unit : mm
TYPE	2016
Α	0.8
В	0.8
С	1.8

#### DIMENSION



TYPE	Dimension [mm]				
1111	L	W	Т	D	
2016	2.0±0.2	1.6±0.2	1.0 max	0.5±0.2	

#### DESCRIPTION

Part no.	Size	Thickness	ss Inductance Inductance tolerance DC Resistance [m\Omega]		Rated DC Current (Isat) [A] Rated DC Current (Irms) [A			rrent (Irms) [A]		
Fait iio.	[inch/mm] [mm] (ma	[mm] (max)	[uH]	(%)	Max.	Тур.	Max.	Тур.	Max.	Тур.
CIGT201610EH1R0MNE	0806/2016	1.0	1.0	±20	43	38	4.2	4.5	4.1	4.3

- \* Inductance : Measured with a LCR meter 4991A(Agilent) or equivalent (Test Freq. 1MHz, Level 0.1V)
- \* DC Resistance : Measured with a Resistance HI-TESTER 3541(HIOKI) or equivalent
- \* Maximum allowable DC current: Value defined when DC current flows and the initial value of inductance has decreased by 30% or

when current flows and temperature has risen to 40  $^{\circ}$ C whichever is smaller. (Reference: ambient temperature is 25  $^{\circ}$ C  $\pm$ 10)

(Isat) : Allowable current in DC saturation : The DC saturation allowable current value is specified when the decrease of

the initial inductance value at 30% (Reference: ambient temperature is 25  $^{\circ}\!\text{C}\pm10)$ 

(Irms) : Allowable current of temperature rise : The temperature rise allowable current value is specified when temperature of

- $^{\star}$  Absolute maximum voltage : Rated Voltage 20V.
- \* Operating temperature range : -40 to +125°C (Including self-temperature rise)

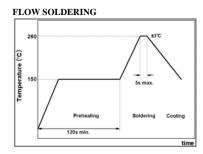
# PRODUCT IDENTIFICATION

<u>CIG</u>	<u>T</u>	<u> 2016</u>	<u>10</u>	<u>EH</u>	<u>1R0</u>	M	<u>N</u>	<u>E</u>
(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)

- (1) Power Inductor
- (3) Dimensior (2016: 2.0mm ×1.6 mm )
- (5) Remark (Characterization Code)
- (7) Toleranc (M:±20%)
- (8) Internal Code
- (9) Packaging (C:paper tape, E:embossed tape)
- (2) Type (T: Metal Composite Thin Film Type)
- (4) Thicknes (10: 1.0mm)
- (6) Inductanı (1R0: 1.0 uH)

# RECOMMENDED SOLDERING CONDITION

# REFLOW SOLDERING 280 230 10s max. Preheating 60s max. 60-120s 30-60s



IRON SOLDERING			
Temperature of	280°C max.		
Soldering Iron Tip	200 Ciliax.		
Preheating	150 °C min.		
Temperature	130 Cillii.		
Temperature	ΔT≤130℃		
Differential	Δ1 = 130 C		
Soldering Time	3sec max.		
Boldering Time	Joec max.		
Wattage	50W max.		

# PACKAGING

Packaging Style	Quantity(pcs/reel)
Embossed Taping	3000 pcs

Item	Specified Value		Test Condition	
Solderability	More than 90% of terminal electrode should be soldered newly.	After being dipped in flux for 4 $\pm$ 1 seconds, and preheated at 150 $\sim$ 180 $^{\circ}$ C for 2 $\sim$ 3 min, the specimen shall be immersed in solder at 245 $\pm$ 5 $^{\circ}$ C for 4 $\pm$ 1 seconds.		
Resistance to Soldering	No mechanical damage. Remaining terminal Electrode: 75% min. Inductance change to be within ±20% to the initial.	After being dipped in flux for 4 $\pm$ 1 seconds, and preheated at 150 $\sim$ 180 $^{\circ}$ C for 2 $\sim$ 3 min, the specimen shall be immersed in solder at 260 $\pm$ 5 $^{\circ}$ C for 10 $\pm$ 0.5 seconds.		
Thermal Shock (Temperature Cycle test)	No mechanical damage Inductance change to be within ±20% to the initial.		the following conditions. ±3 °C for 30 min	
High Temp. Humidity Resistance Test	No mechanical damage Inductance change to be within ±20% to the initial	85±2°C, 85%RH, for 500 Measure the test items a humidity for 24 hours.	±12 hours. fter leaving at normal temperature and	
Low Temperature Test	No mechanical damage Inductance change to be within ±20% to the initial.	Solder the sample on PC at -55±2°C for 500±12 hc Measure the test items a humidity for 24hours.	ours.	
High Temperature Test	No mechanical damage Inductance change to be within ±20% to the initial.	hours.	CB. Exposure at 125±2°C for 500±12  fter leaving at normal temperature and	
High Temp. Humidity Resistance Loading Test	No mechanical damage Inductance change to be within ±20% to the initial	85±2°C, 85%RH, Rated Current for 500±12 hours.  Measure the test items after leaving at normal temperature and humidity for 24 hours.		
High Temperature Loading Test	No mechanical damage Inductance change to be within ±20% to the initial	85±2°C, Rated Current for 500±12 hours. Measure the test items after leaving at normal temperature and humidity for 24 hours.		
Reflow Test	No mechanical damage Inductance change to be within ±20% to the initial	Peak 260±5℃, 3 times		
Vibration Test	No mechanical damage Inductance change to be within ±20% to the initial.	Solder the sample on PCB. Vibrate as apply 10~55Hz, 1.5mm amplitude for 2 hours in each of three(X,Y,Z) axis (total 6 hours).		
	No mechanical damage	Bending Limit; 2mm Test Speed; 1.0mm/sec. Keep the test board at th PCB thickness : 1.6mm		
Bending Test	20 Unit :mm  10 R340			
	No indication of peeling shall occur on the terminal electrode.	W(kgf)	TIME(sec)	
Terminal Adhesion Test		0.5	10±1	
Drop Test	No mechanical damage Inductance change to be within ±20% to the initial.	Random Free Fall test or 1 meter, 10 drops	n concrete plate.	