

## Metal Composite Power Inductor (Thin Film) Specification Sheet



### CIGT252010LM1R0MNE (2520 / EIA 1008)

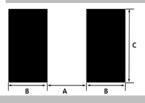
#### **APPLICATION**

Smart phones, Tablet, Wearable devices, Power converter modules, etc.

#### **FFATURES**

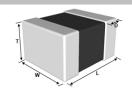
Small power inductor for mobile devices
Low DCR structure and high efficiency inductor for power circuits.
Monolithic structure for high reliability
Free of all RoHS-regulated substances
Halogen free

#### RECOMMENDED LAND PATTERN



	Unit : mm
TYPE	2520
Α	1.2
В	0.8
С	2.0

#### DIMENSION



TYPE	Dimension [mm]					
IIFL	L	W	Т	D		
2520	2.5±0.2	2.0±0.2	1.0 max	0.55±0.25		

#### DESCRIPTION

Part no.	Size	Thickness	Inductance	Inductance tolerance	DC Resistance [mΩ]		2] Rated DC Current (Isat) [A]		Rated DC Current (Irms) [A]	
raitio.	[inch/mm]	[mm] (max)	[uH]	(%)	Max.	Тур.	Max.	Тур.	Max.	Тур.
CIGT252010LM1R0MNE	1008/2520	1.0	1.0	±20	50	43	3.8	4.2	3.1	3.5

- \* Inductance : Measured with a LCR meter 4991A(Agilent) or equivalent (Test Freq. 1MHz, Level 0.1V)
- \* DC Resistance : Measured with a Resistance HI-TESTER 3541(HIOKI) or equivalent
- \* Maximum allowable DC current : Value defined when DC current flows and the initial value of inductance has decreased by 30% or when current flows and temperature has risen to 40 °C whichever is smaller. (Reference: ambient temperature is 25 °C±10)

(Isat): Allowable current in DC saturation: The DC saturation allowable current value is specified when the decrease of

(Irms) : Allowable current of temperature rise : The temperature rise allowable current value is specified when temperature of the inductor is raised 40 ℃ by DC current. (Reference: ambient temperature is 25 ℃±10)

- \* Absolute maximum voltage : Absolute maximum voltage DC 20V.
- $^{\star}$  Operating temperature range : -40 to +125  $^{\circ}\text{C}$  (Including self-temperature rise)

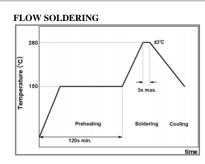
#### PRODUCT IDENTIFICATION

<u>CIG</u>	<u>T</u>	<u>2520</u>	<u>10</u>	<u>LM</u>	<u>1R0</u>	<u>M</u>	<u>N</u>	<u>E</u>
(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)

- (1) Power Inductor
- (3) Dimensior (2520: 2.5mm × 2.0mm )
- (5) Remark (Characterization Code)
- (7) Toleranc (M:±20%)
- (8) Internal Code
- (9) Packaging (C:paper tape, E:embossed tape)
- (2) Type (T: Metal Composite Thin Film Type)
- (4) Thicknes (10: 1.0mm)
- (6) Inductan (1R0: 1.0 uH)

#### RECOMMENDED SOLDERING CONDITION

# REFLOW SOLDERING 280 230 230 180 180 Preheating Soldering Cooling 30 - 60s max.



IRON SOLDERING					
Temperature of Soldering Iron Tip	280 ℃ max.				
Preheating Temperature	150℃min.				
Temperature Differential	ΔT≤130℃				
Soldering Time	3sec max.				
Wattage	50W max.				

#### PACKAGING

Packaging Style	Quantity(pcs/reel)
Embossed Taping	3000 pcs

Item	Specified Value	Test Condition			
Solderability	More than 90% of terminal electrode should be soldered newly.	After being dipped in flux for $4\pm1$ seconds, and preheated at $150\sim180^{\circ}\mathrm{C}$ for $2\sim3$ min, the specimen shall be immersed in solder at $245\pm5^{\circ}\mathrm{C}$ for $4\pm1$ seconds.			
Resistance to Soldering	No mechanical damage. Remaining terminal Electrode: 75% min. Inductance change to be within ±20% to the initial.	After being dipped in flux for 4 $\pm$ 1 seconds, and preheated at 150 $\sim$ 180 $^{\circ}$ C for 2 $\sim$ 3 min, the specimen shall be immersed in solder at 260 $\pm$ 5 $^{\circ}$ C for 10 $\pm$ 0.5 seconds.			
Thermal Shock (Temperature Cycle test)	No mechanical damage Inductance change to be within ±20% to the initial.	Repeat 100 cycles under the following conditions. -40±3°C for 30 min → 85±3°C for 30 min			
High Temp. Humidity Resistance Test	No mechanical damage Inductance change to be within ±20% to the initial	85±2°C, 85%RH, for 500: Measure the test items a and humidity for 24 hours	fter leaving at normal temperature		
Low Temperature Test	No mechanical damage Inductance change to be within ±20% to the initial.	Solder the sample on PC at -55±2°C for 500±12 ho Measure the test items a and humidity for 24hours	ours.  fter leaving at normal temperature		
High Temperature Test	No mechanical damage Inductance change to be within ±20% to the initial.	hours.	B. Exposure at 125±2 °C for 500±12 fter leaving at normal temperature .		
High Temp. Humidity Resistance Loading Test	No mechanical damage Inductance change to be within ±20% to the initial	· · · · · · · · · · · · · · · · · · ·	Current for 500±12 hours. fter leaving at normal temperature s.		
High Temperature Loading Test	No mechanical damage Inductance change to be within ±20% to the initial Measure the test items after leaving at normal tempand humidity for 24 hours.				
Reflow Test	No mechanical damage Inductance change to be within ±20% to the initial Peak 260±5℃, 3 times				
Vibration Test	No mechanical damage Inductance change to be within ±20% to the initial.  Solder the sample on PCB. Vibrate as apply 10~55Hz, amplitude for 2 hours in each of three(X,Y,Z) axis (total hours).				
	No mechanical damage	Bending Limit; 2mm Test Speed; 1.0mm/sec. Keep the test board at th PCB thickness : 1.6mm	e limit point in 5 sec.		
Bending Test	19	20 R340 46	Unit :mm		
	No indication of peeling shall occur on the	W(kgf)	TIME(sec)		
	terminal electrode.	0.5	10±1		
Terminal Adhesion Test		₩ w			
Drop Test	No mechanical damage Inductance change to be within ±20% to the initial.	Random Free Fall test on concrete plate. 1 meter, 10 drops			
lpeak (AC+DC Load Life)	No mechanical damage Inductance change to be within ±20% to the initial	85±2°C, 85%RH, Load(Ipeak) for 120 hours. (Frequncy:1MHz, Load(Ipeak):1.5hr on / 0.5hr off) Measure the test items after leaving at normal temperature and humidity for 24 hours.  * Load(Ipeak) = Irms(max)×1.4			