

DGY RF Power Detector with Buffered Output and >40dB Dynamic Range

FEATURES

Internal Schottky Diode RF Detector with Two Input Power Ranges:

LTC5505-1, -28dBm to 18dBm LTC5505-2, -32dBm to 12dBm

- Wide Input Frequency Range: 300MHz to 3GHz (LTC5505-1); 300MHz to 3.5GHz (LTC5505-2)
- Temperature Compensated
- Buffered Detector Output
- Wide V_{CC} Range of 2.7V to 6V
- Low Operating Current: 0.5mA
- Low Shutdown Current: <2µA
- Low Profile (1mm) ThinSOTTM Package

APPLICATIONS

- Multimode Mobile Phone Products
- PCS Devices
- Wireless Data Modems
- Wireless and Cable Infrastructure
- RF Power Alarm
- Envelope Detector

DESCRIPTION

The LTC®5505-X is an RF power detector for RF applications operating in the 300MHz to 3.5GHz range. A temperature compensated Schottky diode peak detector and buffer amplifier are combined in a small 5-pin ThinSOT package. The supply voltage range is optimized for operation from a single lithium-ion cell or 3xNiMH.

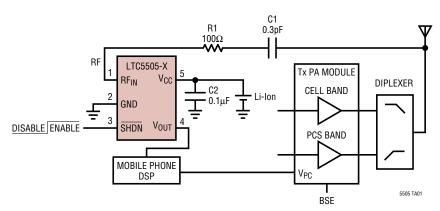
The RF input voltage is peak detected using an on-chip Schottky diode. The detected voltage is buffered and supplied to the V_{OUT} pin. A power saving shutdown mode reduces supply current to less than $2\mu A$.

The LTC5505-1 operates with input power levels from -28dBm to 18dBm. The LTC5505-2 operates with input power levels from -32dBm to 12dBm.

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TYPICAL APPLICATION

Dual Band Mobile Phone Tx Power Control

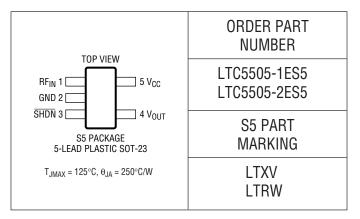




ABSOLUTE MAXIMUM RATINGS

(Note 1)

PACKAGE/ORDER INFORMATION



Consult LTC Marketing for parts specified with wider operating temperature ranges.

ELECTRICAL CHARACTERISTICS The • denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^{\circ}C$. $V_{CC} = 3.6V$, $\overline{SHDN} = V_{CC} = HI$, $\overline{SHDN} = 0V = LO$, RF Input Signal is Off, unless otherwise noted.

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
V _{CC} Operating Voltage		•	2.7		6	V
I _{VCC} Shutdown Current	SHDN = LO	•			2	μА
I _{VCC} Operating Current	SHDN = HI, I _{VOUT} = 0mA	•		0.5	0.75	mA
V _{OUT} V _{OL} (No RF Input)	$R_{LOAD} = 2k$, $\overline{SHDN} = HI$, Enabled $\overline{SHDN} = LOW$, Disabled		170	260 1	350	mV mV
V _{OUT} Output Current	$V_{OUT} = 1.75V$, $V_{CC} = 2.7V$, $\Delta V_{OUT} = 10$ mV	•	1	2		mA
V _{OUT} Enable Time	$\overline{SHDN} = HI, C_{LOAD} = 33pF, R_{LOAD} = 2k$	•		8	20	μS
V _{OUT} Bandwidth	$C_{LOAD} = 33pF, R_{LOAD} = 2k \text{ (Note 4)}$			4		MHz
V _{OUT} Load Capacitance	(Note 7)	•			33	pF
V _{OUT} Slew Rate	$V_{RFIN} = 2V$ Step, $C_{LOAD} = 33pF$, $R_{LOAD} = 2k$ (Note 3)			10		V/µs
V _{OUT} Noise	V_{CC} = 3V, Noise BW = 1.5MHz, 50Ω RF Input Termination			1.4		mV _{P-P}
SHDN Voltage, Chip Disabled	V _{CC} = 2.7V to 6V	•			0.35	V
SHDN Voltage, Chip Enabled	V _{CC} = 2.7V to 6V	•	1.4			V
SHDN Input Current	SHDN = 3.6V	•		24	40	μА

ELECTRICAL CHARACTERISTICS The \bullet denotes the specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^{\circ}C$. $V_{CC} = 3.6V$, $\overline{SHDN} = V_{CC} = HI$, $\overline{SHDN} = 0V = LO$, RF Input Signal is Off, unless otherwise noted.

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
RF _{IN} Input Frequency Range (LTC5505-1) (LTC5505-2)			300 to 3000 300 to 3500		MHz MHz
RF _{IN} Input Power Range (LTC5505-1)	RF Frequency = 900MHz (Note 5, 6, 7) V_{CC} = 2.7V to 6V RF Frequency = 1800MHz (Note 5, 6, 7) V_{CC} = 2.7V to 6V RF Frequency = 2400MHz (Note 5, 7) V_{CC} = 2.7V to 6V RF Frequency = 2700MHz (Note 5, 7) V_{CC} = 2.7V to 6V		-28 to 18 -26 to 18 -24 to 16 -22 to 16		dBm dBm dBm dBm
RF _{IN} Input Power Range (LTC5505-2)	RF Frequency = 900MHz (Note 5) RF Frequency = 1800MHz (Note 5) RF Frequency = 2400MHz (Note 5) RF Frequency = 2700MHz (Note 5)		-32 to 12 -32 to 12 -32 to 12 -30 to 12		dBm dBm dBm dBm
RF _{IN} AC Input Resistance (LTC5505-1)	F = 850MHz F = 1850MHz		165 20		Ω Ω
RF _{IN} Input Shunt Capacitance (LTC5505-1)			2		pF
RF _{IN} AC Input Resistance (LTC5505-2)	F = 850MHz F = 1850MHz		165 59		Ω Ω
RF _{IN} Input Shunt Capacitance (LTC5505-2)			1.3	-	pF

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: Specifications over the –40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

Note 3: The rise time at V_{OUT} is measured between 0.5V and 1.5V.

Note 4: Bandwidth is calculated using the 10% to 90% rise time equation: BW = 0.35/rise time.

Note 5: RF performance is tested at:

1800MHz, 14dBm, -14dBm (LTC5505-1)

1800MHz, 12dBm, -14dBm (LTC5505-2)

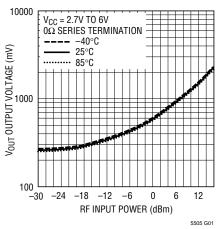
Note 6: For input RF power levels >16dBm, V_{CC} minimum is 3V and an RF input series resistor of 20Ω is required to limit the input current.

Note 7: Guaranteed by design.

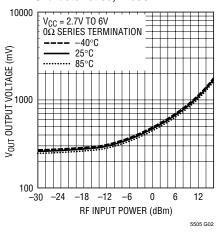


TYPICAL PERFORMANCE CHARACTERISTICS

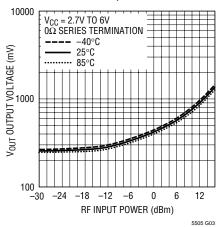
LTC5505-1 Typical Detector Characteristics, 0.85GHz



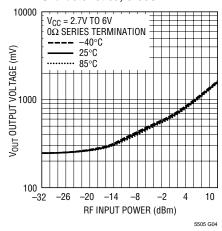
LTC5505-1 Typical Detector Characteristics, 1.85GHz



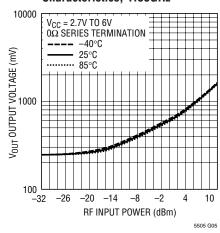
LTC5505-1 Typical Detector Characteristics, 2.45GHz



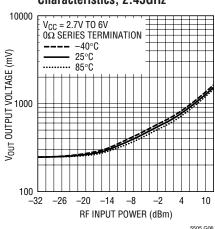
LTC5505-2 Typical Detector Characteristics, 0.85GHz



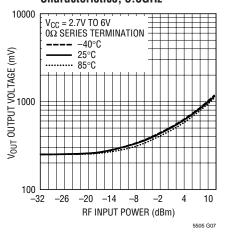
LTC5505-2 Typical Detector Characteristics, 1.85GHz



LTC5505-2 Typical Detector Characteristics, 2.45GHz



LTC5505-2 Typical Detector Characteristics, 3.5GHz



5505f



PIN FUNCTIONS

 RF_{IN} (Pin 1): RF Input Voltage. Referenced to $V_{CC}.$ An external coupling capacitor to the RF source is required. The frequency range is 300MHz to 3GHz. This pin has an internal 250Ω termination, an internal Schottky diode detector and peak detector capacitor. (See Note 6 in the Electrical Characteristics.)

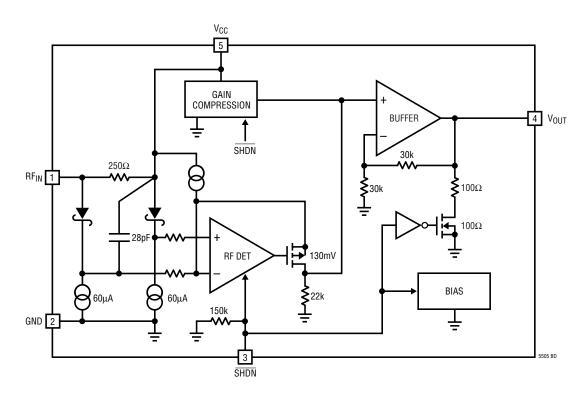
GND (Pin 2): System Ground.

SHDN (Pin 3): Shutdown Input. A logic low on the SHDN pin places the part in shutdown mode. A logic high enables the part. SHDN has an internal 150k pull down resistor to ensure that the part is in shutdown when the drivers are in a tri-state condition.

V_{OUT} (Pin 4): Buffered and Level Shifted Detector Output Voltage.

V_{CC} (Pin 5): Power Supply Voltage, 2.7V to 6V. V_{CC} should be bypassed appropriately with ceramic capacitors.

BLOCK DIAGRAM



APPLICATIONS INFORMATION

Operation

The LTC5505-X RF detector integrates several functions to provide RF power detection over frequencies ranging from 300MHz to 3.5GHz. These functions include an internally compensated buffer amplifier, an RF Schottky diode peak detector and level shift amplifier to convert the RF feedback signal to DC, a delay circuit to avoid voltage transients at V_{OUT} when coming out of shutdown and a gain compression circuit to extend the detector dynamic range.

Buffer Amplifier

The buffer amplifier has a gain of two and is capable of driving a 2mA load. The buffer amplifier typically has an output voltage range of 0.25V to 1.75V.

RF Detector

The internal RF Schottky diode peak detector and level shift amplifier converts the RF input signal to a low frequency signal. The detector demonstrates excellent efficiency and linearity over a wide range of input power. The Schottky detector is biased at about 60µA and drives a peak detector capacitor of 28pF.

Gain Compression

The gain compression circuit changes the feedback ratio as the RF peak-detected input voltage increases above 100mV. Below 100mV, the voltage gain from the peak detector to the buffer output is 1.5. Above 200mV, the voltage gain is reduced to 0.7. The compression expands the low power detector range due to higher gain.

Modes of Operation

MODE	SHDN	OPERATION
Shutdown	Low	Disabled
Enable	High	Power Detect

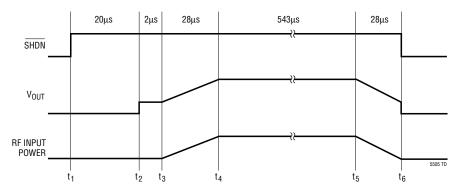
Applications

The LTC5505-1 and LTC5505-2 can be used as selfstanding signal strength measuring receivers for a wide range of input signals from -32dBm to 18dBm for frequencies from 300MHz to 3.5GHz.

The LTC5505-1 and LTC5505-2 can be used as demodulators for AM and ASK modulated signals with data rates up to 5MHz. Depending on specific application needs, the RSSI output can be split into two branches, providing AC-coupled data (or audio) output and DC-coupled, RSSI output for signal strength measurements and AGC.

The LTC5505-1 and LTC5505-2 can be used for dual band mobile phone transmitter power control (refer to Typical Application schematic on first page). The circuit uses a capacitive tap at the Tx PA outputs. For example, a 0.3pF capacitor (C1) followed by a 100Ω resistor (R1) forms a coupling circuit with about a 20dB loss at the cellular band and 18dB loss at the PCS band, referenced to the LTC5505-2 IC RF input pin. For improved coupling accuracy, the C1 capacitor should be a high tolerance component (± 0.05 pF.)

Example of LTC5505-X GSM/DCS Power Control Timing Diagram



 t_1 : PART COMES OUT OF SHUTDOWN $20\mu s$ MAXIMUM PRIOR TO BURST

t2: CIRCUITS POWER UP AND SETTLE.

t3: BASEBAND CONTROLLER STARTS RF POWER RAMP UP AT 22µs AFTER SHDN IS ASSERTED HIGH.

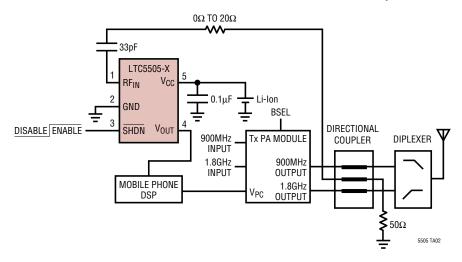
t4: BASEBAND CONTROLLER COMPLETES RAMP UP.

t₅: BASEBAND CONTROLLER STARTS RF POWER RAMP DOWN AT END OF BURST.

t₆: LTC5505-X RETURNS TO SHUTDOWN MODE BETWEEN BURSTS

TYPICAL APPLICATION

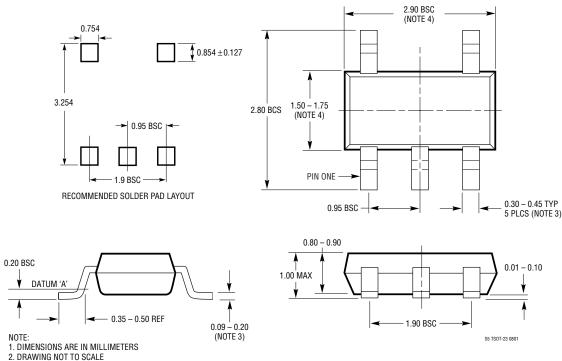
Dual Band Mobile Phone Tx Power Control with Directional Coupler



PACKAGE DESCRIPTION

S5 Package 5-Lead Plastic TSOT-23

(Reference LTC DWG # 05-08-1635)



3. DIMENSIONS ARE INCLUSIVE OF PLATING 4. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH AND METAL BURR

5. MOLD FLASH SHALL NOT EXCEED 0.254mm 6. JEDEC PACKAGE REFERENCE IS MO-193

