



DSGP.1575.25.4.A.02

Description:

GPS L1 / GALILEO E1 1575.42MHz 25*25*4mm Ceramic Patch SMD Antenna

Features:

4.34 dBi Peak Gain for GPS/GALILEO Band

Dimensions: 25 x 25 x 4mm

SMD Direct Mount Ceramic Patch Antenna

TS16949 Approved

RoHS & Reach Compliant



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1. Introduction



The DSGP.1575.25.4.A.02 is a 25mm ceramic GPS L1 / GALILEO E1 passive patch antenna. 25mm square with a height of just 4mm, this antenna is perfect for applications in telematics devices, vehicle tracking/fleet management systems, and navigation devices. Typical applicable industries include transportation, defense, marine, agriculture, and navigation.

The antenna has been tuned on a 50*50mm ground plane, working at 1575.42MHz with a 4.34dBi gain. The antenna has a stable hemispherical radiation pattern. The ceramic patch is mounted via SMT process, ideal for high-volume, low-cost assembly. It is manufactured and tested in a TS16949 first tier automotive approved facility.

For further optimization to customer specific device environments, custom tuned patch antennas can be supplied, subject to NRE and MOQ. For more details please contact your regional Taoglas sales office.



2. Specifications

		GNSS	Frequency	Bands Cover	ed		
GPS/QZSS	L1 1575.42MHz	L2 1227.6MHz	L5 1176.45MHz	L6 1278.75MHz			
GLONASS	L5R 1176.45MHz	L3PT 1201.5MHz	L2PT 1246MHz	L1CR 1575.42MHz	L1PT 1602MHz		
				\square			
Galileo	E5a 1176.45MHz	E5b 1201.5MHz	E4 1215MHz	E3 1256MHz	E6 1278.75MHz	E2 1561MHz	L1 1575.42MHz
							\square
BeiDou	B1 1561MHz	B2 1207.14MHz	B3 1268.52MHz				
Compass	E5B(B2)/ E6(B3) 1268.56MHz	E2(B1) 1561MHz					
SBAS	Omnistar 1542.5MHz	WAAS/EGN OS 1575.42MHz					

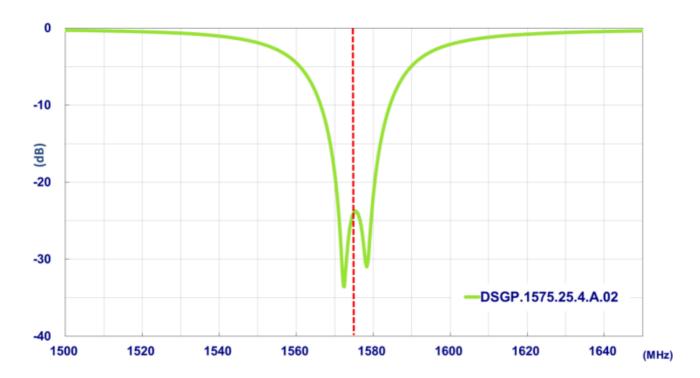


	Electrical
Frequency (MHz)	1575.42
VSWR (max.)	2.0:1
Passive Antenna Efficiency (%)	85.13
Passive Antenna Gain at Zenith (dBi)	4.34
Return Loss (dB)	<-10
Impedance	50Ω
	Mechanical
Dimension	25 x 25 x 4mm
Weight	9.4g
	Environmental
Operation Temperature	-40°C to 85°C
Humidity	Non-condensing 65°C 95% RH
Moisture Sensitivity Level (MSL)	3 (168 Hours)

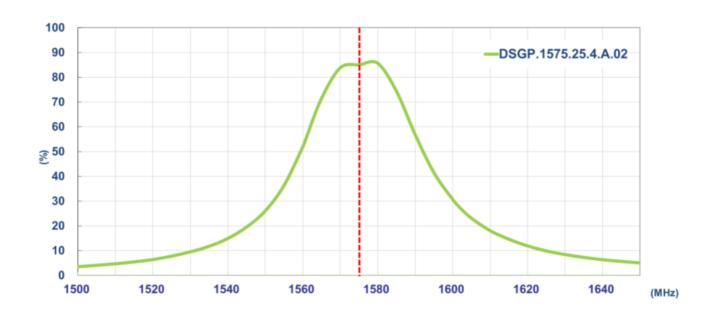


3. Antenna Characteristics

3.1 Return Loss

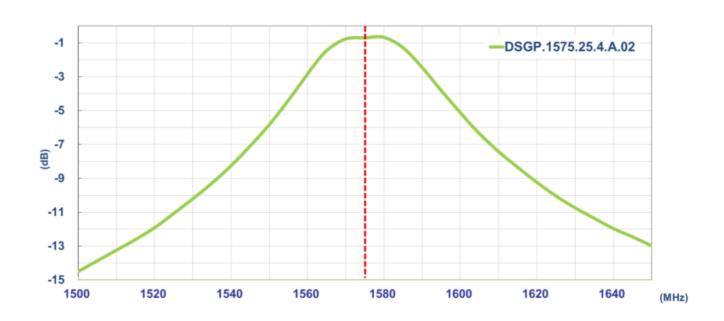


3.2 Efficiency





3.3 Average Gain



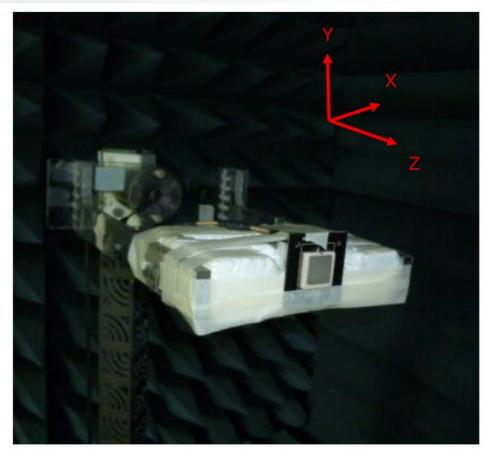
3.4 Peak Gain





4. Radiation Patterns

4.1 Test Setup

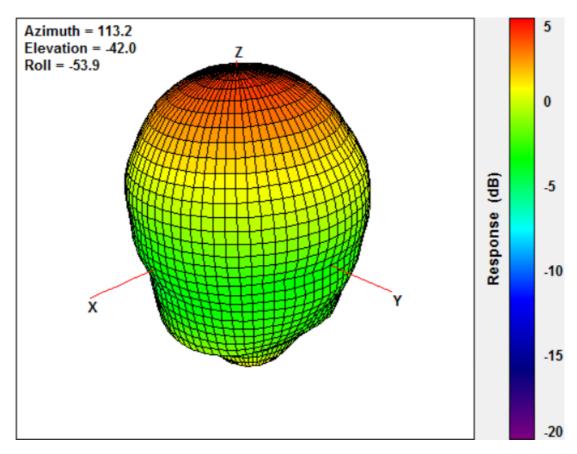


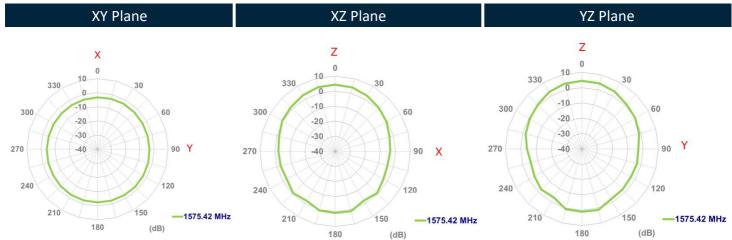
On Evaluation Board

Taoglas Part number: DSGPD.25A



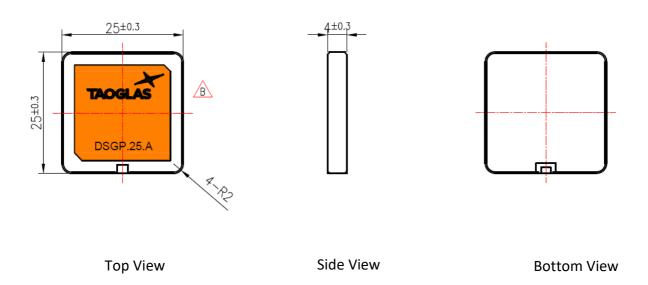
4.2 1575.42MHz 3D and 2D Radiation Patterns







5. Mechanical Drawing (Units: mm)





6. Antenna Integration Guide



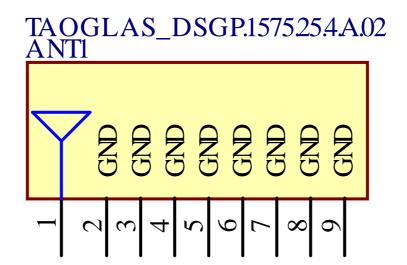




6.1 Schematic Symbol and Pin Definition

The circuit symbol for the antenna is shown below. The antenna has 8 pins with all as functional.

Pin	Description
1	RF Feed
2, 3, 4, 5, 6, 7, 8	Ground



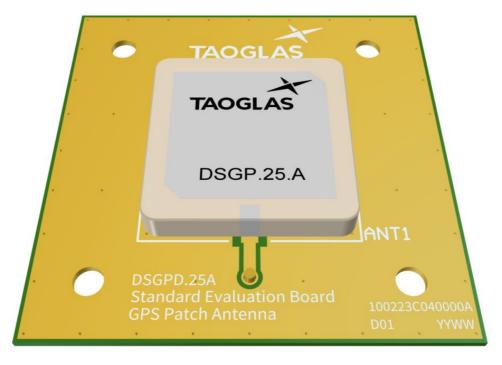


6.2 Antenna Integration

The antenna should be placed at the center of the ground plane with a length and width of 50mm. Maintaining a square symmetric ground plane shape and symmetric environment around the antenna is critical to maintaining the excellent axial ratio and phase center performance shown in this datasheet.



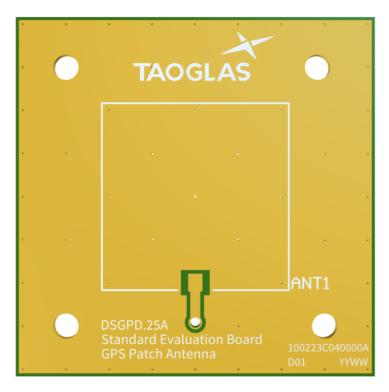
Top Side w/ Solder Mask



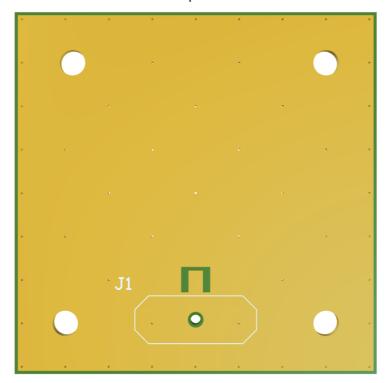
Top Side w/o Solder Mask

6.3 PCB Layout

The footprint and clearance on the PCB must comply with the antenna specification. The PCB layout shown in the diagram below demonstrates the antenna footprint.

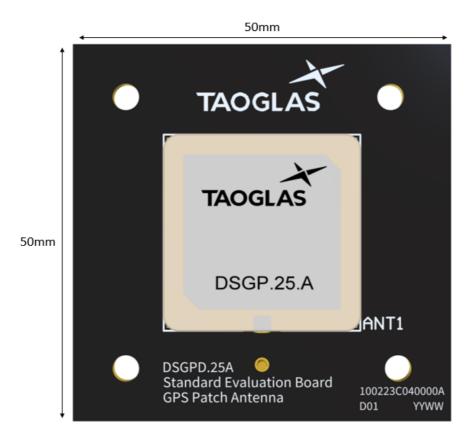


Topside

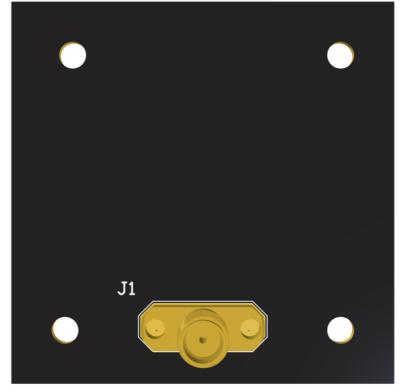


Bottom Side

6.4 Evaluation Board



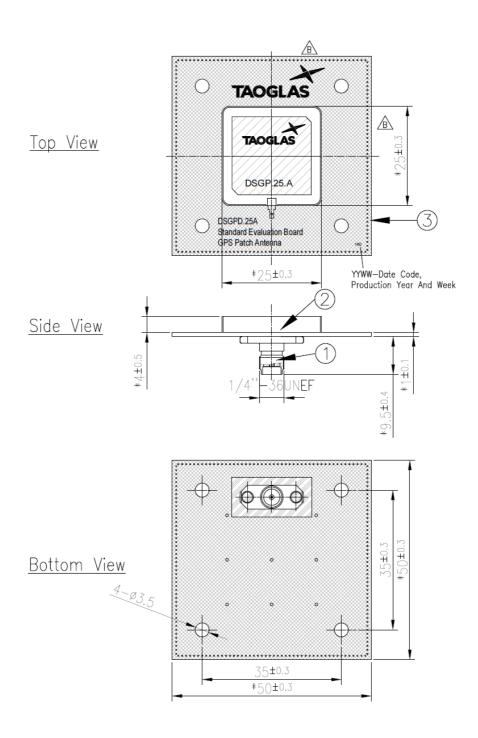
Topside



Bottom Side



7. Evaluation Board Mechanical Drawing



Notes

1. Silver area

2. Solder mask

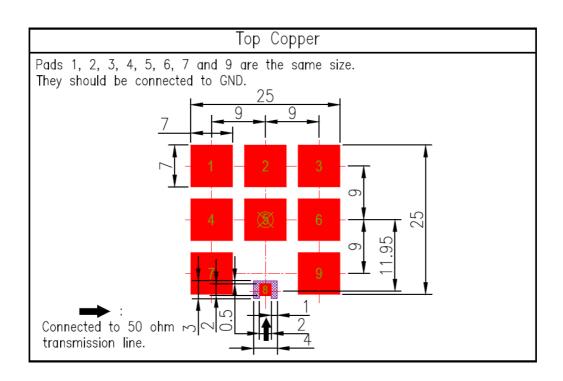
3. Solder Area

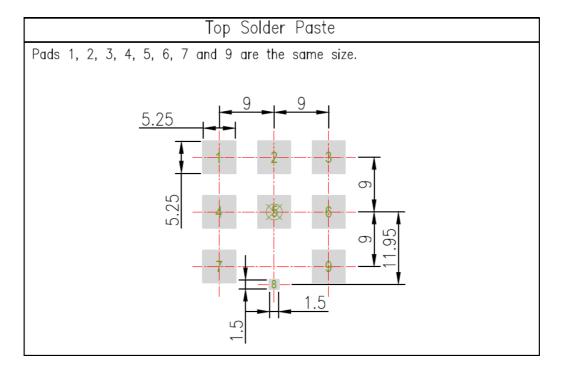
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	Name	P/N	Material	Finish	QTY
1	PCB SMA(F) ST	200411I000007A	Brass	Au Plated	1
2	DSGP.1575.25.4.A.02 Antenna	001514L050007A	Ceramic	Clear	1
3	PCB (50x50x1mm)	100213D000007A	FR4 1.0t	Black	1

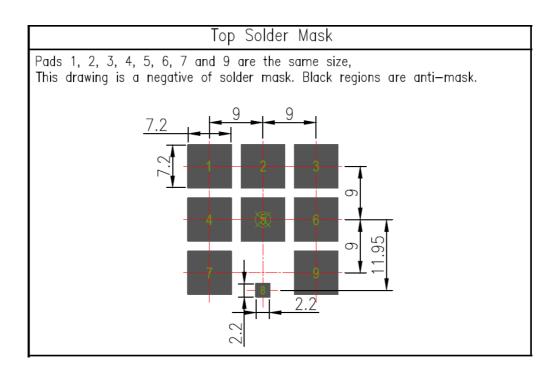


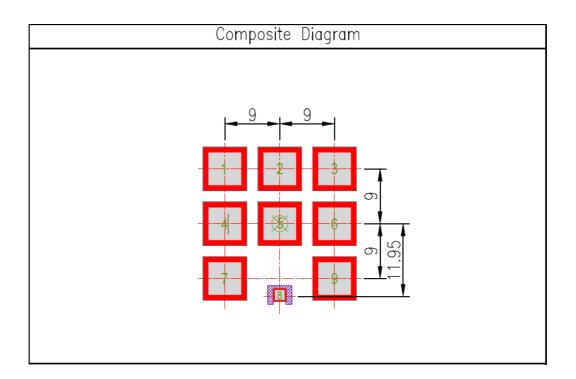
8. PCB Footprint Recommendation











NOTE:

- 1. Ag Plated area
- 2. Solder Mask area
- 3. Copper area
- 4. Paste area
- 5. Copper Keepout Area
- 6. Copper keepout should extend through all PCB layers.
- 7. Any vias in pads should be either filled or tented to prevent solder from wicking away from the pad during reflow.
- 8. The dimension tolerances should follow standard PCB manufacturing guidelines

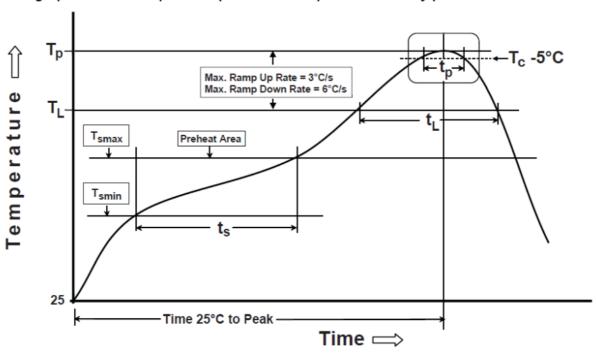


9. Recommended Reflow Soldering Profile

DSGP.1575.25 can be assembled following Pb-free assembly. According to the Standard IPC/JEDEC J-STD-020C, the temperature profile suggested is as follows:

Phase	Profile Features	Pb-Free Assembly (SnAgCu)
	Temperature Min (Tsmin)	150°C
PREHEAT	Temperature Max (Tsmax)	200°C
	Time(ts) from (Tsmin to Tsmax)	60-120 seconds
RAMP-UP	Avg. Ramp-up Rate (Tsmax to TP)	3°C/second(max)
DEELOW	Temperature (TL)	217°C
REFLOW	Total Time above TL (tL)	30-100 seconds
PEAK	Temperature (TP)	260°C
PEAK	Time(tp)	2-5 seconds
RAMP-DOWN	Rate	3°C/second(max)
	Time from 25°C to Peak Temperature	8 minutes max.
	Composition of solder paste	96.5Sn/3Ag/0.5Cu
	Solder Paste Model	SHENMAO PF606-P26

The graphic shows temperature profile for component assembly process in reflow ovens

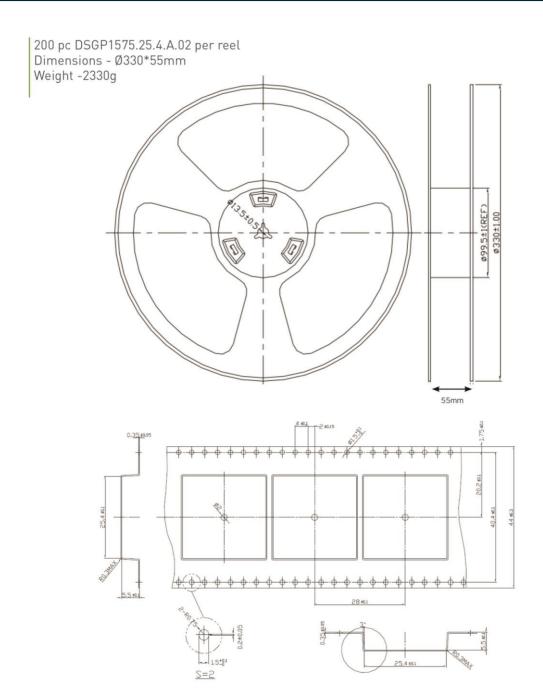


Soldering Iron condition : Soldering iron temperature 270°C±10°C.

Apply preheating at 120°C for 2-3 minutes. Finish soldering for each terminal within 3 seconds, if soldering iron temperature over270°C±10°C or 3 seconds, it will make cause component surface peeling or damage.

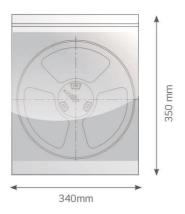


10. Packaging

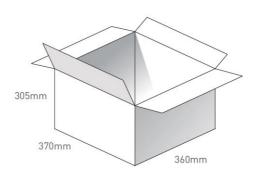




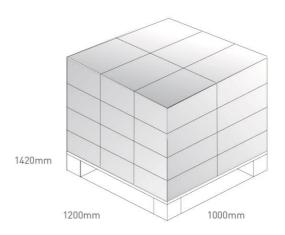
1 pc reel in small in Anti-static Bag Dimensions - 340*350*70mm Weight - 2.63Kg



4 Reels / 800 pcs in one carton Carton Dimensions - 370*360*305mm Weight - 11.3Kg



Pallet Dimensions 1200*1000*1420mm 24 Cartons per Pallet 6 Cartons per layer 4 Layers





Changelog for the datasheet

SPE-17-8-036- DSGP.1575.25.4.A.02

Revision: B (Current	Version)
Date:	2023-03-24
Changes:	Antenna Integration Guide Added
Changes Made by:	Cesar Sousa

Previous Revisions

vision: A (Origina	First Release)	
Date: Notes:	2017-05-22	
Author:	Jack Conroy	