

## Product Summary

V <sub>BR</sub> (Min)	I <sub>PP</sub> (Max)	C <sub>10</sub> (Typ)
6V	5.5A	0.6pF

## Description

The DT1240-08LP3810 is a high-performance device suitable for protecting four high-speed I/Os. These devices are assembled in U-DFN3810-9 (Type B) package and have high ESD surge capability and low capacitance.

## Applications

Typically used at high-speed ports such as USB2.0, USB3.0, USB3.1, IEEE1394 (Firewire®, iLink), Serial ATA, DVI™, HDMI™1.4, HDMI™2.0 and PCI™.

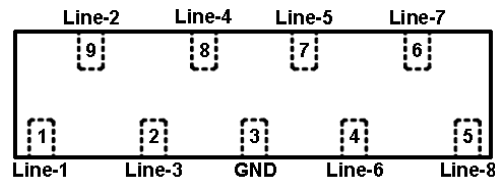
## Features

- Clamping Voltage: 9V at 10A 100ns, TLP; 9.4V at 5.5A (8µs/20µs)
- IEC 61000-4-2 (ESD): Air – ±16kV, Contact – ±14kV
- IEC 61000-4-5 (Lighting): 5.5A (8/20µs)
- 8 Channels of ESD Protection
- Low Channel Input Capacitance of 0.6pF Typical
- TLP Dynamic Resistance: 0.30Ω
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

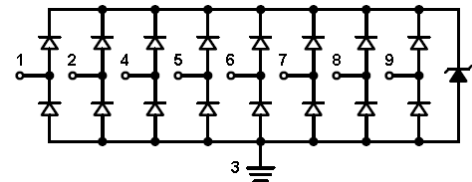
## Mechanical Data

- Case: U-DFN3810-9
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Schematic
- Terminals: Finish – NiPdAu, Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.005 grams (Approximate)

U-DFN3810-9 (Type B)



Pin Description (Top View)



Device Schematic

## Ordering Information (Note 4)

Part Number	Compliance	Marking Code	Reel Size (inches)	Tape Width (mm)	Quantity
DT1240-08LP3810-7	Standard	PD38	7	8	5,000/Tape & Reel

- Notes:
- No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  - See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information

**PD38 YM**

PD38 = Product Type Marking Code  
 YM = Date Code Marking  
 Y = Year (ex: I = 2021)  
 M = Month (ex: 9 = September)

### Date Code Key

Year	2017	...	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	E	...	I	J	K	L	M	N	O	P	R	S

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current, per IEC 61000-4-5	I <sub>PP</sub>	5.5	A	I/O to V <sub>SS</sub> , 8/20μs
Peak Pulse Power, per IEC 61000-4-5	P <sub>PP</sub>	60	W	I/O to V <sub>SS</sub> , 8/20μs
ESD Protection – Contact Discharge, per IEC 61000-4-2	V <sub>ESD_CONTACT</sub>	±14	kV	I/O to V <sub>SS</sub>
ESD Protection – Air Discharge, per IEC 61000-4-2	V <sub>ESD_AIR</sub>	±16	kV	I/O to V <sub>SS</sub>
Operating Temperature	T <sub>OP</sub>	-55 to +85	°C	—
Storage Temperature	T <sub>STG</sub>	-55 to +150	°C	—

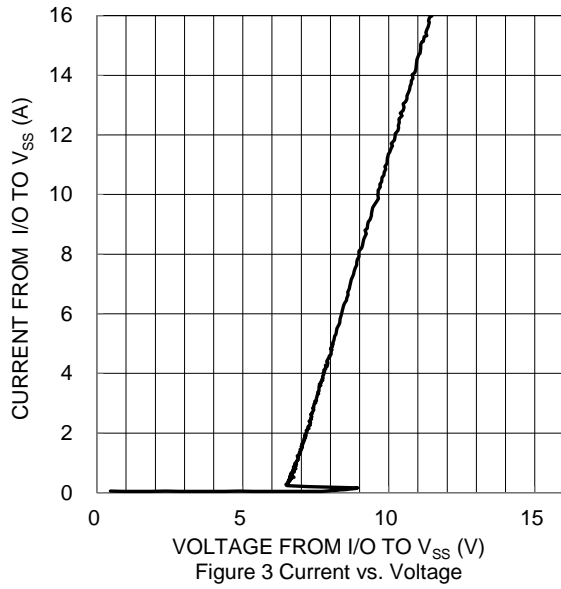
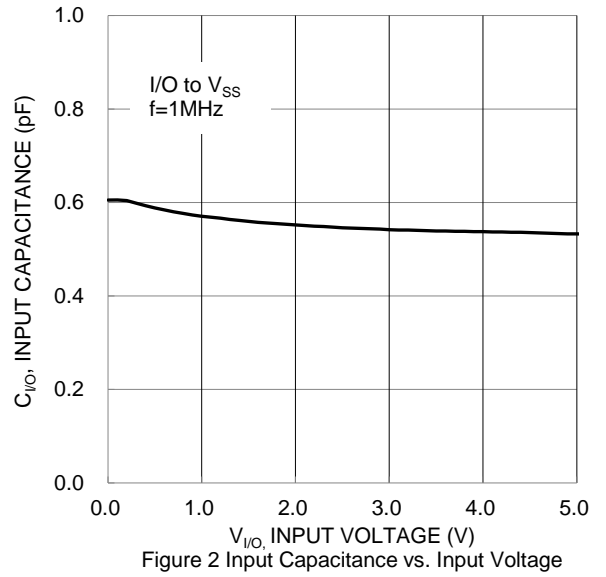
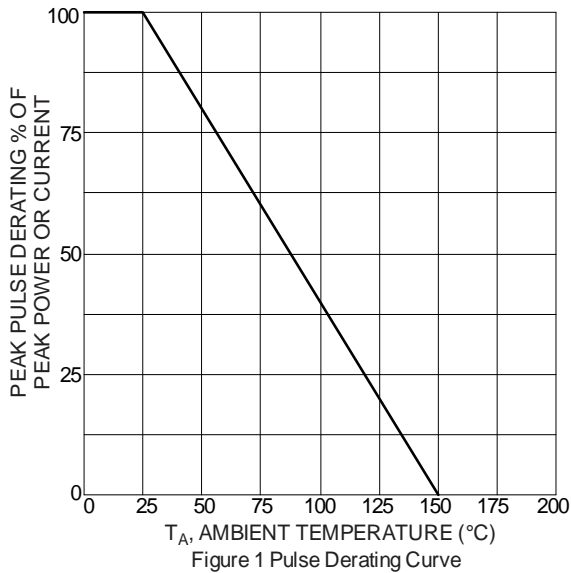
**Thermal Characteristics**

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	P <sub>D</sub>	350	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	R <sub>θJA</sub>	360	°C/W

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Working Voltage	V <sub>RWM</sub>	—	—	5.5	V	—
Reverse Current	I <sub>R</sub>	—	—	0.5	μA	V <sub>R</sub> = 5.5V, I/O to V <sub>SS</sub>
Reverse Breakdown Voltage	V <sub>BR</sub>	6	—	—	V	I <sub>R</sub> = 1mA, I/O to V <sub>SS</sub>
Forward Clamping Voltage	V <sub>F</sub>	-1.0	-0.85	—	V	I <sub>F</sub> = -15mA, I/O to V <sub>SS</sub>
Reverse Clamping Voltage (Note 6)	V <sub>C</sub>	—	9.4	11	V	I <sub>PP</sub> = 5.5A, I/O to V <sub>SS</sub> , 8/20μs
ESD Clamping Voltage	V <sub>ESD</sub>	—	9	—	V	TLP, 10A, t <sub>P</sub> = 100ns, I/O to V <sub>SS</sub>
Dynamic Reverse Resistance	R <sub>DIF-R</sub>	—	0.3	—	Ω	TLP, 10A, t <sub>P</sub> = 100ns, I/O to V <sub>SS</sub>
Dynamic Forward Resistance	R <sub>DIF-F</sub>	—	0.25	—	Ω	TLP, 10A, t <sub>P</sub> = 100ns, V <sub>SS</sub> to I/O
Channel Input Capacitance	C <sub>I/O</sub>	—	0.6	—	pF	V <sub>I/O</sub> = 2.5V, V <sub>SS</sub> = 0V, f = 1MHz

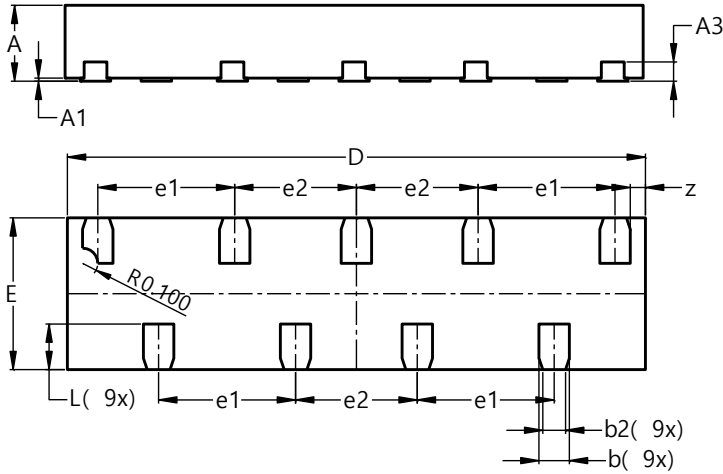
Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's website at <http://www.diodes.com/package-outlines.html>.  
6. Clamping voltage value is based on an 8x20μs peak pulse current (I<sub>PP</sub>) waveform.



**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**U-DFN3810-9 (Type B)**

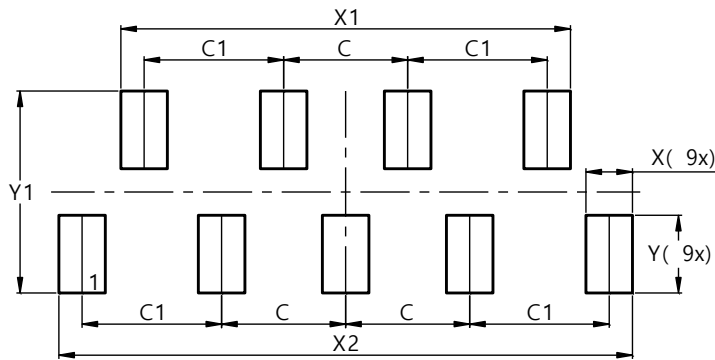


U-DFN3810-9 (Type B)			
Dim	Min	Max	Typ
A	0.45	0.55	0.50
A1	0.00	0.05	0.02
A3	--	--	0.127
b	0.15	0.25	0.20
b2	0.10	0.20	0.15
D	3.75	3.85	3.80
E	0.95	1.05	1.00
e1	--	--	0.90
e2	--	--	0.80
L	0.25	0.35	0.30
z	--	--	0.10
All Dimensions in mm			

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**U-DFN3810-9 (Type B)**



Dimensions	Value (in mm)
C	0.800
C1	0.900
X	0.300
X1	2.900
X2	3.700
Y	0.500
Y1	1.300