

# EA4529YH-T1011 User's Guide

## USB Car Charger Solution

### Description

This document describes the characteristic and operation of the Active Semi **EA4529YH-T1011** evaluation kit (EVK). It provides setup and operation instructions, schematic, layout, BOM, and test data. This EVK demonstrates the ACT4529YH-T1011 version of the IC. Other ACT4529YH-Txxxx versions, including automotive versions, can be evaluated on this EVK by replacing the IC and any other necessary components.

EVK OPN	PDC	USB AUTO DETECT	QC2.0	CERTIFICATION
EA4529YH-T0001	Yes	YES	No	MFi
EA4529YH-T0010	Yes	No	Yes	QC2.0
EA4529YH-T0011	Yes	Yes	Yes	N/A
EA4529YH-T1011	Yes	Yes	Yes	N/A
EA4529MYH-T0010	Yes	No	Yes	SAE J1752 M8A /QC2.0
EA4529MYH-T1011	Yes	Yes	Yes	SAE J1752 M8A

### Features

The EVK is a QC2.0 car charger solution. The EVK contains the high efficiency step-down DC/DC converter that operates in either CV (Constant Output Voltage) mode or CC (Constant Output Current) mode. The EVK provides up to 2.4A output current at 125kHz switching frequency. It operates from  $V_{in} = 6V$  to 32V and provides an output voltage of 5V, 9V, or 12V depending on the load's requested voltage. Gerber files are available to minimize time-to-market for applications that want to use the EVK as an end product.

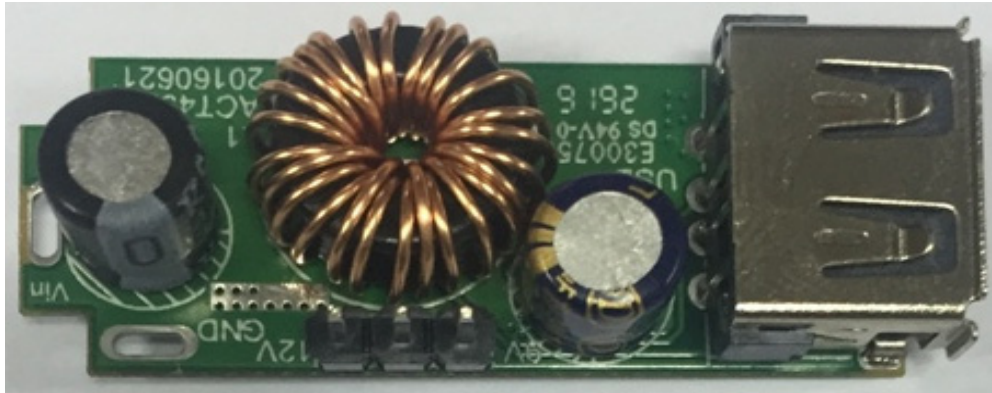


Figure 1 – EVK Picture - Top

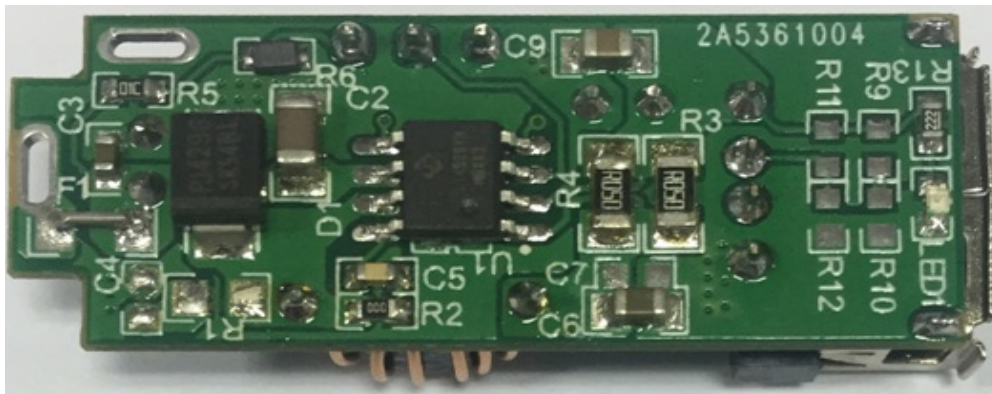


Figure 2 – EVK Picture - Bottom

## Setup

### Required Equipment

EA4529YH-T1011 EVK

Power supply – 40V @ 4A for full power operation

Oscilloscope – >100MHz, >2 channels

Loads –Electronic/resistive load with 3.5A minimum current capability.

Digital Multimeters (DMM)

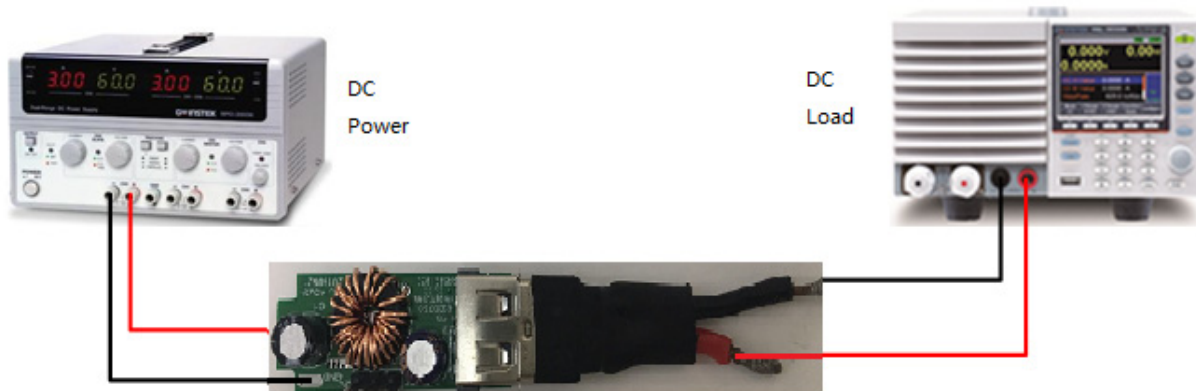


Figure 3 – EVK Setup

## Hardware Setup

1. Connect a DC Power to the Vin and GND on the left side of the EVK.
2. Using a USB connector, connect the EVK output to an electronic load.
3. Recommended Operating Conditions

Table 1. Recommended Operating Conditions

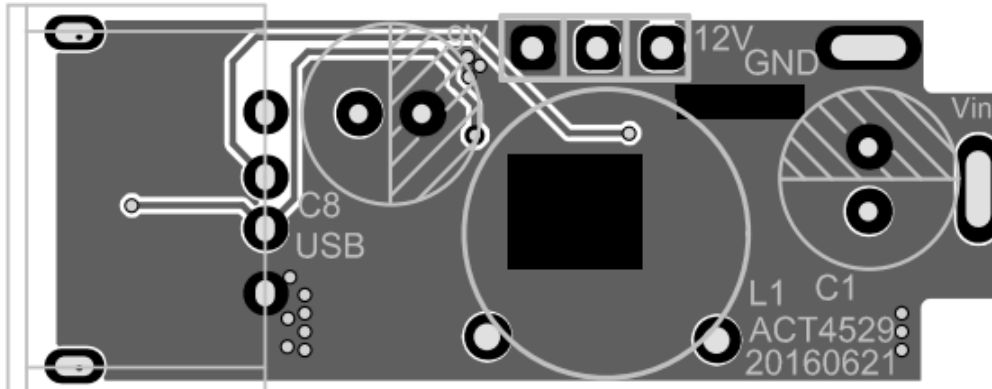
Parameter	Description	Min	Typ	Max	Unit
VIN	All buck input voltages	10	12	36	V
IOUT	Maximum load current		2.4		A

## EVK Operation

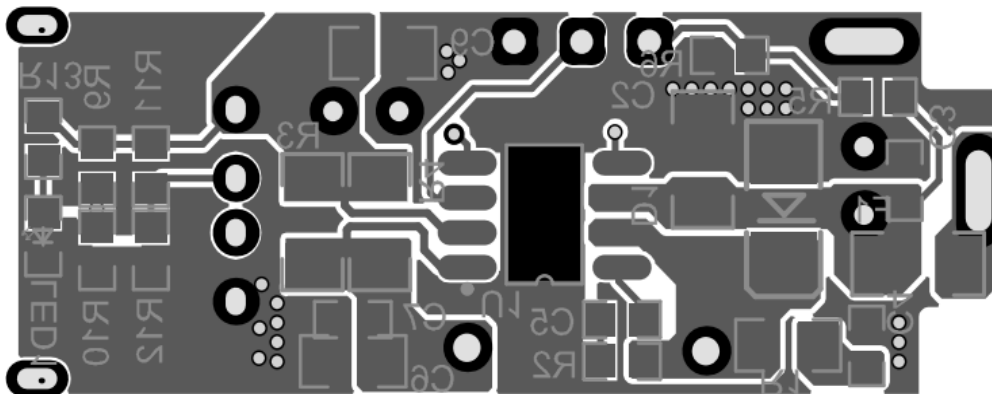
### Turn on

Apply the 12V input voltage. LED1 turn on to indicate that the buck converter is operational.

### PCB Layout



Top Layer

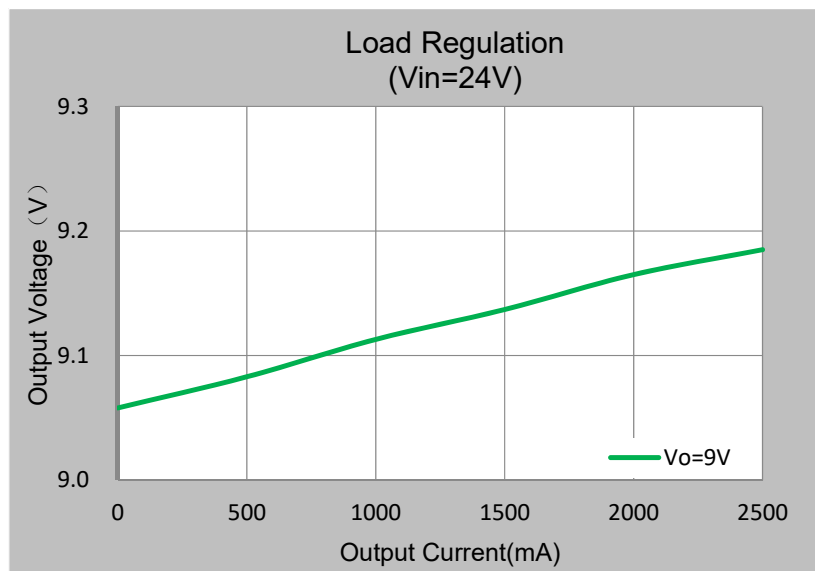
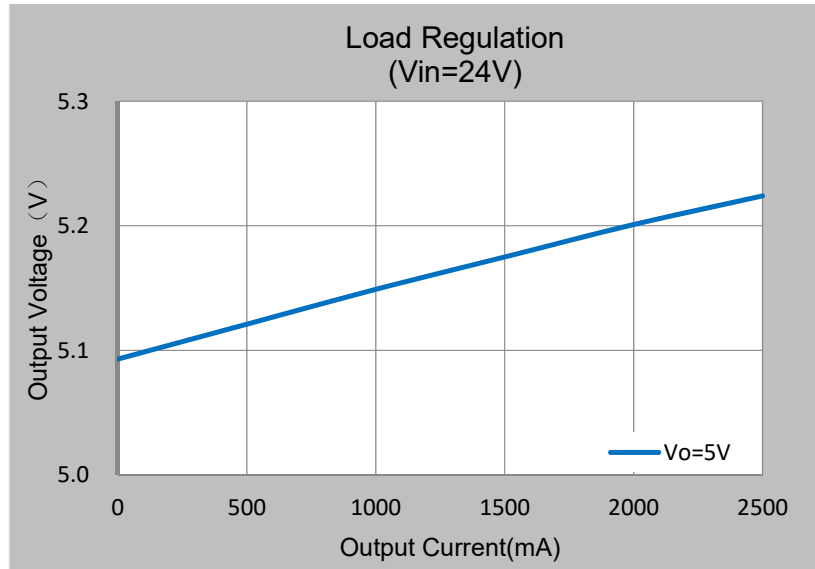


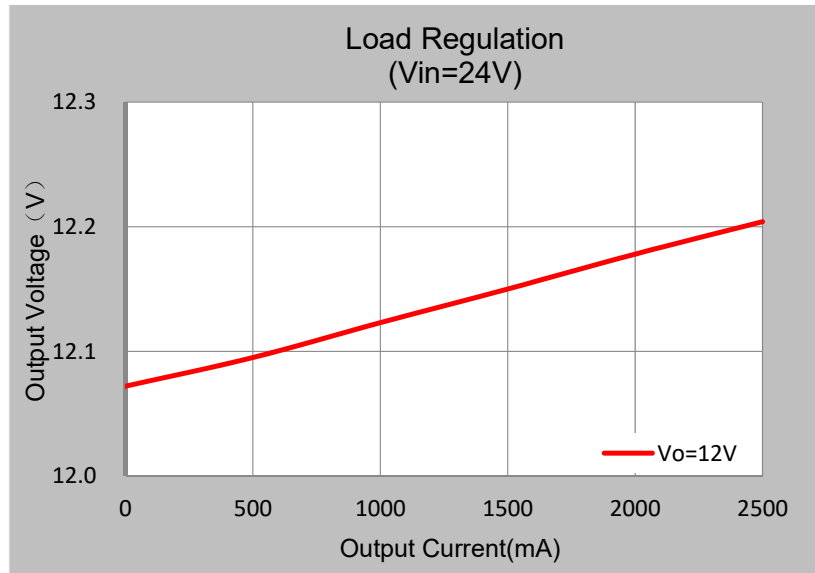
Bottom Layer

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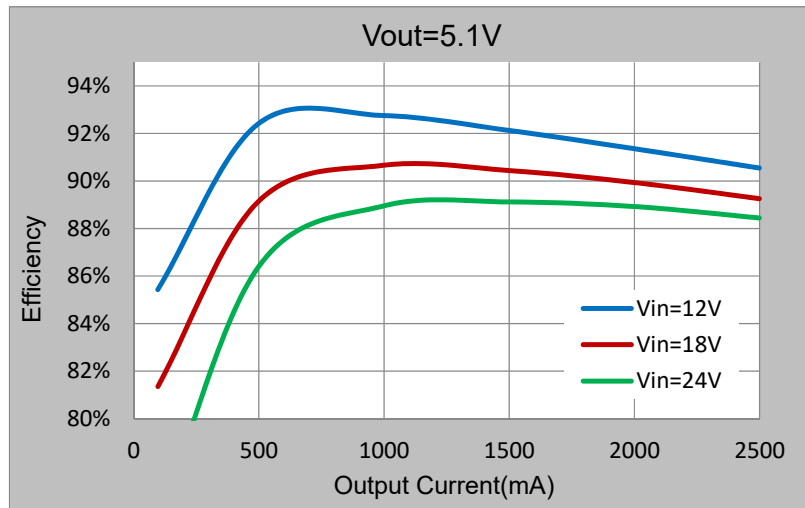
## Test Results

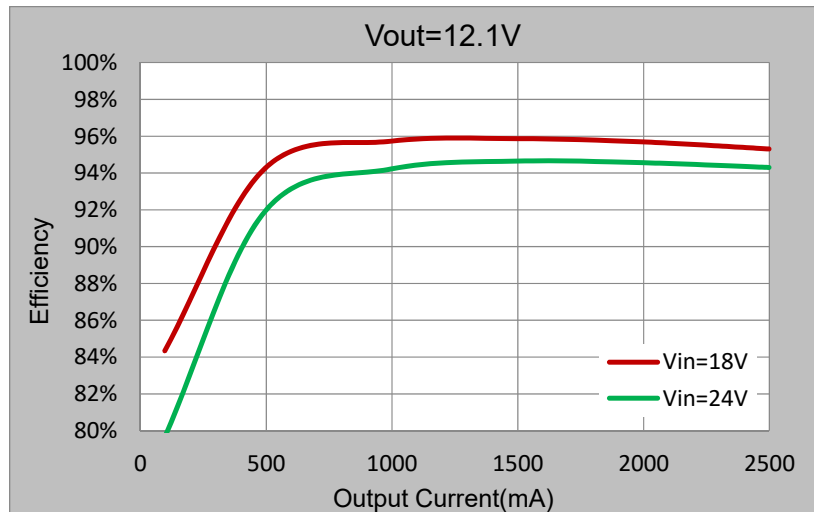
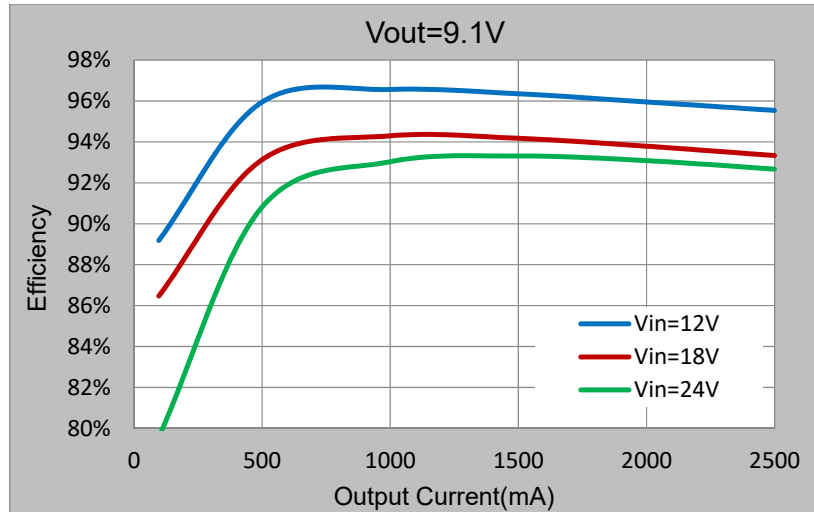
### Output Regulation



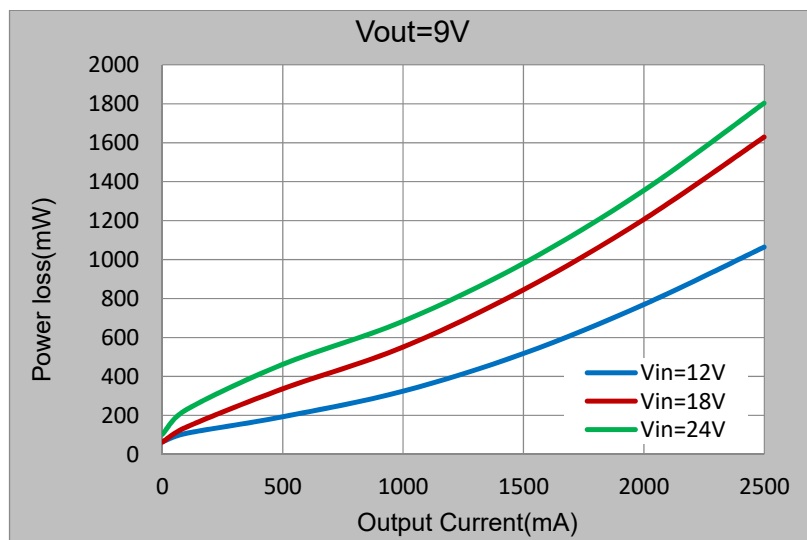
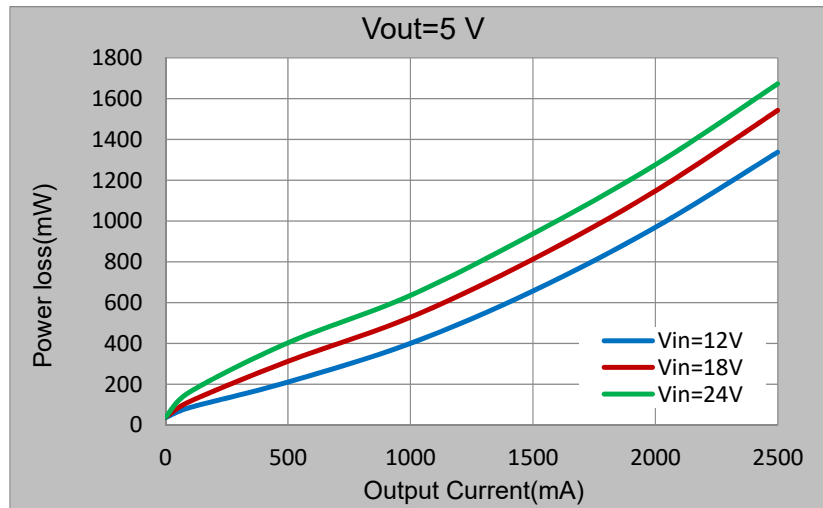


**Efficiency ( $T_a=25^{\circ}C$ )**

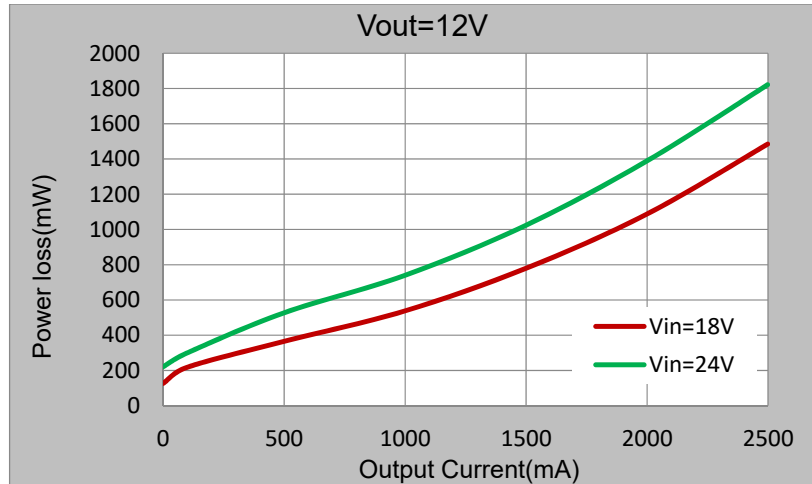




**Power Loss**



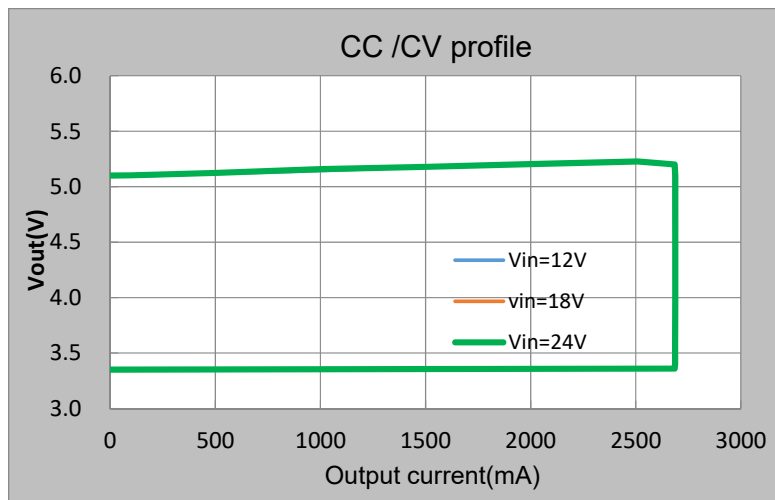




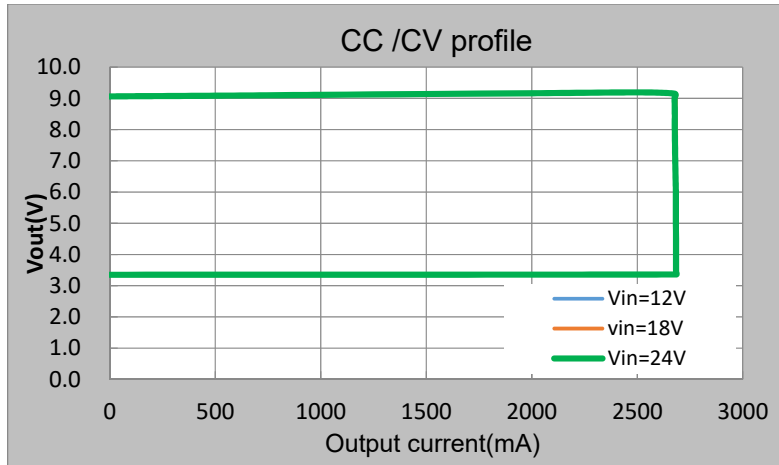
**Output Constant Current and Constant Voltage ( $T_a=25^{\circ}C$ )**

Set DC load to CC mode, increase output current from 0A to the maximum load current and measure the voltage on the C3 capacitor. Then set DC electronic load to CV mode, decrease output voltage from maximum load voltage to 0V and measure the output current and the output voltage on the C3 capacitor.

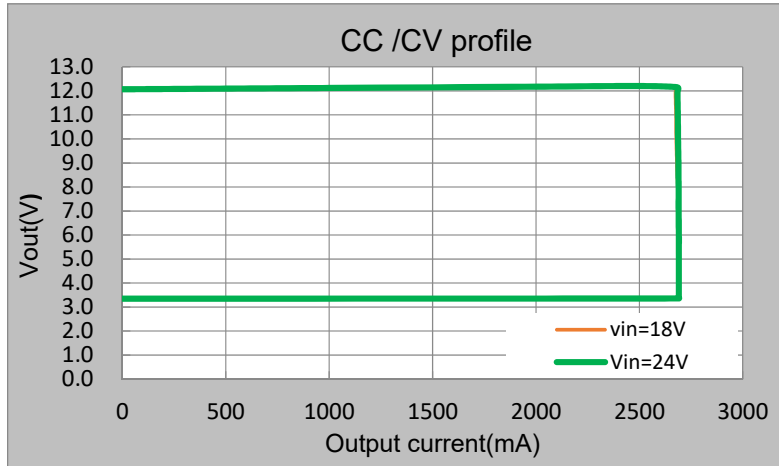
$V_o=5V$



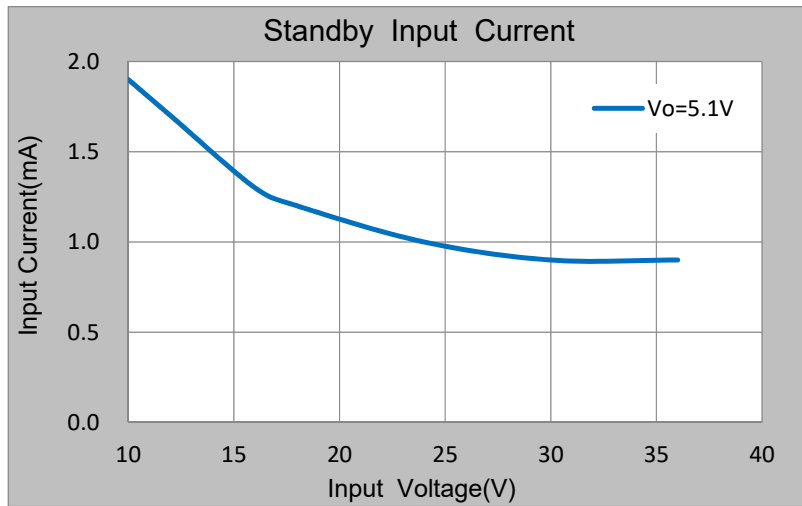
$V_o=9V$



Vo=12V

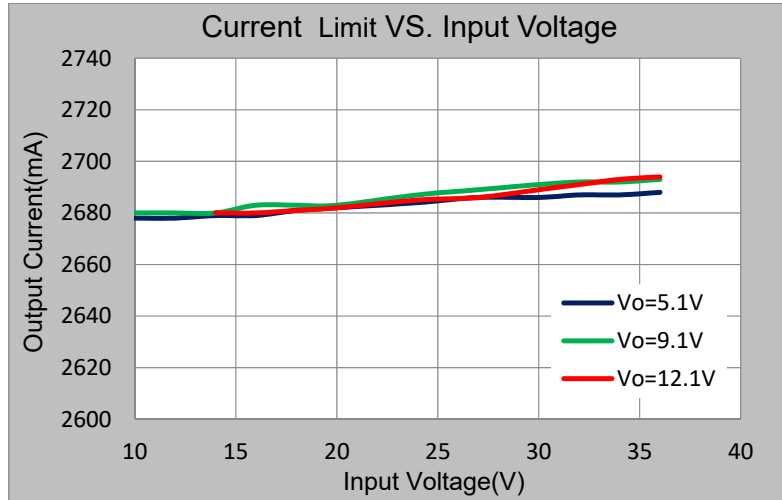


**Standby Input Current**



**Current Limit vs. input voltage**

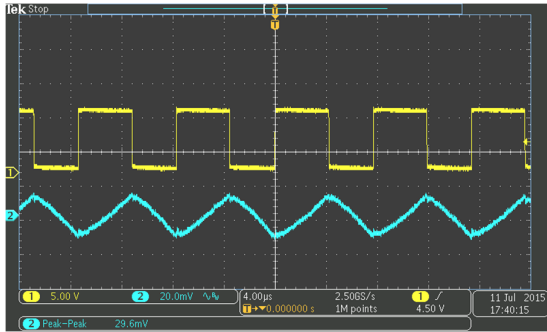
Set DC electronic load to CV mode to measure the output current.



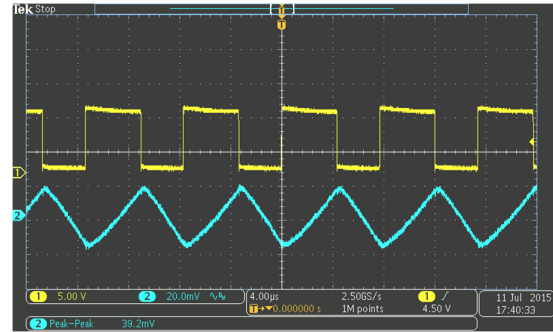
**Ripple**

CH1:Vsw, CH2:Vout

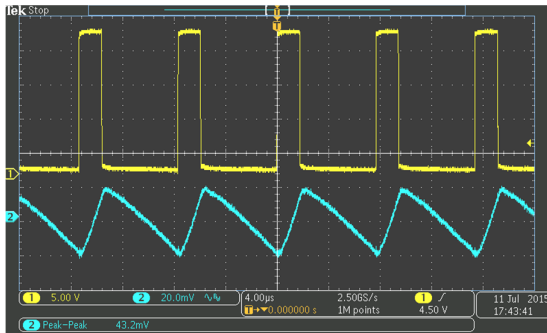
Vin=10V Vout=5.1V Iout=1A



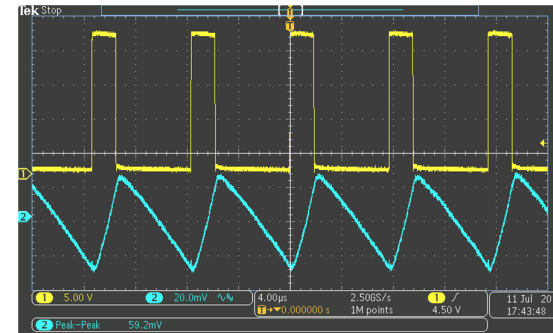
Vin=10V Vout=5.1V Iout=2.4A



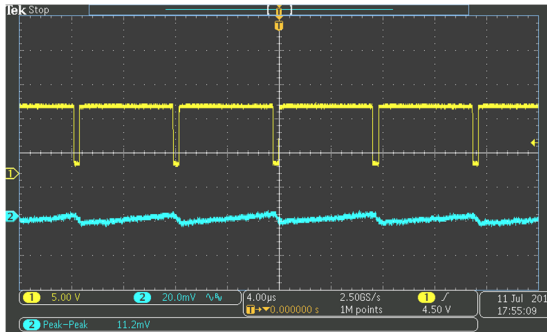
Vin=24V Vout=5.1V Iout=1A



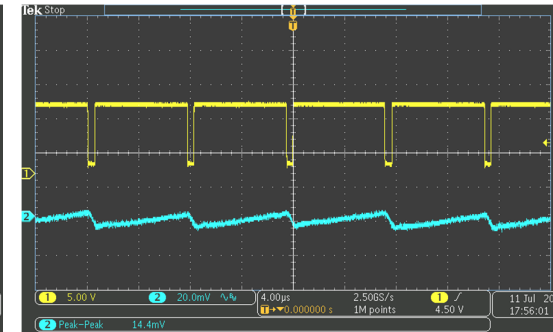
Vin=24V Vout=5.1V Iout=2.4A



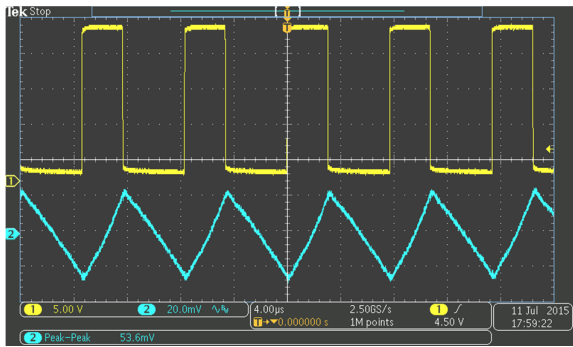
Vin=10V Vout=9.1V Iout=1A



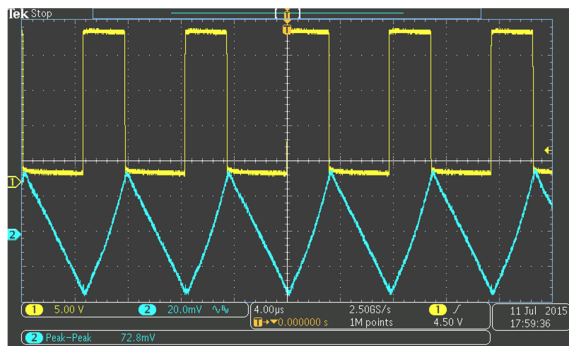
Vin=24V Vout=9.1V Iout=2.4A



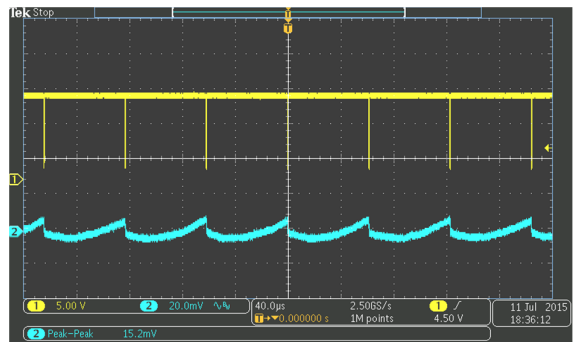
Vin=24V Vout=9.1V Iout=1A



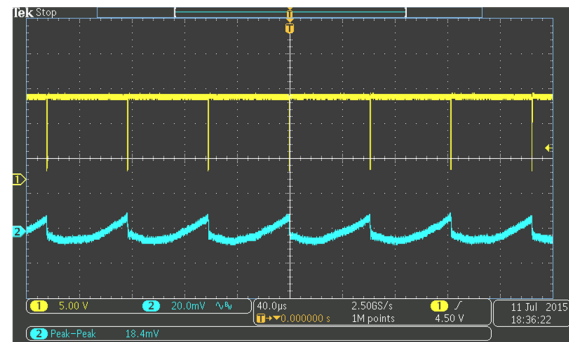
Vin=24V Vout=9.1V Iout=2.4A



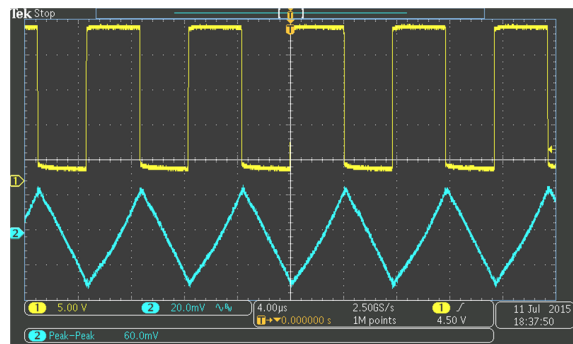
Vin=12V Vout=12.1V Iout=1A



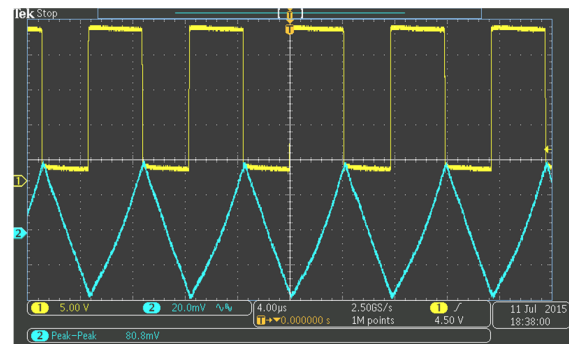
Vin=12V Vout=12.1V Iout=2.4A



Vin=24V Vout=12.1V Iout=1A



Vin=24V Vout=12.1V Iout=2.4A



Ripple is measured by using 20MHz bandwidth limited oscilloscope.

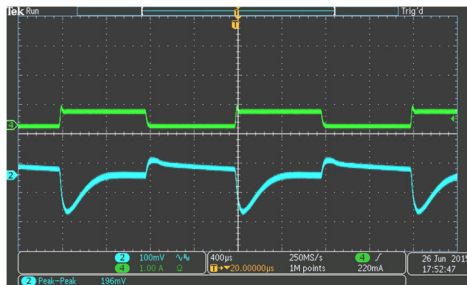
Test Conditions		Output Ripple(mV)	
Vout(V)	Vin(V)	Io=1A	Io=2.4A
5.1	10	30	39
	24	43	59
9.1	10	11	14
	24	54	73
12.1	12	15	18
	24	60	80

### Load Dynamic Response

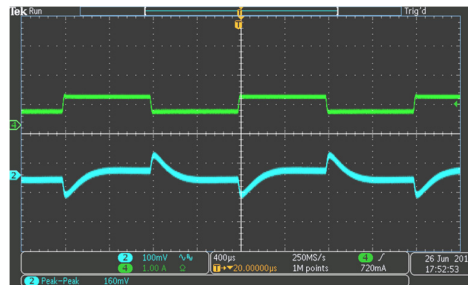
CH2:Vout ripple, CH4:Iout

Vout=5V

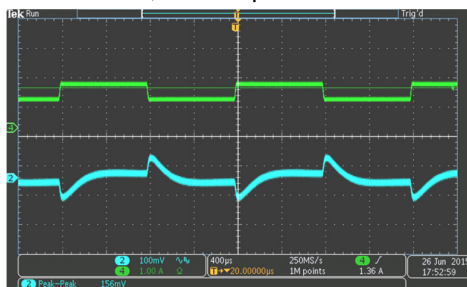
Vin=24V, load step 0A-0.5A-0A



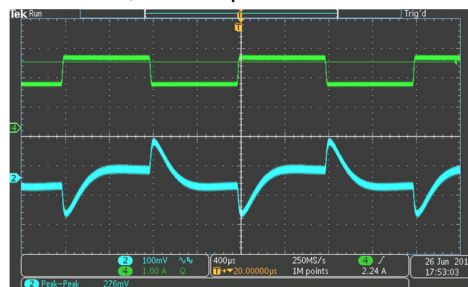
Vin=24V, load step 0.5A-1A-0.5A



Vin=24V, load step 1A-1.5A-1A



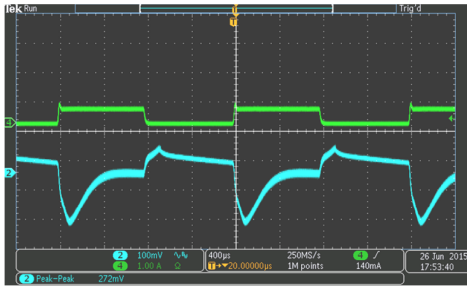
Vin=24V, load step 1.5A-2.4A-1.5A



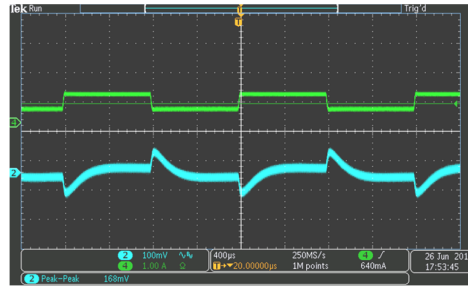
Vout=9V

Vin=24V, load step 0A-0.5A-0A

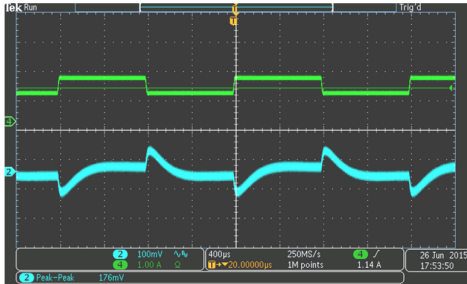
Vin=24V, load step 0.5A-1A-0.5A



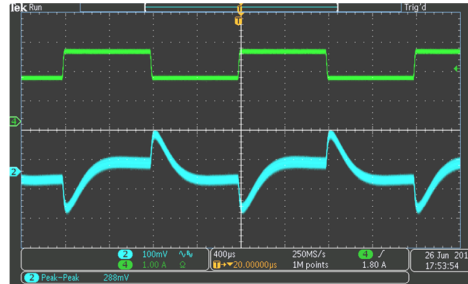
Vin=24V, load step 1A-1.5A-1A



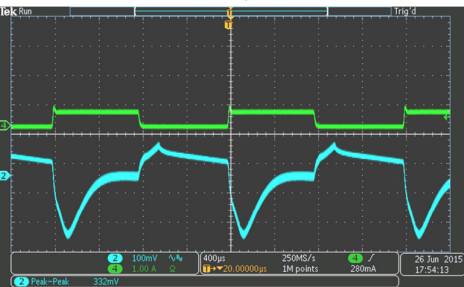
Vin=24V, load step 1.5A-2.4A-1.5A



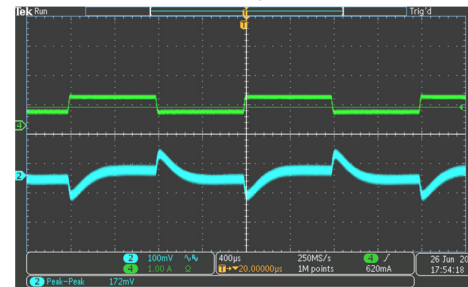
Vout=12V  
Vin=24V, load step 0A-0.5A -0A



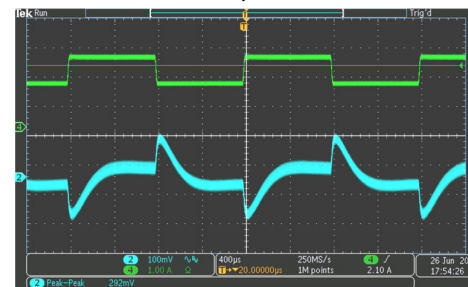
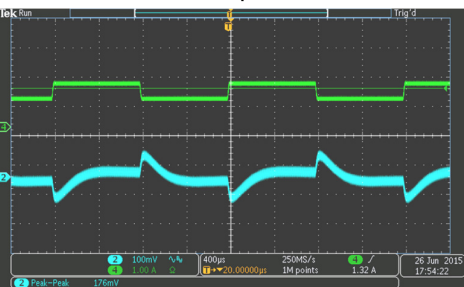
Vin=24V, load step 0.5A-1A-0.5A



Vin=24V, load step 1A-1.5A-1A

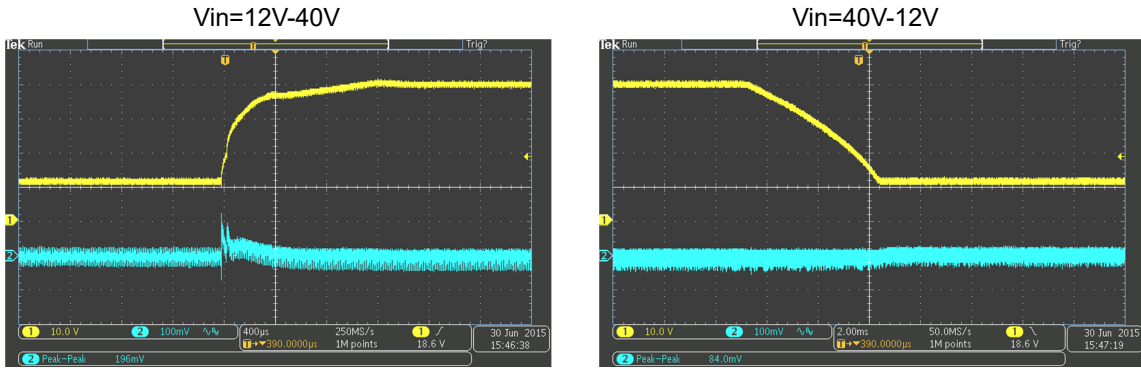


Vin=24V, load step 1.5A-2.4A-1.5A



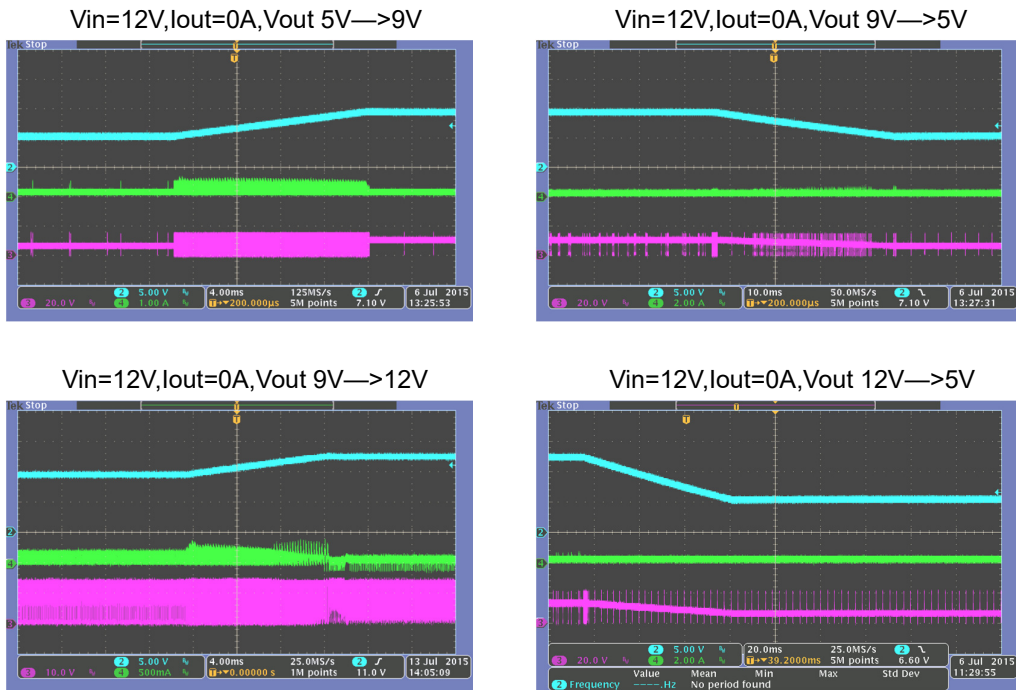
**Line Dynamic Response(Vin change from 12V to 40V, 0.1V/us)**

CH1:Vin, CH2:Vout ripple , output 5V/2.4A



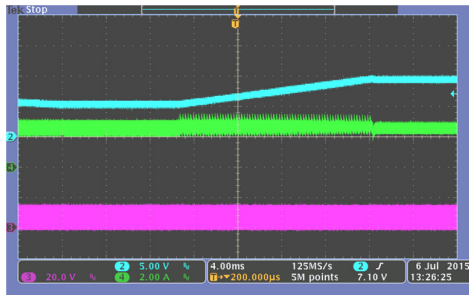
**Output Voltage Transient**

CH2:Vout,CH3:Vsw,CH4:IL

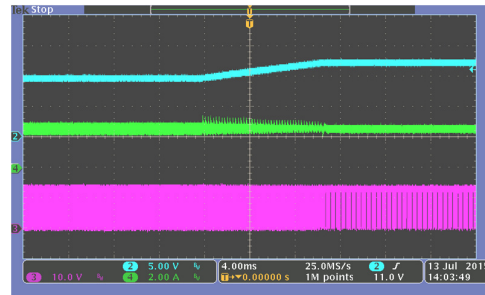




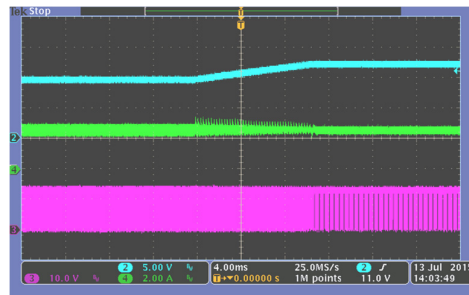
Vin=12V,Iout=2.4A,Vout 5V—>9V



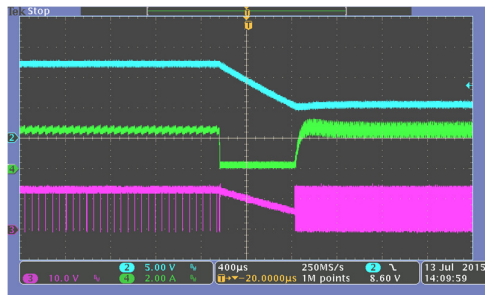
Vin=12V,Iout=2.4A,Vout 9V—>5V



Vin=12V,Iout=2.4A,Vout 9V—>12V



Vin=12V,Iout=2.4A,Vout 12V—>5V



**Key Components Temperature Test**  
 (Ta=25deg C, after 2 hour steady state operation)

Vout=5V

Vin/lout	Ambient (°C)	PCB (°C)	IC (°C)	Schottky (°C)	Inductor (°C)
12V/2.4A	32	91	94	96	96
16V/2.4A	34	96	98	103	102
24V/2.4A	32	100	103	108	107

Vout=9V

Vin/lout	Ambient (°C)	PCB (°C)	IC (°C)	Schottky (°C)	Inductor (°C)
12V/2A	32	75	74	73	71
16V/2A	35	83	84	86	85
24V/2A	34	91	92	95	92

Vout=12V

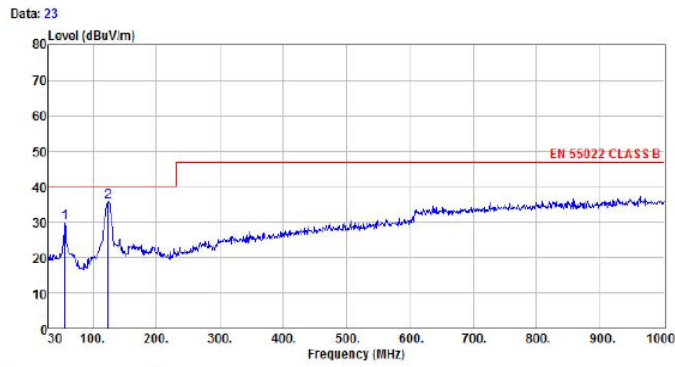
Vin/lout	Ambient (°C)	PCB (°C)	IC (°C)	Schottky (°C)	Inductor (°C)
12V/2A	36	71	74	65	71
16V/2A	37	80	84	79	82
24V/2A	38	91	97	91	94



**EMI TEST**

**Output=5V**

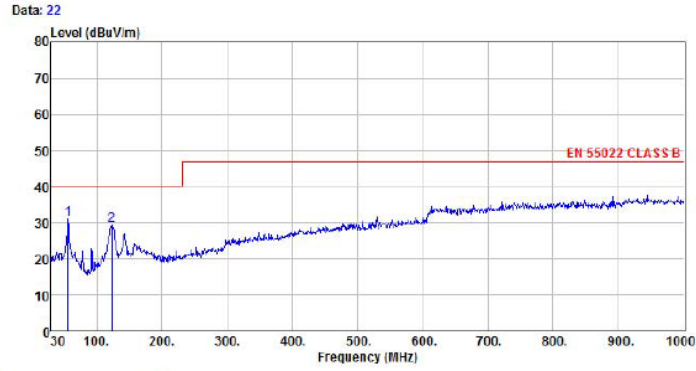
Vin=12V Output 5V2.4A Horizontal



Data: 23  
 Site : chamber  
 Condition : EN 55022 CLASS B 3m VULB9160 HORIZONTAL  
 EUT :  
 Model Name : 5  
 Temp/Humi : 25°C / 53 %  
 Power Rating: AC 230V/50Hz  
 Mode :  
 Memo :

	Freq	ReadAntenna	Cable	Preamp	Level	Limit	Over		
	MHz	Level	Loss	Loss	dB	Line	Limit	Remark	Factor
	MHz	dBuV	dB/m	dB	dB	dBuV/m	dBuV/m	dB	dB/m
1	55.22	16.32	12.40	1.00	0.00	29.72	40.00	-10.28 Peak	13.40
2 pp	124.09	21.86	12.27	1.51	0.00	35.64	40.00	-4.36 Peak	13.78

Vin=12V Output 5V2.4A Vertical

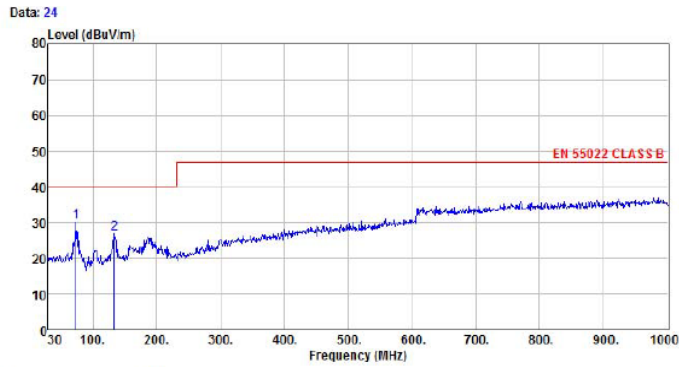


Site : chamber  
 Condition : EN 55022 CLASS B 3m VULB9160 VERTICAL  
 EUT :  
 Model Name : 5  
 Temp/Humi : 25°C / 53 %  
 Power Rating: AC 230V/50Hz  
 Mode :  
 Memo :

	Freq	ReadAntenna Level	Antenna Factor	Cable Loss	Preamp Factor	Level	Limit Line	Over Limit	Remark	Factor
	MHz	dBuV	dB/m	dB	dB	dBuV/m	dBuV/m	dB		dB/m
1	pp	55.22	17.71	12.40	1.00	0.00	31.11	40.00	-8.89 Peak	13.40
2		123.12	15.33	12.27	1.50	0.00	29.10	40.00	-10.90 Peak	13.77

**Output=9V**

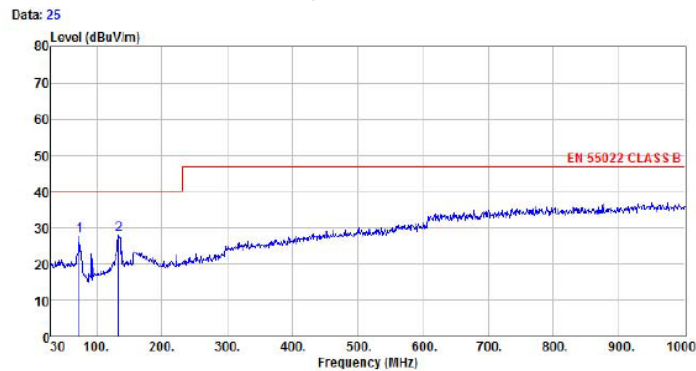
Vin=12V Output 9V2.4A Horizontal



Site : chamber  
 Condition : EN 55022 CLASS B 3m VULB9160 HORIZONTAL  
 EUT :  
 Model Name : 7  
 Temp/Humi : 25°C / 53 %  
 Power Rating: AC 230V/50Hz  
 Mode :  
 Memo :

	Freq	ReadAntenna	Cable	Preamp	Limit	Over				
	MHz	Level	Loss	Factor	Level	Line	Limit	Remark	Factor	
	MHz	dBuV	dB/m	dB	dB	dBuV/m	dBuV/m	dB	dB/m	
1	pp	73.65	19.13	9.87	1.12	0.00	30.12	40.00	-9.88 Peak	10.99
2		133.79	12.29	12.92	1.61	0.00	26.82	40.00	-13.18 Peak	14.53

Vin=12V Output 9V2.4A Vertical



Site : chamber  
 Condition : EN 55022 CLASS B 3m VULB9160 VERTICAL  
 EUT :  
 Model Name : 7  
 Temp/Humi : 25°C / 53 %  
 Power Rating: AC 230V/50Hz  
 Mode :  
 Memo :

	Freq	ReadAntenna	Cable	Preamp	Limit	Over				
	MHz	Level	Loss	Factor	Level	Line	Limit	Remark	Factor	
	MHz	dBuV	dB/m	dB	dB	dBuV/m	dBuV/m	dB	dB/m	
1		73.65	16.69	9.87	1.12	0.00	27.68	40.00	-12.32 Peak	10.99
2	pp	133.79	13.50	12.92	1.61	0.00	28.03	40.00	-11.97 Peak	14.53

**Schematic**

