Gain Equalizers

EQY-XX-24+ Series

50 Ω DC to 20 GHz



CASE STYLE: MC1631-1

The Big Deal

- Excellent Return Loss, 20dB typ.
- Wide bandwidth, DC 20 GHz
- Small Size, 2 mm x 2 mm

Product Overview

EQY-XX-24+ Series of absorptive Gain Equalizers are fabricated using highly repetitive GaAs IPD* MMIC process incorporating resistors, capacitors and inductors having negative insertion loss slope. EQYs are available with nominal attenuation slope of 0,2,3,5,6,8,10,12 dB. They are packaged in tiny 2 x 2 mm 8-Lead MCLPTM package.

Key Features

Feature	Advantages		
Negative Insertion Loss Slope vs. Frequency	Useful for compesating negative gain slope of amplifiers, receivers, transmitters to achieve flat gain versus frequency.		
Wide range of values 0,2,3,5,6,8,10,12 dB	Enables circuit designer to change nominal insertion loss values without mother-board redesign making the EQY-XX-24+ Series ideal for select at test application.		
Wideband operation, DC to 20 GHz	Supports a wide array of applications including wireless cellular, microwave communications, satellite, defense and aerospace, medical broadband and optic applications.		
Excellent Power Handling Capability	Enables its use at the output of a variety of amplfiers		
Small Size and simple to use (2 mm x 2 mm)	As a single chip solution, the EQY-XX-24+ Series occupies less board space than a lumped element approach, minimizes component count and ensures repeatable performance over wide frequency range.		

^{*}GaAs IPD (Gallium Arsenide Integrated Passive Device)

EQY-10-24+

 50Ω 10dB DC to 20 GHz

Product Features

- 10.2 dB Slope
- Small Package 2 x 2 mm MCLP
- Wide Bandwidth, DC-20 GHz
- Excellent Return Loss, 20 dB typ.

Typical Applications

- Fixed Satellite
- Mobile
- Radio location
- Space research



Generic photo used for illustration purposes only

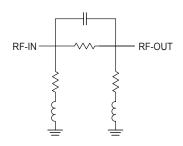
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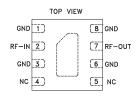
+RoHS Compliant
The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

General Description

EQY-10-24+ is an absorptive Gain Equalizer fabricated using highly repetitive GaAs IPD MMIC process incorporating resistors, capacitors and inductors having negative insertion loss slope. EQY-10-24+ has a nominal attenuation slope of 10.2 dB and is packaged in tiny 2 x 2 mm, 8-Lead MCLPTM package.

simplified schematic & pad description





Function	Pad Number	Description
RF-IN	2	RF-Input pad
RF-OUT	7	RF-Output pad
GND	1,3,6,8 & Paddle	Ground
NC	4,5	No connection, connected to ground externally

Electrical Specifications¹ at 25°C, 50 Ω , unless otherwise noted.

Parameter	Condition (GHz)	Min.	Тур.	Max.	Units
Frequency Range		DC		20	GHz
Insertion Loss	0.01	10.8	11.1	11.4	dB
	5	_	9.6	_	
	10	_	5.8	_	
	18	1.2	1.4	1.8	
	20	_	0.9	_	
VSWR	0.01 -5	_	1.11	_	:1
	5 - 10	_	1.18	_	
	10 - 18	_	1.27	_	
	18 - 20	_	1.09	_	

^{1.} Measured on Mini-Circuits Characterization Test Board TB-EQY-10-24+. See Characterization Test Circuit (Fig. 1)

Absolute Maximum Ratings²

Operating Case Temperature	-55°C to 105°C		
Storage Temperature	-65°C to 150°C		
RF Input Power ³	33 dBm		

^{2.} Permanent damage may occur if any of these limits are excedeed.
3. Derates linearly to 31 dBm at 105°C

Characterization Test Circuit

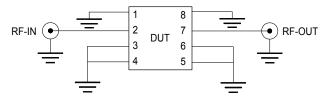
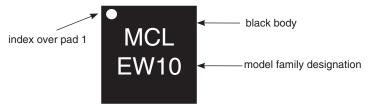


Fig 1. Block Diagram of Test Circuit used for characterization. Test Board TB-EQY-10-24+ Conditions: Attenuation & Return Loss Pin=0 dBm

Product Marking



Marking may contain other features or characters for internal lot control



Typical Performance Data at 25°C

Frequency (MHz)	Insertion Loss (dB)	Input VSWR (:1)	Output VSWR (:1)
10	11.13	1.11	1.11
1000	11.06	1.09	1.09
2000	10.83	1.07	1.07
4000	10.08	1.16	1.18
5000	9.60	1.24	1.27
6000	8.98	1.30	1.33
8000	7.37	1.19	1.20
10000	5.75	1.08	1.10
12000	4.42	1.37	1.37
14000	3.27	1.48	1.50
15000	2.71	1.42	1.45
16000	2.20	1.32	1.35
17000	1.77	1.20	1.21
18000	1.39	1.11	1.09
19000	1.11	1.09	1.09
20000	0.90	1.13	1.13

