

### **Product data sheet**

## 1. General description

The ESDALD05UJ2 is a low capacitance TVS (Transient Voltage Suppressor) array designed to protect high speed data interfaces. It has been specifically designed to protect sensitive electronic components which are connected to data and transmission lines from over-stress caused by ESD (Electrostatic Discharge).

### 2. Features and benefits

- Peak pulse power 136W @ 8/20µs waveform
- Protects two I/O lines and one  $V_{cc}$  line •
- IEC 61000-4-2 (ESD) ±15kV(air), ±8kV(contact)
- IEC 61000-4-5 (Lightning) 5A (8/20µs)
- Low capacitance
- Low leakage current •
- 5V operating voltage
- Solid-state silicon avalanche technology
- Device meets MSL 1 requirements
- . Halogen free and RoHS compliant



大 ✯

1

VDD

4

I/O2

3

2

Unit

W А kV kV °C °C

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## 3. Applications

- xDSLI •
- **USB 2.0** •
- **IEEE 1394 Firewire Ports**
- Notebooks & Handhelds
- Projection TV & Monitors •
- Set-top box
- Flat Panel Displays •

# 4. Ordering information

Type number	Package Name	Orderable part number	Packing method	Small packing quantity	Marking	Package issue date
ESDALD05UJ2	SOT143	ESDALD05UJ2X	Tape and reel	3000	R05	13-Oct-2020

### 5. Absolute maximum ratings

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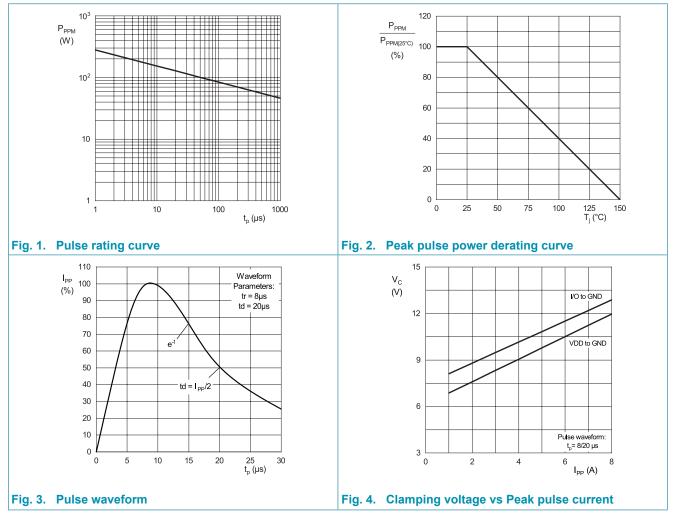
In accordance with the Absolute Maximum Rating System (IEC 60134). $T_i = 25 ^{\circ}$ C unless otherwise specified.				
Symbol	Parameter	Conditions	Values	
Absolute	Absolute maximum rating			
P <sub>PPM</sub>	peak pulse power	t <sub>p</sub> = 8/20 μs	136	
I <sub>PP</sub>	peak pulse current	t <sub>p</sub> = 8/20 μs	8	
V <sub>ESD</sub>	ESD per IEC 61000-4-2 (air) ESD per IEC 61000-4-2 (contact)		±15 ±8	
T <sub>stg</sub>	storage temperature range		-55 to 150	
Tj	operating temperature range		-55 to 150	
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**ESD Protection Diodes Array** 

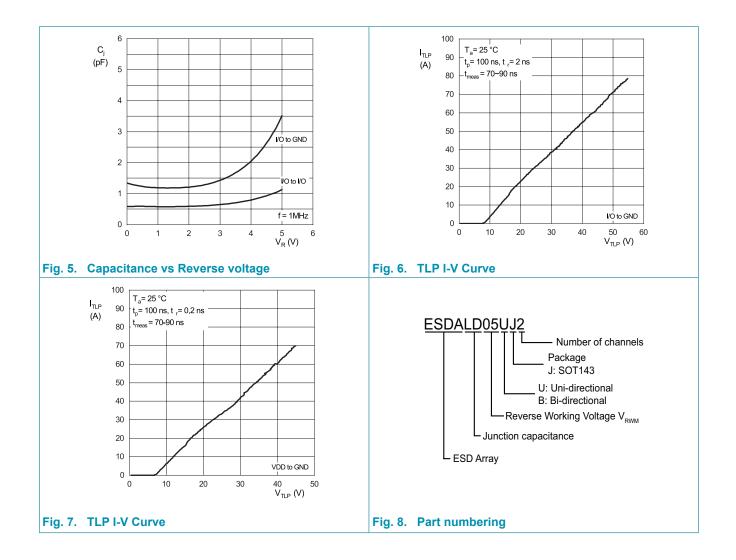
## 6. Characteristics

$T_j$ = 25 °C unless otherwise specified.						
Symbol	Parameter	Condition	Min	Тур	Мах	Unit
V <sub>RWM</sub>	Reverse Working Voltage	Any I/O pin to GND	-	-	5	V
$V_{\text{BR}}$	Reverse Breakdown Voltage	$I_{T}$ = 1 mA; Any I/O pin to GND	6	-	-	V
I <sub>R</sub>	Reverse Leakage Current	V <sub>RWM</sub> = 5 V; Any I/O pin to GND	-	-	100	nA
V <sub>F</sub>	Diode Forward Voltage	I <sub>F</sub> = 15 mA	-	0.85	1.2	V
V <sub>c</sub>	Clamping Voltage	$I_{PP}$ = 1 A; $t_p$ = 8/20 µs; Any I/O pin to GND	-	-	12	V
		$I_{PP}$ = 5 A; $t_p$ = 8/20 µs; Any I/O pin to GND	-	-	15	V
		$I_{PP}$ = 8 A; $t_p$ = 8/20 µs; Any I/O pin to GND	-	-	17	V
CJ	Junction Capacitance	V <sub>R</sub> = 0 V; f = 1 MHz; Between I/O pins	-	0.6	0.8	pF
		$V_R = 0 V$ ; f = 1 MHz; Any I/O pin to GND	-	1.35	-	pF

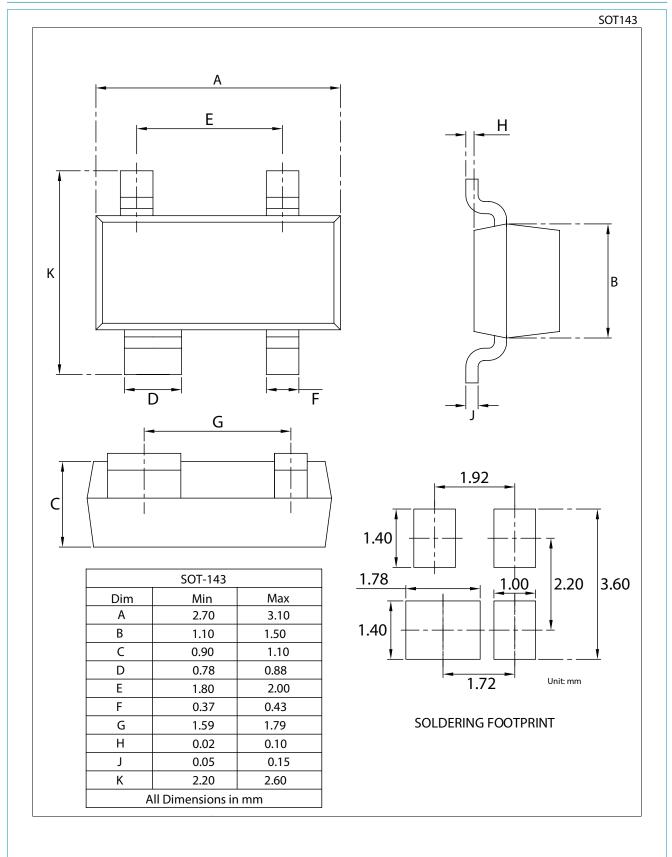
Note: I/O pins are pin2,3



ESD Protection Diodes Array



# 7. Package outline



ESDALD05UJ2
Product data sheet

## ESDALD05UJ2

### **ESD Protection Diodes Array**

## 8. Legal information

#### **Data sheet status**

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.ween-semi.com</u>.

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