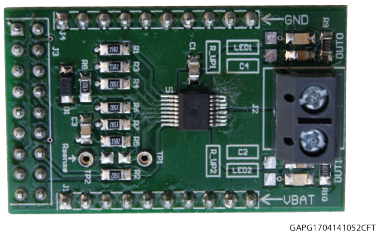
EV-VND7E050AJ

Data brief

VND7E050AJ evaluation board



GAPG170414052CFT

Features

Max transient supply voltage	V_{CC}	40 V
Operating voltage range	V_{CC}	4 to 28 V
Typ. on-state resistance (per Ch)	R_{ON}	50 mΩ
Current limitation (typ)	I_{LIMH}	40 A
Standby current (max)	I_{STBY}	0.5 µA
Minimum cranking supply voltage (V_{CC} decreasing)	$V_{USD_cranking}$	2.85 V

- Simple single IC application board dedicated for VND7E050AJ
- Provides electrical connectivity and thermal heat-sinking for easy prototyping
- General device features
  - Double channel smart high-side driver with CurrentSense analog feedback
  - Very low standby current
  - Compatible with 3 V and 5 V CMOS outputs
  - Extreme low voltage operation for deep cold cranking applications (compliant to LV124, revision 2013)
- Diagnostic functions
  - Analog feedback of load current with high precision proportional current mirror
  - Overload and short to ground (power limitation) indication
  - Thermal shutdown indication
  - OFF-state open-load detection
  - Output short to V_{CC} detection
  - Sense enable/disable
- Protections
  - Undervoltage shutdown
  - Overvoltage clamp
  - Load current limitation
  - Self limiting of fast thermal transients
  - Configurable latch-off on overtemperature or power limitation with dedicated fault reset pin
  - Loss of ground and loss of V_{CC}
  - Reverse battery with external components
  - Electrostatic discharge protection

Product status link
EV-VND7E050AJ

Order code	References
EV-VND7E050AJ	EV-VND7E050AJ evaluation board

Application

- Automotive resistive, inductive and capacitive loads
- Protected supply for ADAS systems: radars and sensors
- Automotive headlamp

Description

This board provides you an easy way to connect STMicroelectronics VIPower M0-7 technology into your existing system.

The board comes pre-assembled with VND7E050AJ high-side driver. On board minimum set of electrical components (as for device datasheet recommendation) is enabling the user to directly connect the load, the power supply and the microcontroller without any additional effort in external component design and connection.

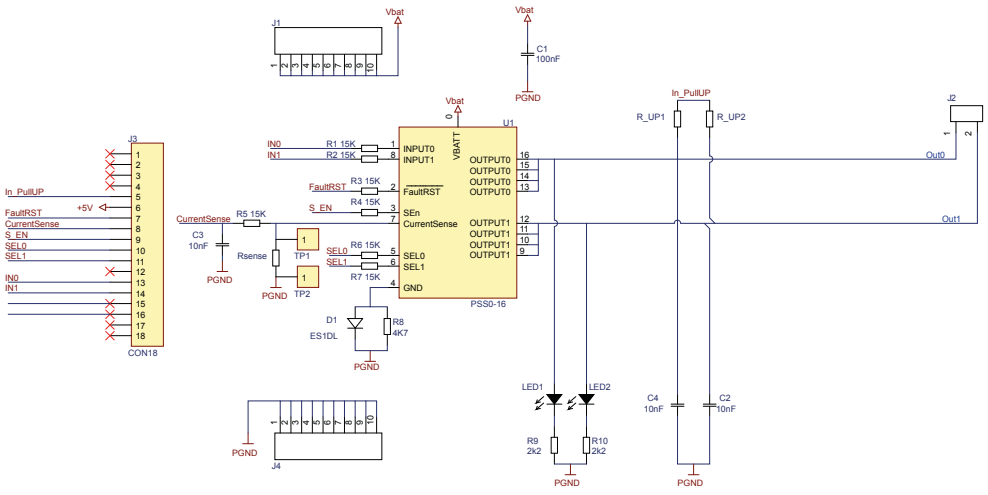
The VND7E050AJ is a double channel high-side driver manufactured using ST proprietary VIPower technology and housed in PowerSSO-16 package. The device is designed to drive 12 V automotive grounded loads through a 3 V and 5 V CMOS-compatible interface and to provide protection and diagnostics.

The device integrates advanced protective functions such as load current limitation, overload active management by power limitation and overtemperature shutdown with configurable latch-off.

A $\overline{\text{FaultRST}}$ pin unlatches the output in case of fault or disables the latch-off functionality.

A dedicated multifunction multiplexed analog output pin delivers sophisticated diagnostic functions including high precision proportional load current sense, supply voltage feedback and chip temperature sense, in addition to the detection of overload and short-circuit to ground, short to V_{CC} and OFF-state open-load. A sense enable pin allows OFF-state diagnosis to be disabled during the module low-power mode as well as external sense resistor sharing among similar devices.

Figure 1. Evaluation board schematic



GADG300920191004FSR

1 Board connections

The figure below shows the placement of the connectors to be used for supplying the evaluation board, connecting the load and controlling the functionality and diagnostic of the device.

Figure 2. Evaluation board connections

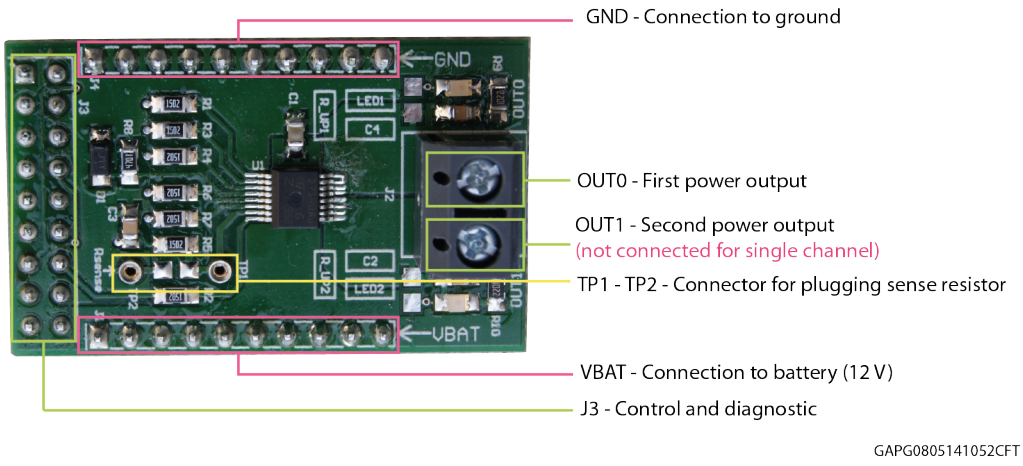


Table 1. J3 connector: pin functions

Connector	Pin number	Pin name	Pin function
J3	1…4	N/A	Not connected
J3	5	IN_PullUP	Connection to optional external pull-up resistor for open load detection in off-state.
J3	6	+5V	5 V Power Supply
J3	7	$\overline{\text{FaultRST}}$	Active low compatible with 3 V and 5 V CMOS outputs pin; it unlatches the output in case of fault; If kept low, sets the outputs in auto-restart
J3	8	CurrentSense	Analog current sense output pin; delivers a current proportional to the selected load current.
J3	9	S_EN	Active high compatible with 3 V and 5 V CMOS outputs pin; it enables the CurrentSense diagnostic pin.
J3	10	SEL0	Active high compatible with 3 V and 5 V CMOS outputs pin; together with SEL1, it addresses the CurrentSense multiplexer
J3	11	SEL1	Active high compatible with 3 V and 5 V CMOS outputs pin; together with SEL0, it addresses the CurrentSense multiplexer
J3	12	N/A	Not connected
J3	13	IN0	Voltage controlled input pin with hysteresis, compatible with 3 V and 5 V CMOS outputs. It controls OUT0 switch state
J3	14	IN1	Voltage controlled input pin with hysteresis, compatible with 3 V and 5 V CMOS outputs. It controls OUT1 switch state. (1)
J3	15…18	N/A	Not connected

1. *Input not available for single channel.*

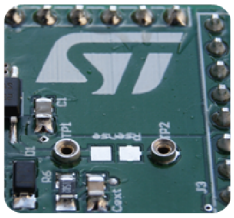
In case the user wishes to utilize the CurrentSense/MultiSense function of the device, it is necessary to plug a sense resistor in R_{SENSE}.

The package includes a through-hole resistor, to be mounted on TP1-TP2 (see Figure 4. Mounting through-hole sense resistor).

Different R_{SENSE} values can be adopted based on user preference.

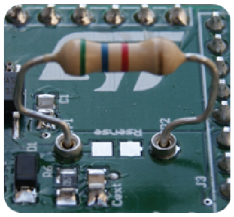
Another option is soldering an SMD resistor on the dedicated PCB pad, as shown in Figure 5. Pads for soldering SMD resistor.

Figure 3. No sense resistor



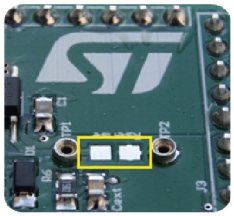
GAPG0904141657CFT

Figure 4. Mounting through-hole sense resistor



GAPG1004141007CFT

Figure 5. Pads for soldering SMD resistor



GAPG1004141008CFT

2 Thermal data

Table 2. Thermal data

Symbol	Parameter	Max	Unit
$R_{thj\text{-}amb}$	Thermal resistance junction-ambient (max)	39	°C/W

Table 3. PCB specifications

Parameter	Value
Board dimensions	25 mm x 41.5 mm
Number of Cu layer	2
Layer Cu thickness	35 µm
Board finish thickness	1.6 mm +/- 10%
Board Material	FR4
Thermal vias separation	1.1 mm
Thermal vias diameter	0.5 mm

Revision history

Table 4. **Document revision history**

Date	Version	Changes
29-Aug-2019	1	Initial release.