

GaAs MMIC SP8T NON-REFLECTIVE POSITIVE CONTROL SWITCH, DC* - 8 GHz

Typical Applications

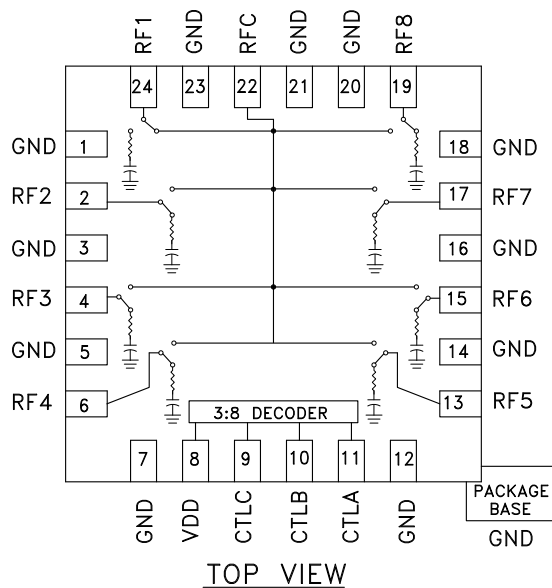
This switch is suitable for usage in DC - 8.0 GHz 50-Ohm or 75-Ohm systems:

- Broadband
- Fiber Optics
- Switched Filter Banks
- Wireless below 8 GHz

Features

- Broadband Performance: DC - 8 GHz
- High Isolation: >30 dB at 6 GHz
- Low Insertion Loss: 2.3 dB at 6 GHz
- Integrated Positive Supply 3:8 TTL Decoder
- 24 Lead 4x4mm QFN Package: 9 mm²

Functional Diagram



General Description

The HMC321ALP4E is a broadband non-reflective GaAs SP8T switch in low cost leadless surface mount packages. Covering DC to 8 GHz, this switch offers high isolation and low insertion loss. This switch also includes an on board binary decoder circuit which reduces the required logic control lines to three. The switch operates using a positive control voltage of 0/+5 volts, and requires a fixed bias of +5 volts. This switch is suitable for usage in 50-Ohm or 75-Ohm systems.

* DC blocking capacitors are required at ports RFC and RF1, 2, 3, 4, 5, 6, 7, 8. Their value will determine the lowest transmission frequency.

Electrical Specifications, $T_A = +25^\circ C$, With 0/+5V Control, 50 Ohm System

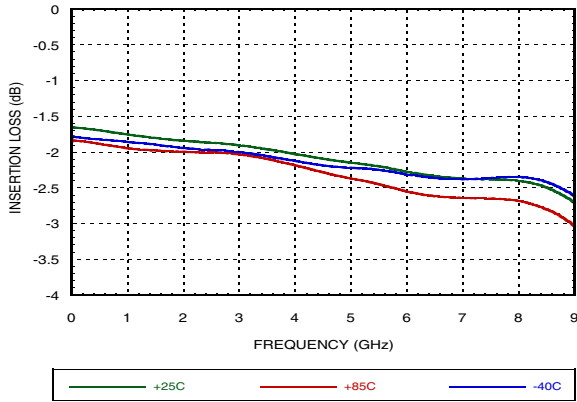
Parameter	Frequency	Min.	Typ.	Max.	Units
Insertion Loss	DC - 2.0 GHz		1.7	1.8	dB
	DC - 4.0 GHz		1.8	1.9	dB
	DC - 8.0 GHz		2.2	3.1	dB
Isolation	DC - 2.0 GHz	45	50		dB
	DC - 4.0 GHz	35	40		dB
	DC - 6.0 GHz	25	30		dB
	DC - 8.0 GHz	20	28		dB
Return Loss	"On State"	DC - 4.0 GHz	12	16	dB
		DC - 8.0 GHz	10	15	dB
Return Loss (RF1 - RF8)	"Off State"	2.0 - 8.0 GHz	12	15	dB
Input Power for 1 dB Compression	0.5 - 8.0 GHz	25	26		dBm
Input Third Order Intercept (Two-tone Input Power = +7 dBm Each Tone, 1 MHz Spacing)	0.5 - 6.0 GHz	35	38		dBm
Switching Characteristics	DC - 8.0 GHz	t_{RISE}, t_{FALL} (10/90% RF)	25		ns
		t_{ON}, t_{OFF} (50% CTL to 10/90% RF)	150		ns

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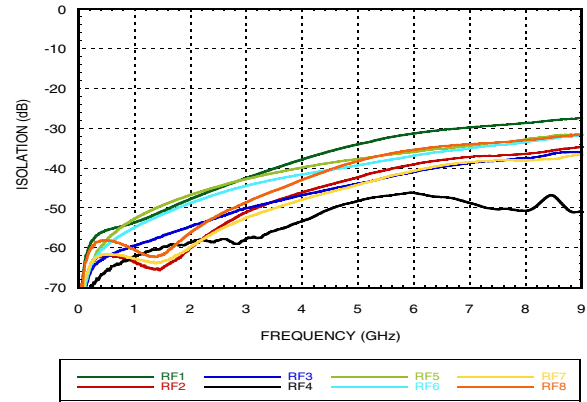
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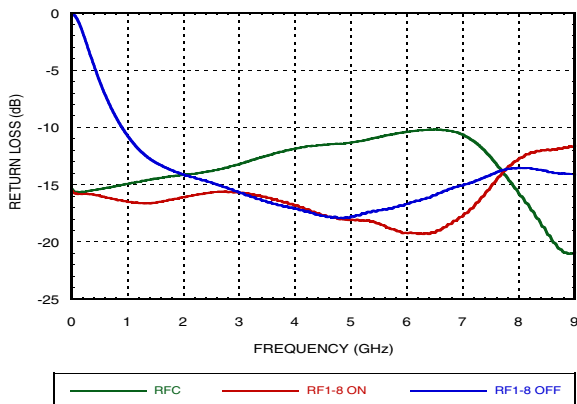
Insertion Loss vs. Temperature



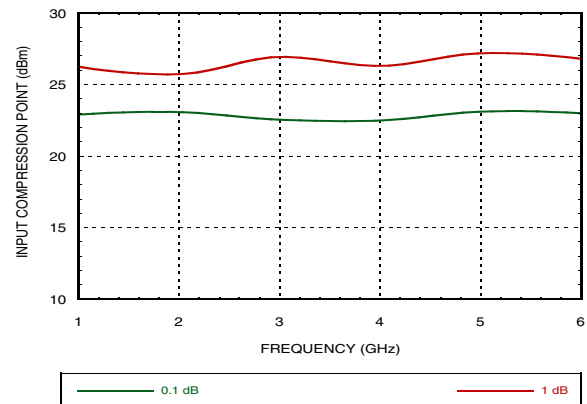
Isolation



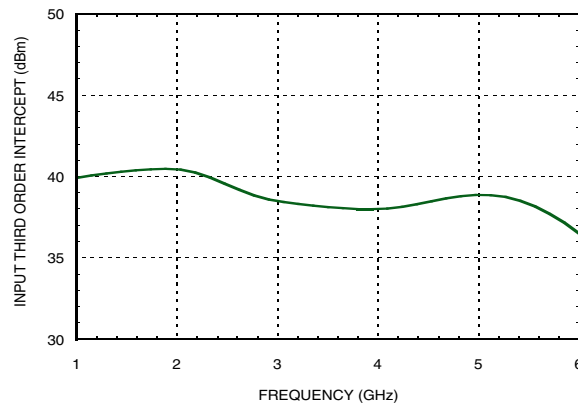
Return Loss



0.1 and 1 dB Input Compression Point



Input Third Order Intercept Point



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Bias Voltage & Current

Vdd Range = +5 Vdc ± 10%		
Vdd (Vdc)	Idd (Typ.) (mA)	Idd (Max.) (mA)
+5	4	8

Control Voltages

State	Bias Condition
Low	0 to +0.8 Vdc at 0 µA Typical
High	+2.0 to +5 Vdc at 20 µA Typical

Truth Table

Control Input			Signal Path State
A	B	C	RFC to:
Low	Low	Low	RF1
High	Low	Low	RF2
Low	High	Low	RF3
High	High	Low	RF4
Low	Low	High	RF5
High	Low	High	RF6
Low	High	High	RF7
High	High	High	RF8

Note:

DC blocking capacitors are required at ports RFC and RF1, 2, 3, 4, 5, 6, 7, 8. Their value will determine the lowest transmission frequency.

Absolute Maximum Ratings

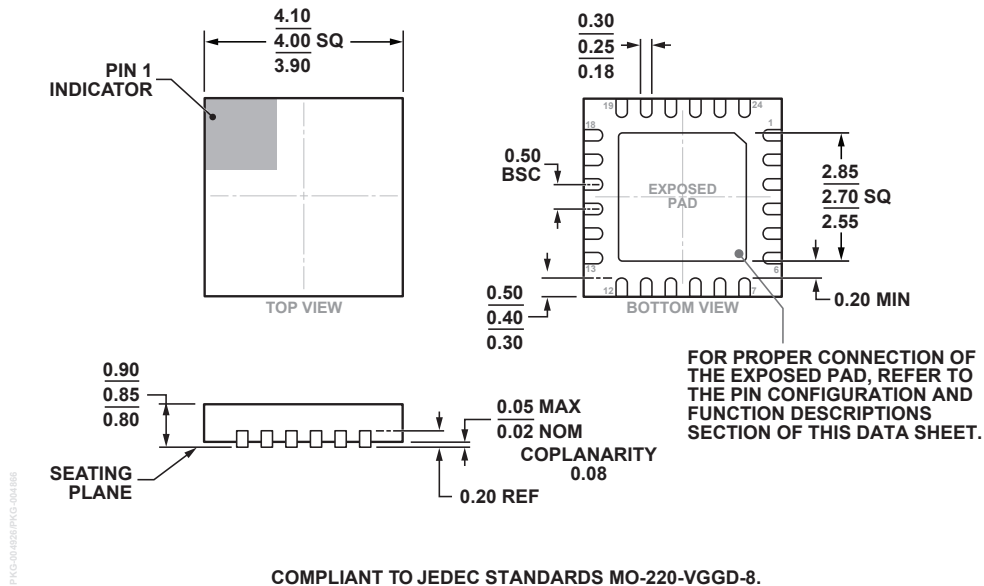
Bias Voltage Range (Port Vdd)	+7.0 Vdc
Control Voltage Range (A, B, & C)	-0.5V to Vdd +0.5 Vdc
Maximum Input Power Vdd = +5V	+26 dBm
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
ESD Sensitivity (HBM)	Class 1A
ESD Sensitivity (FICDM)	Class II



**ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS**

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Outline Drawing



24-Lead Lead Frame Chip Scale Package [LFCSP]
 4 mm x 4 mm Body and 0.85 mm Package Height
 (CP-24-16)
 Dimensions shown in millimeters

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[2]
HMC321ALP4E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL3 ^[1]	H321A XXXX

[1] Max peak reflow temperature of 260 °C

[2] 4-Digit lot number XXXX

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Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 3, 5, 7, 12, 14, 16, 18, 20, 21, 23	GND	Package bottom has exposed metal paddle that must also be connected to PCB RF ground.	
2, 4, 6, 13, 15, 17, 19, 22, 24	RF1 - RF8 & RFC	This pin is DC coupled and matched to 50 Ohm. Blocking capacitors are required.	
8	VDD	Supply Voltage +5V ± 10%	
9	CTLC	See truth table and control voltage table.	
10	CTLB	See truth table and control voltage table.	
11	CTLA	See truth table and control voltage table.	

Application Circuit

