EV28114DG-00A

600mA Synchronous Buck Step-Down Converter

The Future of Analog IC Technology

DESCRIPTION

The EV28114 evaluation board is designed for low dropout step down converter applications. It implements the MP28114 1.7MHz Fixed Frequency, Current Mode, PWM step-down converter. The device integrates a main switch and a synchronous rectifier for high efficiency without an external Schottky diode. It is ideal for powering portable equipments that runs from a single cell Lithium-Ion (Li+) Battery. It can supply 600mA of load current from a 2.5V to 6V input voltage. The output voltage can be regulated as low as 0.6V. In 100% Duty Cycle Dropout operation, it works with minimum input voltage as low as output voltage.

ELECTRICAL SPECIFICATIONS

Parameter	Symbol	Value	Units
Input Voltage Range	V_{IN}	2.5 – 6.0	V
Output Voltage	V_{OUT}	1.8	V
Load Max	I_{OUT}	600	mA

FEATURES

- High Efficiency: Up to 95%
- 600mA Available Load Current
- 2.5V to 6V Input Voltage Range
- Output Voltage as Low as 0.6V
- 100% Duty Cycle in Dropout
- Short Circuit Protection
- Thermal Fault Protection
- <0.1μA Shutdown Current
- Programmable Enable Control

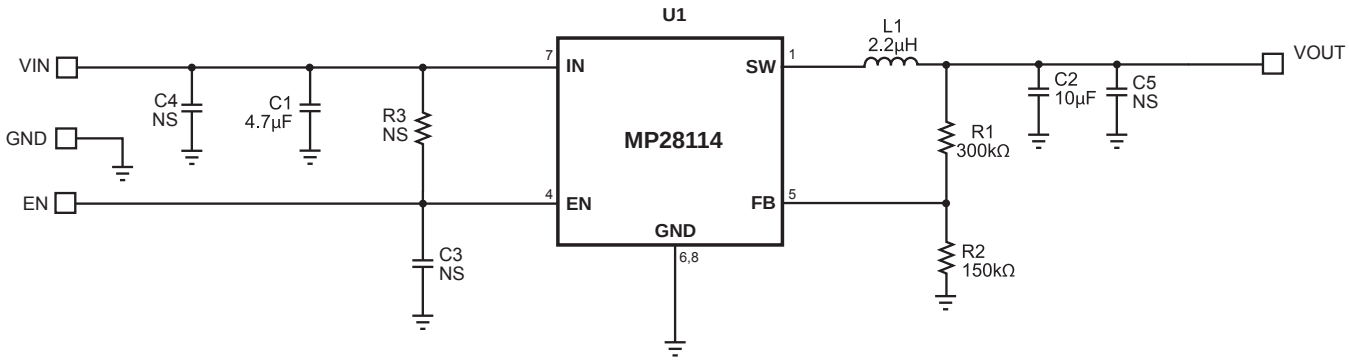
APPLICATIONS

- Cellular and Smart Phones
- Microprocessors/DSP Core Supplies
- PDAs
- MP3 Players
- Digital Still and Video Cameras
- Portable Instruments

EV28114DG-00A EVALUATION BOARD

Dimensions (2.5"X x 2.5"Y x 0.5"Z)	
Board Number	**MPS IC Number**
EV28114DG-00A	MP28114DG

EVALUATION BOARD SCHEMATIC



EV28114DG-00A BILL OF MATERIALS

Qty	Ref	Value	Description	Package	Manufacturer	Manufacturer P/N
1	C1	4.7uF	Ceramic Cap., 10V, X5R	SM0805	TDK	C2012X5R1A475K
1	C2	10uF	Ceramic Cap., 6.3V, X5R	SM0805	KYOCERA	CM21X5R106K06AT
3	C3,C4, C5		Do Not Stuff			
1	L1	2.2uH	1.63A	SMD	TOKO	D52LC-#A914BYW-2R2M
1	R1	300kΩ	Film Res., 1%	SM0603	Yageo	RC0603FR-07300KL
1	R2	150kΩ	Film Res., 1%	SM0603	Yageo	RC0603FR-07150KL
1	R3		Do Not Stuff			
1	U1		DC-DC Converter	QFN8	MPS	MP28114DG

PRINTED CIRCUIT BOARD LAYOUT

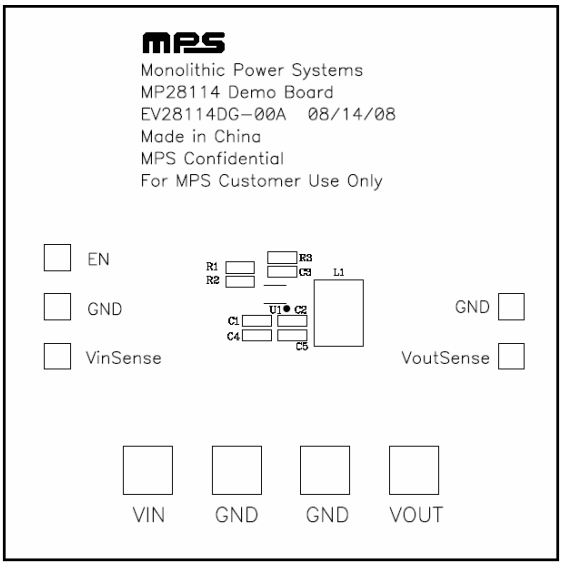


Figure 1—Top Silk Layer

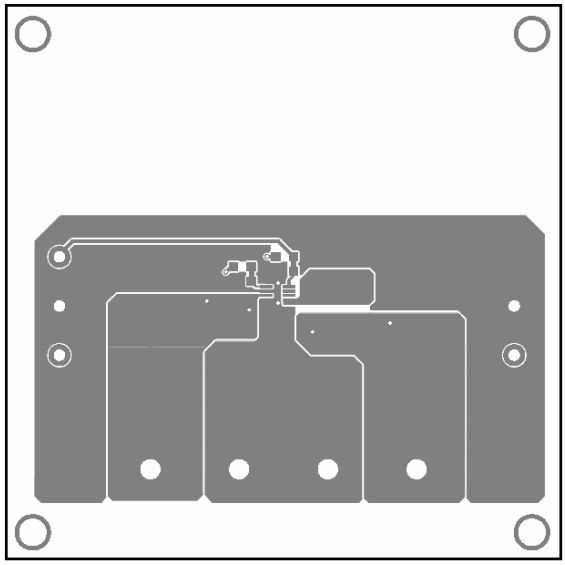


Figure 2—Top Layer

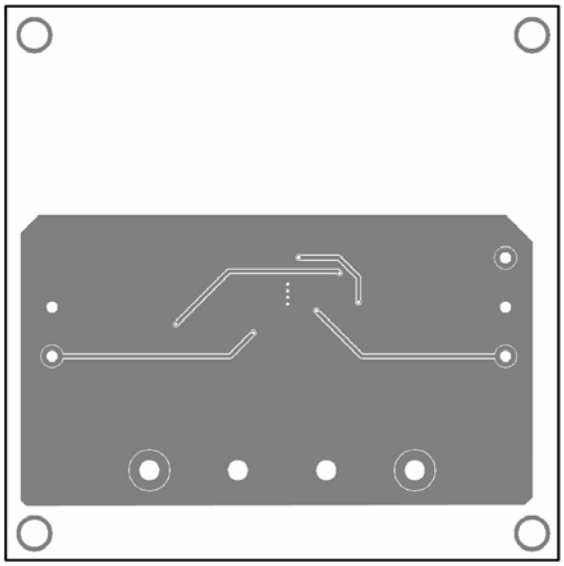


Figure 3—Bottom Layer