One Technology Way • P.O. Box 9106 • Norwood, MA 02062-9106, U.S.A. • Tel: 781.329.4700 • Fax: 781.461.3113 • www.analog.com

Evaluation Board for the 8-/10-Lead Family of 14-/16-/18-Bit PulSAR ADCs

FEATURES

Full featured evaluation board for 8-/10-lead PulSAR ADCs
Versatile analog signal conditioning circuitry
On-board reference, reference buffers, and ADC drivers
PC software for control and data analysis of time and frequency domain
System demonstration platform compatible (EVAL-SDP-CB1Z)

EQUIPMENT NEEDED

Evaluation board (see Table 5)
Wall adapter power supply
Additional equipment needed
SDP board (EVAL-SDP-CB1Z) (optional)
Precision source
Cable (SMA input to evaluation board)

GENERAL DESCRIPTION

The 10-lead PulSAR® evaluation board covers the following 10-lead PulSAR analog-to-digital converters (ADCs): AD7685 (16-bit), AD7686 (16-bit), AD7687 (16-bit), AD7688 (16-bit), AD7690 (18-bit), AD7691 (18-bit), AD7693 (16-bit), AD7942 (14-bit), AD7946 (14-bit), AD7980 (16-bit), AD7982 (18-bit), AD7983 (16-bit), AD7984 (18-bit), AD7988-5 (16-bit), AD7989-5 (18-bit), AD7915 (16-bit) and AD7916 (16-bit).

The 8-lead PulSAR evaluation board covers the following 8-lead PulSAR ADCs: AD7683 (16-bit), AD7684 (16-bit), and AD7694 (16-bit).

These low power ADCs offer very high performance of up to 18 bits with throughputs ranging from 100 kSPS to 1.33 MSPS. The evaluation board is designed to demonstrate the performance of the ADCs and to provide an easy to understand interface for a variety of system applications. A full description of these products is available in their respective data sheets, which should be consulted when using this evaluation board.

The evaluation board is ideal for use with Analog Devices, Inc., system demonstration platform (SDP). This evaluation board interfaces to the SDP board via a 120-pin connector. SMA connectors, J6 and J10, are provided for the low noise analog signal source.

On-board components include a high precision buffered band gap 5.0 V reference (ADR435), a signal conditioning circuit with two op amps (ADA4841-1), and a power supply to derive the necessary voltage levels to supply all voltage needs. The 8-lead board also includes a level shifter (ADG3304) to interface the ADC with the EVAL-SDP-CB1Z.

SIMPLIFIED EVALUATION BOARD BLOCK DIAGRAM

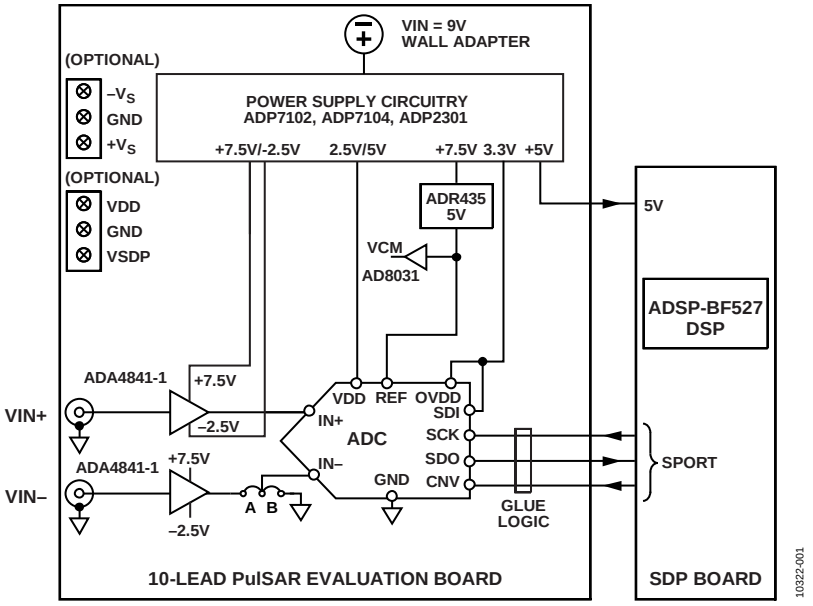

Figure 1.

TABLE OF CONTENTS

REVISION HISTORY

3/15—Rev. B to Rev. C

Changes to General Description Section 1
Changes to Table 3 4
Changes to Table 5 30

2/14—Rev. A to Rev. B

Changes to Equipment Needed Section and General Description Section 1
Changes to Evaluation Kits Contents Section 3
Change to Table 3 4
Changes to Table 5 30

10/12—Rev. 0 to Rev. A

Added 8-Lead PulSAR Evaluation Board ..... Universal
Changes to Description of User Panel Section 10
Changes to Figure 27 Caption 16
Added Figure 28; Renumbered Sequentially 17
Changes to Figure 36 Caption and Figure 37 Caption 23
Changes to Figure 38 Caption and Figure 39 Caption 24
Changes to Figure 40 Caption 25
Added Figure 41 and Figure 42 26
Added Figure 43 and Figure 44 27
Added Figure 45 28
Changes to Software Section 29
Changes to Table 5 and Related Links Section 30

5/12—Revision 0: Initial Version

EVALUATION BOARD KIT CONTENTS

Evaluation board for ADC of your choice
(U1 device is specific to the evaluation board ordered)
Business card with Analog Devices website address for software and documentation
9 V wall wart

HARDWARE REQUIREMENTS

9 V wall wart (supplied)
Standard USB A to Mini-B USB cable
Signal source, ac source with low distortion, and dc source with low noise
Band-pass filter suitable for 16- and 18-bit testing (value based on signal frequency)
SDP board for data transfer to PC
Signal source and cables

EVALUATION BOARD HARDWARE

SETTING UP THE EVALUATION BOARD

Figure 27 shows the evaluation board schematic. The board consists of the ADC, U1, with a reference, U6 (ADR435), and ADC drivers, U12 and U14 (ADA4841-1). The 8-lead board also has a level shifter, U16 (ADG3304).

The evaluation board is a flexible design that enables the user to adjust compensation components in addition to operating from an adjustable bench top power supply.

POWER SUPPLIES

The evaluation board requires power from a wall adapter. The on-board power supply design is designed to operate from 9 V.

Table 1. Power Supplies Provided on the Board

Power Supply (V)	Function	Components Used
+5	SDP power	ADP2301
+7.5	Positive rail	ADP7102
−2.5	Negative rail	ADP2301
+2.5/+5	ADC	ADP7104
+3.3	V_{DRIVE} (digital power)	ADP7104

Each supply is decoupled where it enters the board and again at each device. A single ground plane is used on this board to minimize the effect of high frequency noise interference.

In addition to this, there is also the ability to power the board from a bench top power supply. The screw terminals, J2 and J3, are provided for this function. When bench power is used, the wall wart and on-board power supply are no longer required. Solder links also must be changed: SL1 = B, SL2 = B, SL7 = B, SL4 = B, and SL3 = B.

REFERENCE

An external 5 V reference (U6, ADR435) is used to supply the ADCs directly.

SERIAL INTERFACE

The evaluation board uses the SPORT interface from the ADSP-BF527 DSP.

A number of AND gates are used to clock and gate the SPORT transfer to the ADC device. See U9, U10, and U11. The 8-lead board also has a level shifter, U16 (ADG3304), which interfaces the ADC logic levels (5 V) to the EVAL-SDP-CB1Z (3.3 V).

SOLDER LINKS

There is one three solder link option on the board. It is configured depending on which generic of the ADC is on the specific evaluation board as described in Table 3.

Table 2. Table of Jumper Detail with Factory Default Setting

Link	Setting	Function	Comment
SL2	A	$-V_S$	Change to B if using bench supplies
SL1	A	$+V_S$	Change to B if using bench supplies
SL3	A	V_SDP	Change to B if using bench supplies
SL7	A	VDD for ADC	Change to B if using bench supplies
SL4	A	VREF	Change to B if using bench supplies

Table 3. Table of Jumpers Specific to Different ADCs

Link	Setting	Configuration	Generic
SL10	A	Differential input	AD7684, AD7687, AD7688, AD7690, AD7691, AD7693, AD7982, AD7984, AD7989-5, AD7915, AD7916
SL10	B	Single-ended	AD7683, AD7685, AD7686, AD7694, AD7942, AD7946, AD7980, AD7983, AD7988-5

ANALOG INPUTS

The analog inputs to the evaluation board are SMA connectors, J6 and J10. These inputs are buffered with dedicated amplifier circuitry (U12 and U14), as shown in Figure 27. The circuit allows different configurations, input range scaling, filtering, addition of a dc component, and use of different op amp and supplies. The analog input amplifiers are set as unity-gain buffers at the factory. The amplifier positive rail is driven from 7.5 V (from U13, ADP7102). The negative amplifier rail is driven from $-V_S$ (generated by U3, ADP2301).

The default configuration sets both U12 and U14 at midscale, generated from a buffered reference voltage divider (VCM).

The evaluation board is factory configured for providing either a single-ended path or a fully differential path as shown in Table 3.

For dynamic performance, a fast Fourier transform (FFT) test can be done by applying a very low distortion ac source.

For low frequency testing, the audio precision source can be used directly because the outputs on these are isolated. Set the outputs for balanced and floating ground. Different sources can be used; however, most are single-ended sources that use a fixed output resistance.

Because the evaluation board uses the amplifiers in unity-gain, the noninverting input has a common-mode input with a series 590 Ω resistor, and it needs to be taken into account when directly connecting a source (voltage divider).

EVALUATION BOARD SOFTWARE

INSTALLING THE SOFTWARE

The evaluation board software can be downloaded from the relevant product page on the Analog Devices website.

Install the software prior to connecting the SDP board to the USB port of the PC. This ensures that the SDP board is recognized when it connects to the PC.

1. Start the Windows® operating system and download the software from the relevant product page on the Analog Devices website.
2. Unzip the downloaded file. Run the **setup.exe** file.
3. After installation is completed, power up the evaluation board as described in the Power Supplies section.
4. Plug the evaluation board into the SDP board and the SDP board into the PC using a USB cable.
5. When the software detects the evaluation board, proceed through any dialog boxes that appear to finalize the installation.

The default location for the software is the following: **C:\Program Files\Analog Devices\8 & 10 Lead PulSAR ADCs**.

This location contains the executable software and example files.

INSTALLATION STEPS

Proceed through the installation, allowing the software and drivers to be placed in the appropriate locations. Connect the SDP board to the PC only after the software and drivers have been installed.

There are two parts to the software installation. First, install the software related to the evaluation board, as shown in Figure 2 to Figure 7.

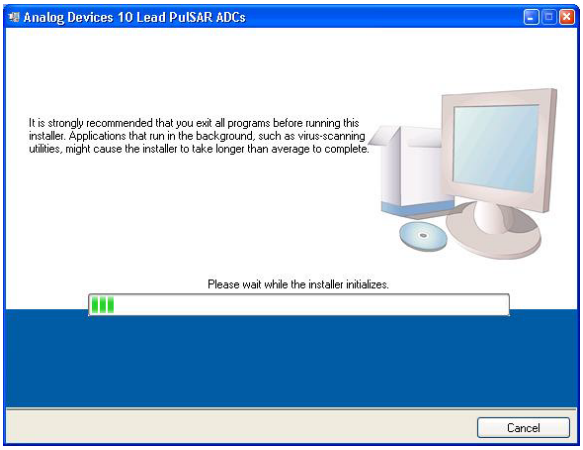

Figure 2. Evaluation Board Software Installation Launches

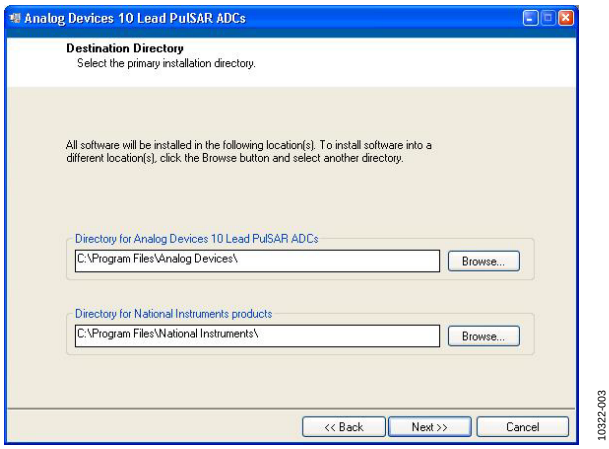

Figure 3. Choose Folder Location, Default Folder Shown

Figure 4. Accept National Instruments Software License Agreement

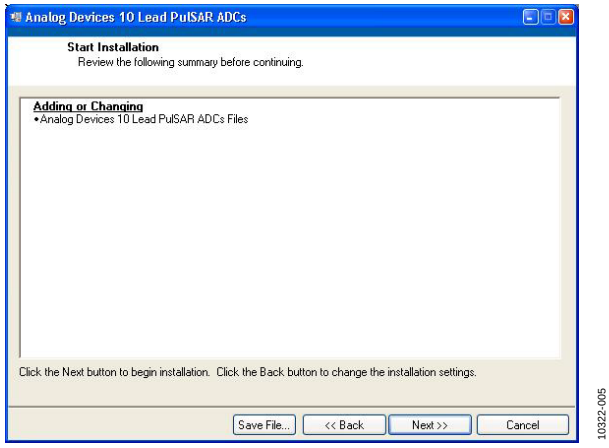

*Figure 5. Click **Next >>** to Install Software*

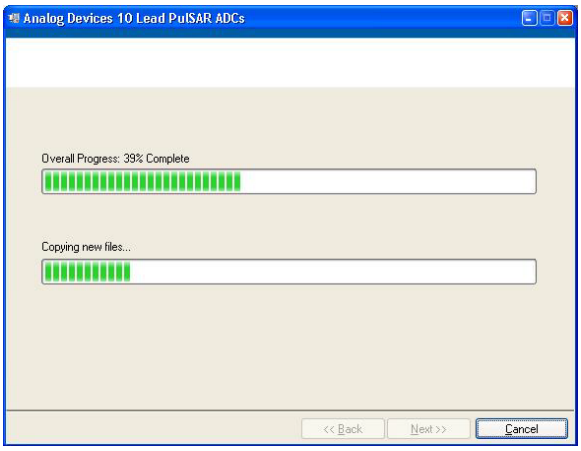

Figure 6. Bar Showing Installation Progress

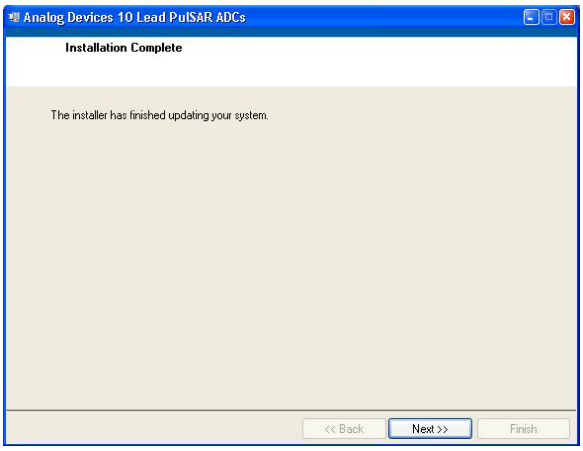

*Figure 7. **Installation Complete**, Click **Next >>** to Complete and Finish*

The second part of the software installation is the drivers related to the SDP board. These drivers must be installed for the evaluation board to function correctly. See Figure 8 to Figure 12.

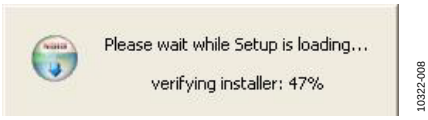

Figure 8. Installation for SDP Starting

*Figure 9. Click **Next >** to Install the ADI SDP Drivers*

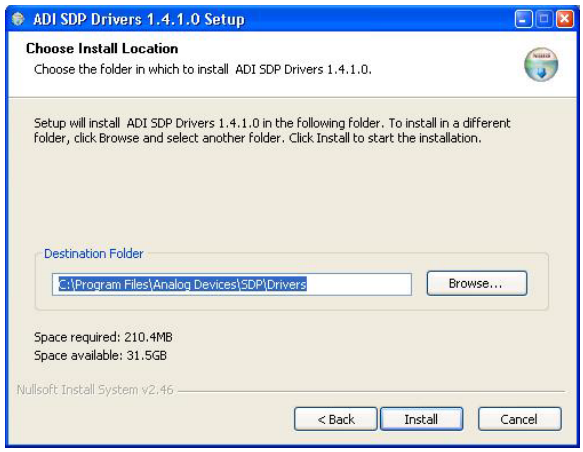

*Figure 10. **Choose Install Location**, Default Folder Shown*

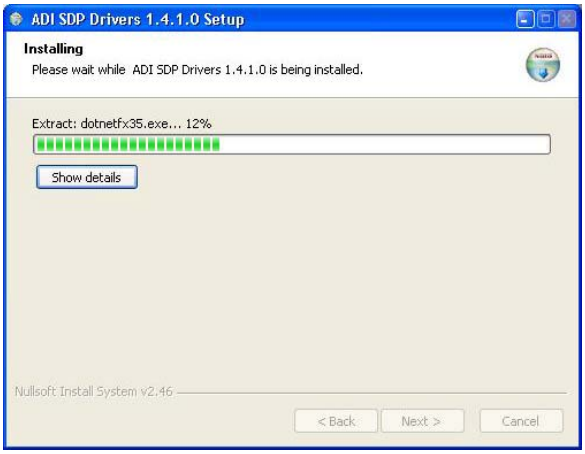

Figure 11. Installation in Progress

*Figure 12. Click **Finish** to Complete Installation*

When you first plug in the SDP board via the USB cable provided, allow the new **Found Hardware Wizard** to run. You can check that the drivers and the board are connected correctly by looking at the **Device Manager** of the PC. The **Analog Devices System Development Platform (32MB)** should appear under **ADI Development Tools**.

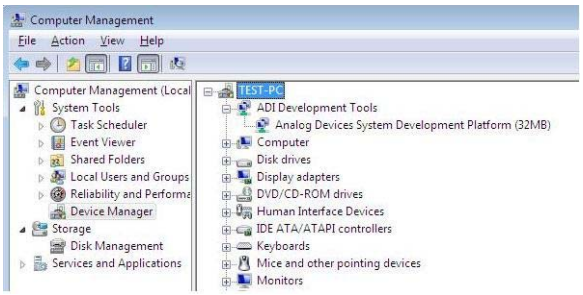

Figure 13. Device Manager

BOARD OPERATION/CONNECTION SEQUENCE

The following is the board operation/connection sequence:

1. Connect the SDP controller board to the evaluation board with the J5 connector (screw into place as required). The software is configured to find the evaluation board on either connector of the SDP board.
2. Power the board with the appropriate supply as described in the Power Supplies section.
3. Connect to the PC with the USB cable.
4. Launch the software. Click **Start** > **All Programs** > **Analog Devices** > **8 & 10 Lead PulSAR ADCs**.
5. Apply signal source and capture data.

RUNNING THE SOFTWARE WITH THE HARDWARE CONNECTED

To run the program, take the following steps:

1. Click **Start** > **All Programs** > **Analog Devices** > **8 & 10 Lead PulSAR ADCs**. To uninstall the program, click **Start** > **Control Panel** > **Add or Remove Programs** > **Analog Devices 8 & 10 Lead PulSAR ADCs**.
2. If the SDP board is not connected to the USB port when the software is launched, a connectivity error displays (see Figure 14). Connect the evaluation board to the USB port of the PC, wait a few seconds, click **Rescan**, and follow the instructions.

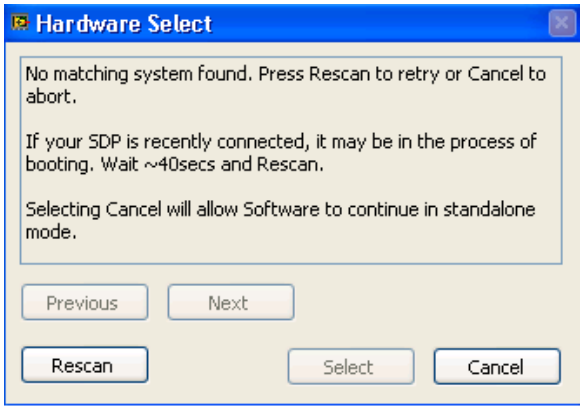

Figure 14. SDP Board Not Connected to the USB Port Pop-Up Window Error

3. When evaluation board is detected, Figure 15 displays. Click **OK** to continue.

Figure 15. Software Detects Evaluation Board

4. The software then connects to the board and displays what is shown in Figure 16.

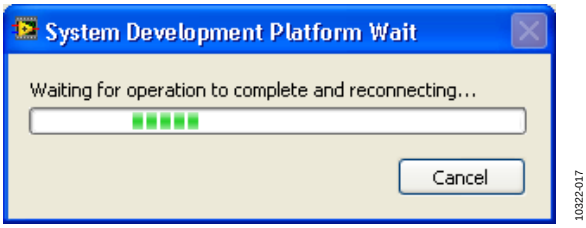

Figure 16. Software Connects to SDP Board

5. When the board is correctly detected, the software panel opens.

RUNNING THE SOFTWARE WITHOUT HARDWARE

The software can run in standalone mode when no evaluation board hardware is connected to the USB port.

1. Click **Start** > **All Programs** > **Analog Devices** > **8 & 10 Lead PulSAR ADCs**.
2. The software automatically seeks to find the hardware connected; therefore, when no hardware is connected, it displays a connectivity error (see Figure 17). To continue without hardware in standalone mode, click **Cancel**.

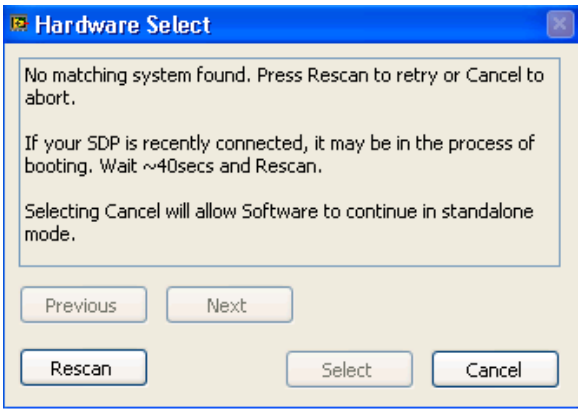

Figure 17. No Hardware Connected Pop-Up Window Error

3. The software alerts you that no hardware is connected, and that the software will continue in standalone mode.

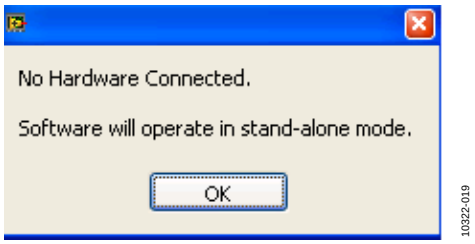

Figure 18. Software Indicates Operating in Standalone Mode

4. Within standalone/offline mode, you can load example files or previously saved files and analyze these files.
5. If you decide to connect hardware at this point, close the software and relaunch it to allow the software to search for the board again.

SOFTWARE OPERATION

When the software launches, the panel opens and the software looks for the hardware connected to the PC. The software detects the generic attached to the PC (see Figure 19). The product panel then launches.

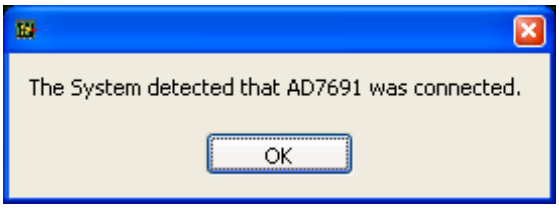

Figure 19. Software Detects AD7691

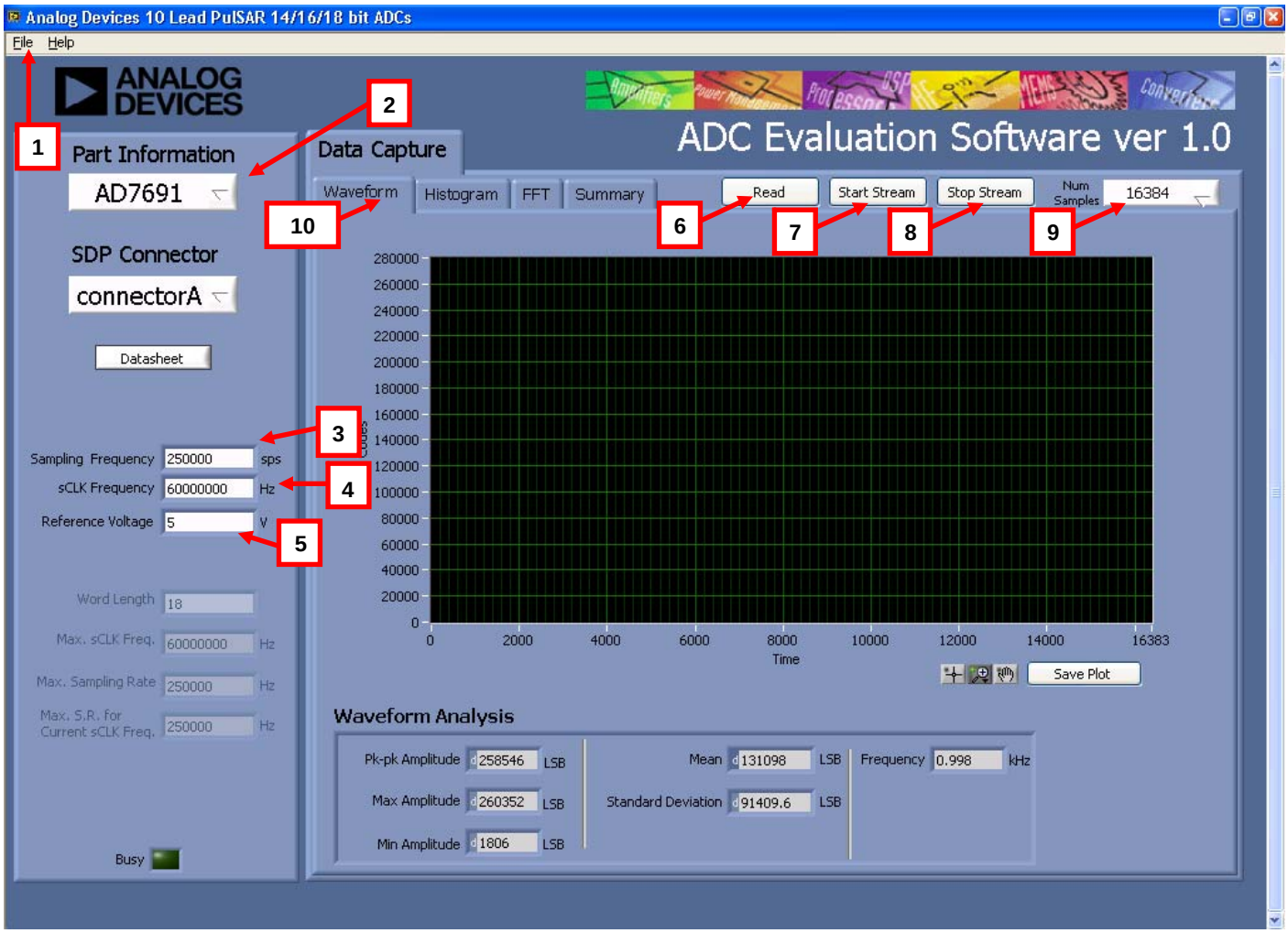

Figure 20. Setup Screen

DESCRIPTION OF USER PANEL

The following is the description of the user panel:

1. File menu with choice of
   a. **Load Data**: load previously captured data
   b. **Save Data as .tsv**: save captured data in tsv (tab separated values) format for future analysis
   c. **Save Picture**: use to save the current screen capture
   d. **Print**
   e. **Exit**
2. When hardware is connected to the USB port, the software automatically detects which generic is connected and displays it here. Without hardware, the software can be operated in standalone mode for data analysis, and the device information notes the device number pulled from the saved data file.
3. **Sampling Frequency**: The default sampling frequency matches the maximum sample rate of the ADC connected to the board. The user can adjust the sampling frequency; however, there are limitations around the sample frequency related to the SCLK frequency applied. The sample frequency must be an integer divider of the SCLK frequency. In addition, where unusable sample frequencies are input, the software automatically adjusts the sample frequency accordingly. Units can be entered, such as 10k for 10,000 Hz. Because the maximum sample frequency possible is device dependent, with some of the ADCs capable of operating up to 250 kSPS, while others can go to 1.3 MSPS, the software matches the particular ADC ability. If the user enters a value larger than the ability of the existing device, the software indicates this and reverts to the maximum sample frequency.
4. **sCLK Frequency**: The default SCLK frequency is set to 60 MHz, which is the maximum allowable from the SDP. The SCLK is applied to the ADC SCK pin. The SDP board limits the SCLK frequency; nominal values for correct operation are 60 MHz, 30 MHz, and 20 MHz. Where the user adjusts the SCLK/sample rate to values that are not supported by the SDP clock or the ADC sample rate, the software overrides by adjusting values accordingly and identify this to the user (see Figure 21). The SCLK frequency is rounded down.

Figure 21. Software Overwritten User Settings to a Sample Rate/SCLK Rate Suitable for SDP Data Transfer

5. External **Reference Voltage**. By default, this reference is 5 V (ADR435 on-board reference). The minimum/maximum voltage calculations are based on this reference voltage. If the user changes the reference voltage, this input must be changed accordingly.
6. Click **Read** to perform a single capture.
7. Click **Start Stream** to perform a continuous capture from the ADC.
8. Click **Stop Stream** to stop streaming data.
9. Select the number of samples (**Num Samples**) to analyze.
10. There are four tabs available that display the data in different formats:
    a. **Waveform**
    b. **Histogram**
    c. **FFT**
    d. **Summary**

To exit the software, go to **File** > **Exit** or use the **EXIT** button in the bottom left corner.

Within any of the chart panels, the tools shown in Table 4 allow user control of the different chart displays.

Table 4.

Symbol	Description
	This tool is used to control the cursor, if present.
	This tool is used to zoom in and out.
	This tool is used for panning.

To save the plot, click **Save Plot**.

WAVEFORM CAPTURE

Figure 22 illustrates the waveform capture. The input signal is a 1 kHz sine wave. The waveform analysis reports the amplitudes recorded from the captured signal in addition to the frequency of the signal tone.

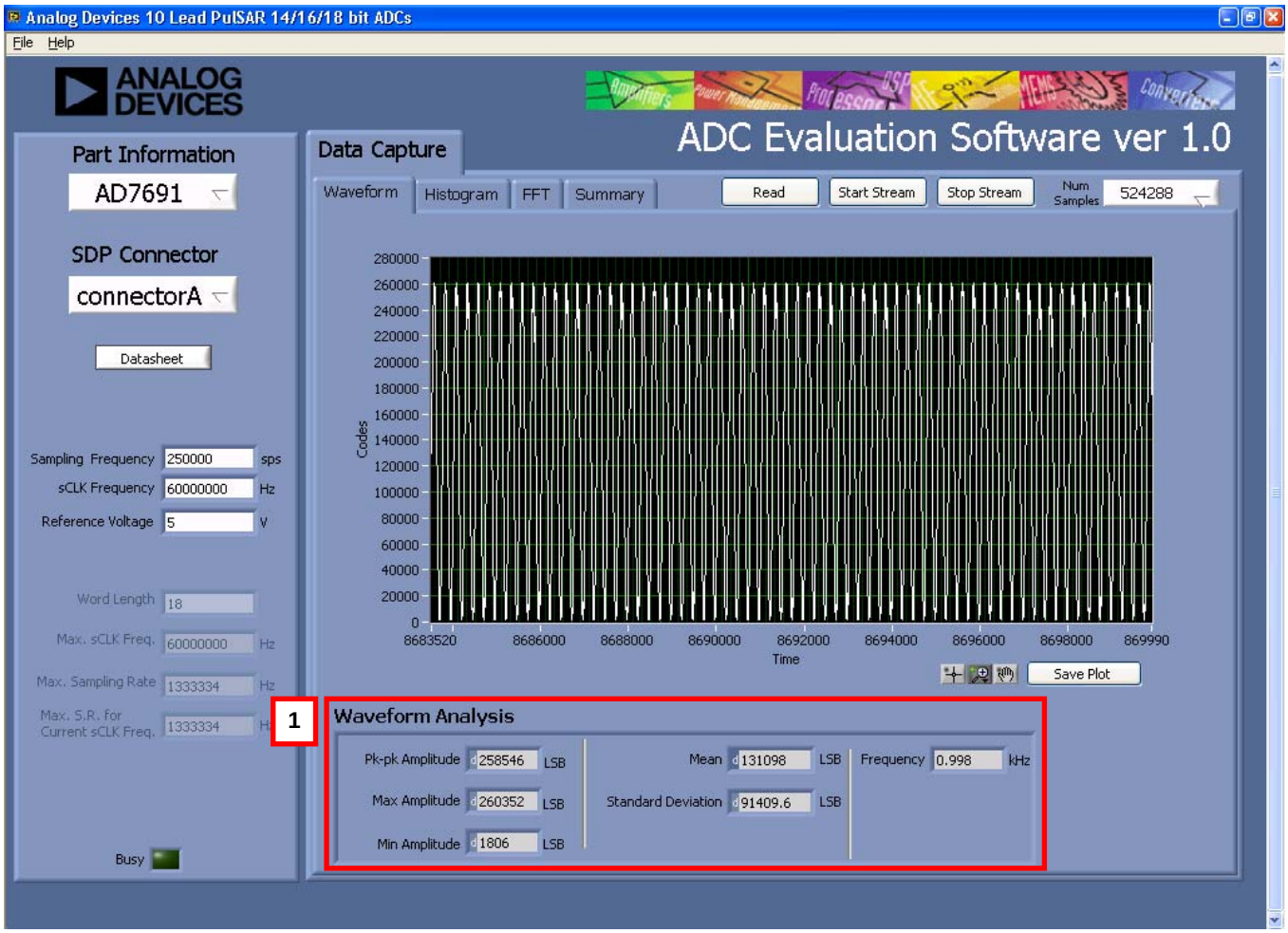

Figure 22. Waveform Tab

AC TESTING—HISTOGRAM

The ac testing histogram tests the ADC for the code distribution for the ac input, computes the mean and standard deviation, or transition noise, of the converter, and displays the results. Raw data is captured and passed to the PC for statistical computations. To perform a histogram test, select **Histogram** and click **Start Stream**.

Note that an ac histogram needs a quality signal source applied to the input J6/J10 connectors. Figure 23 shows the histogram for a 1 kHz sine wave applied to the ADC input.

Figure 23 shows the histogram results for the signal applied. It also illustrates the different measured values for the data captured (see Number 1 within Figure 23).

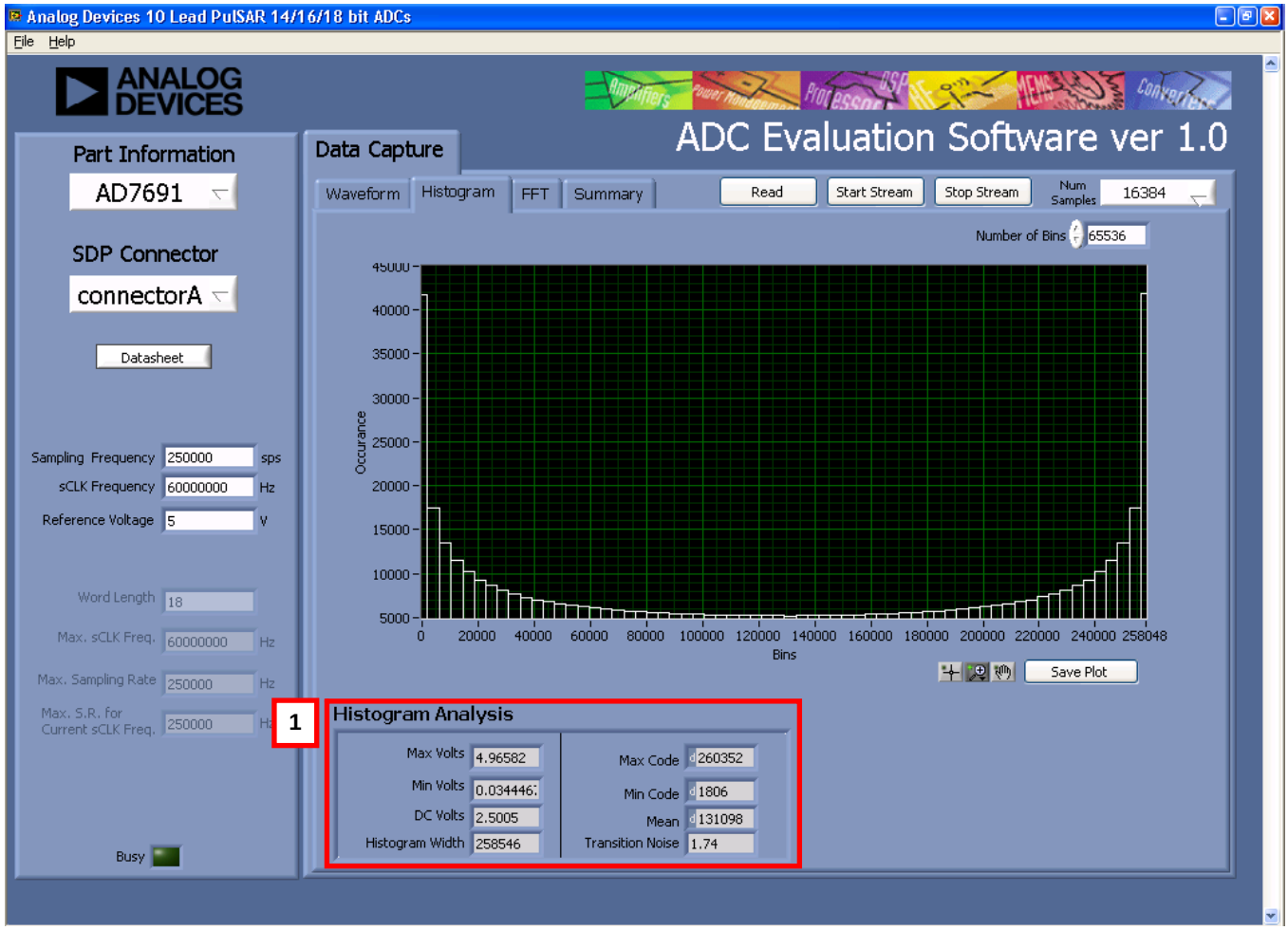

Figure 23. Histogram Tab, Histogram Captured for Sine Wave

DC TESTING—HISTOGRAM

More commonly, the histogram is used for dc testing where the user tests the ADC for the code distribution for dc input, computes the mean and standard deviation, or transition noise, of the converter, and displays the results. Raw data is captured and passed to the PC for statistical computations. To perform a histogram test, select **Histogram** and click **Start Stream**. Note that a histogram test can be performed without an external source because the evaluation board has a buffered $V_{REF}/2$ source at the ADC input. To test other dc values, apply a source to the J6/J10 inputs. It may be required to filter the signal to make the dc source noise compatible with that of the ADC.

AC TESTING—FFT CAPTURE

This tests the traditional ac characteristics of the converter and displays an FFT of the result. As in the histogram test, raw data is captured and passed to the PC where the FFT is performed displaying signal-to-noise ratio (SNR), signal-to-noise-and-distortion ratio (SINAD), total harmonic distortion (THD), and spurious-free dynamic range (SFDR). The data can also be displayed in the time domain. To perform an ac test, apply a sinusoidal signal to the evaluation board at the SMA inputs, J6/J10. Low distortion, better than 100 dB, is required to allow true evaluation of the device. One possibility is to filter the input signal from the ac source. A band-pass filter can be used, and its center frequency must match the test frequency of interest. Furthermore, if using a low frequency band-pass filter when the full-scale input range is more than a few V p-p, use the on-board amplifiers to amplify the signal, thus preventing the filter from distorting the input signal.

Figure 24 displays the histogram of the captured data that includes the following:

- The spectrum information
- The fundamental frequency and amplitude in addition to the second-to-fifth harmonics
- The performance data (SNR, dynamic range, THD, SINAD, and noise performance)

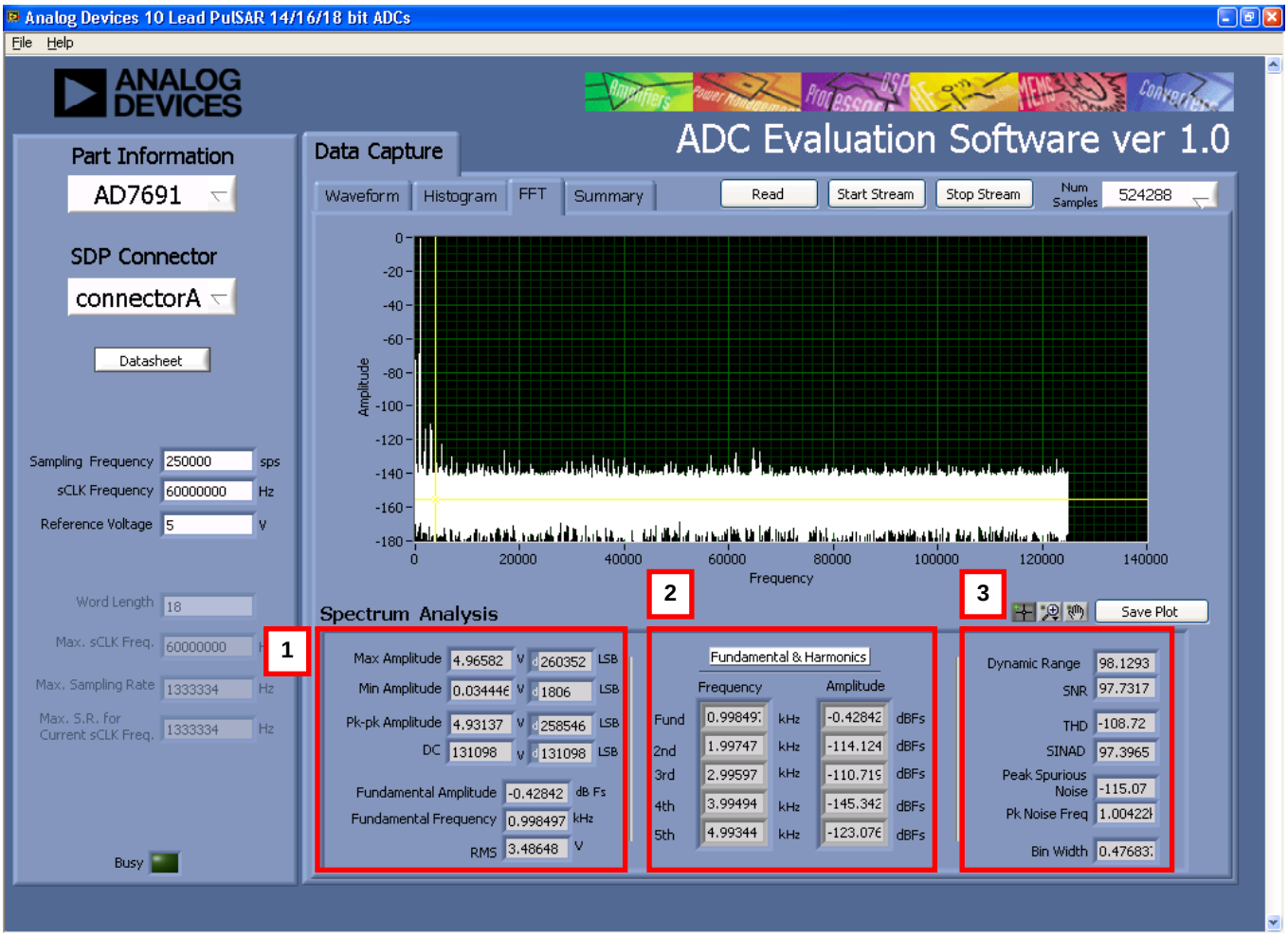

Figure 24. FFT Tab

SUMMARY TAB

The **Summary** tab captures all the display information and provides them in one panel with a synopsis of the information including key performance parameters, such as SNR and THD.

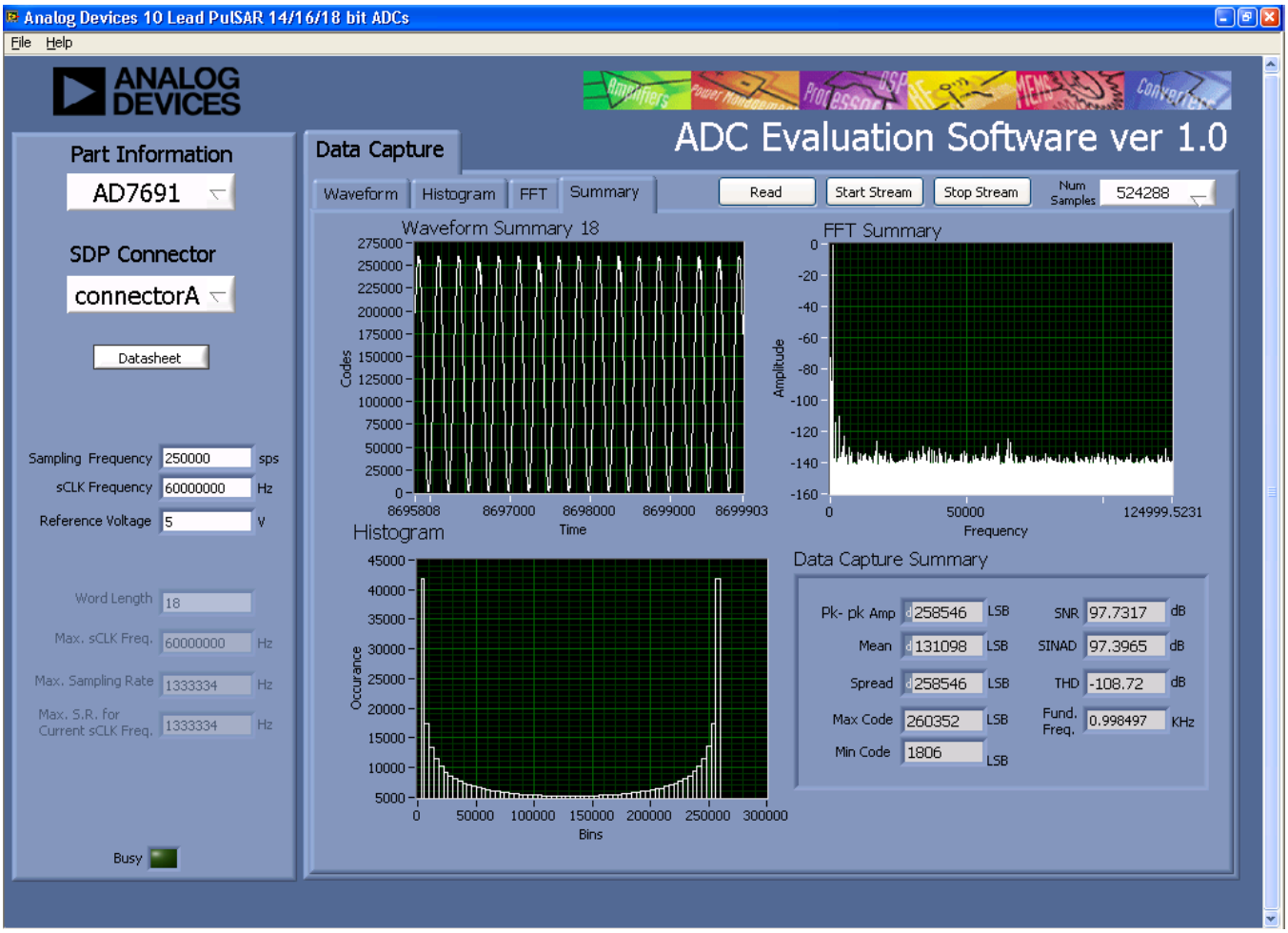

Figure 25. Summary Tab, Shows All Captured Windows

SAVE FILE

The software can save the current captured data for later analysis, and the file format is .tsv (tab separated values).

The user is prompted with a **Choose or Enter Path of File** box (see Figure 26); save to an appropriate folder location.

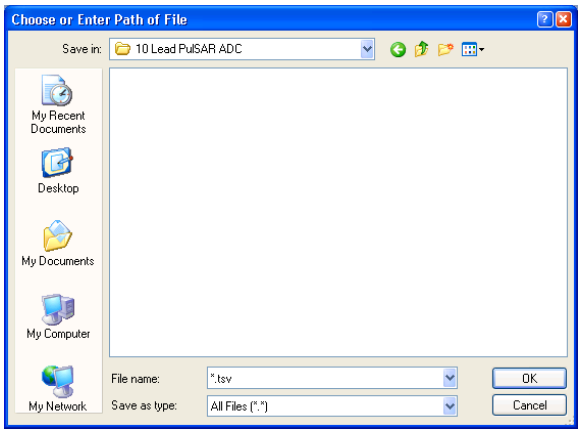

Figure 26. Save Dialog Box

LOAD FILE

The user is prompted with a **Load File** box. The user may have to navigate to find these example files. The default location for the example files is **C:\Program Files\Analog Devices\8 & 10 Lead PulSAR ADCs\Example files**.

EVALUATION BOARD SCHEMATICS

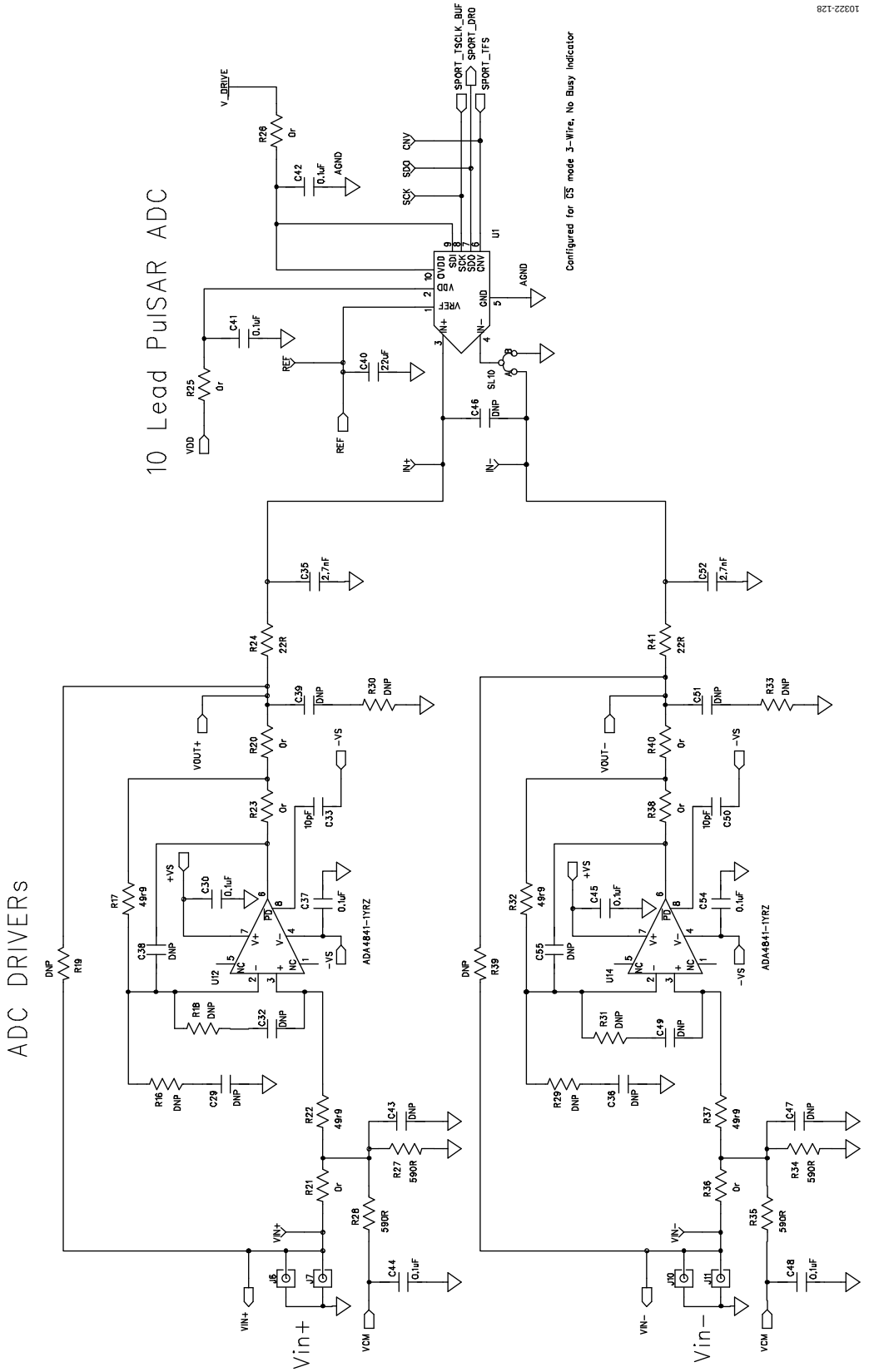

Figure 27. 10-Lead Evaluation Board, ADC and Driver

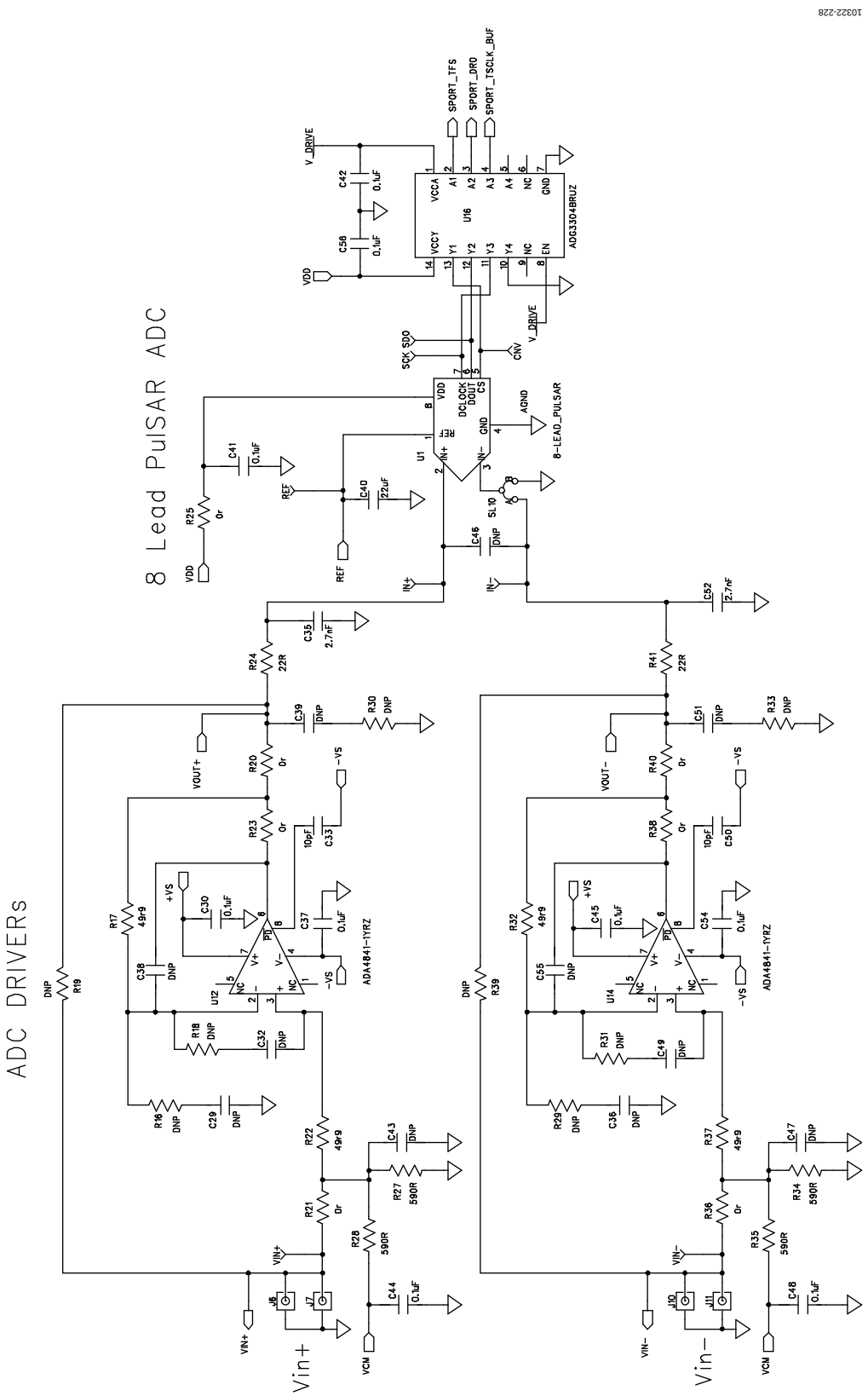

Figure 28. 8-Lead Evaluation Board, ADC and Driver

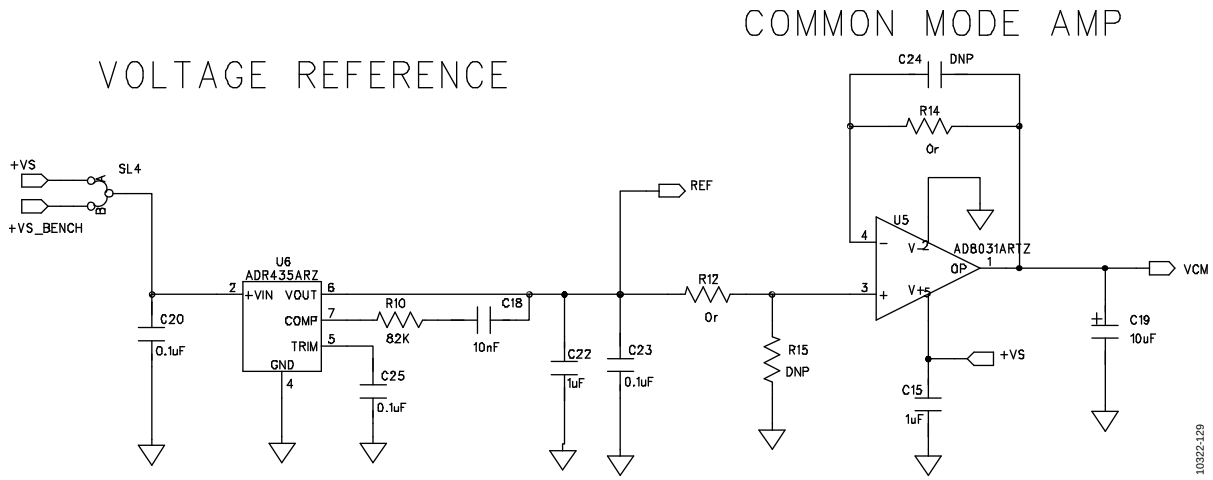

Figure 29. Voltage Reference and Common-Mode Buffer

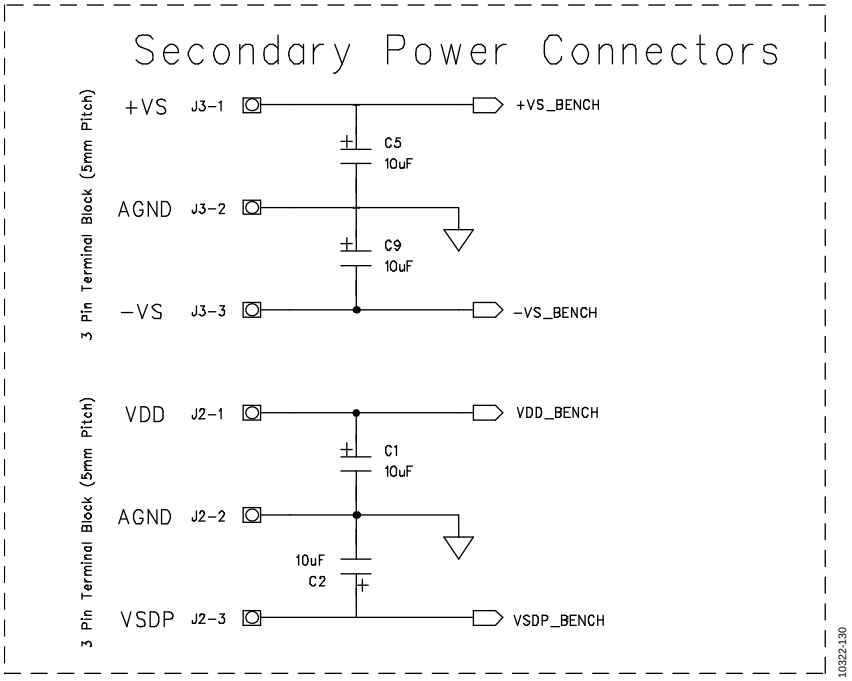

Figure 30. Secondary Power Connector for Bench Supply Purposes

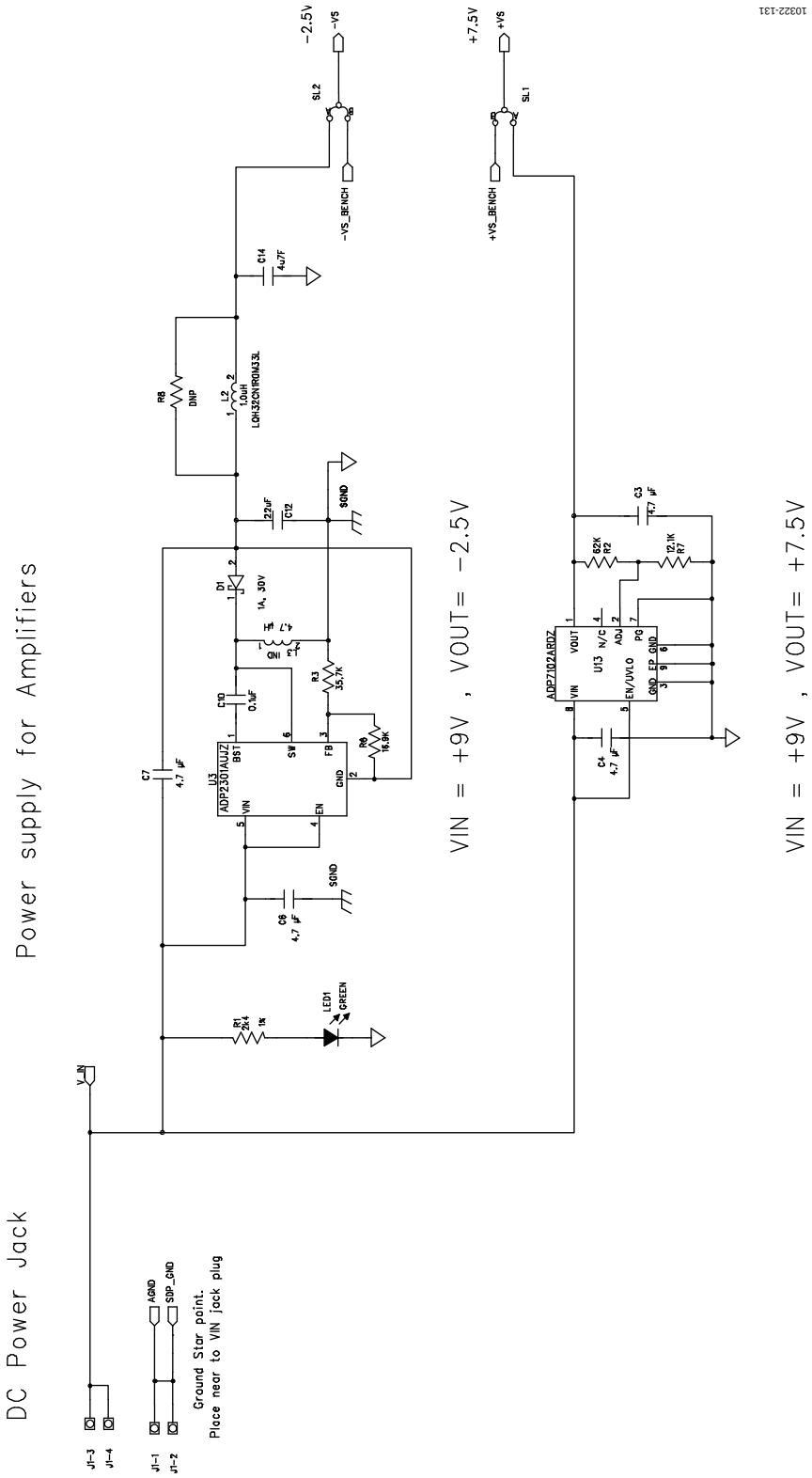

Figure 31. On-Board Amplifier Power Supply, +7.5/−2.5 V

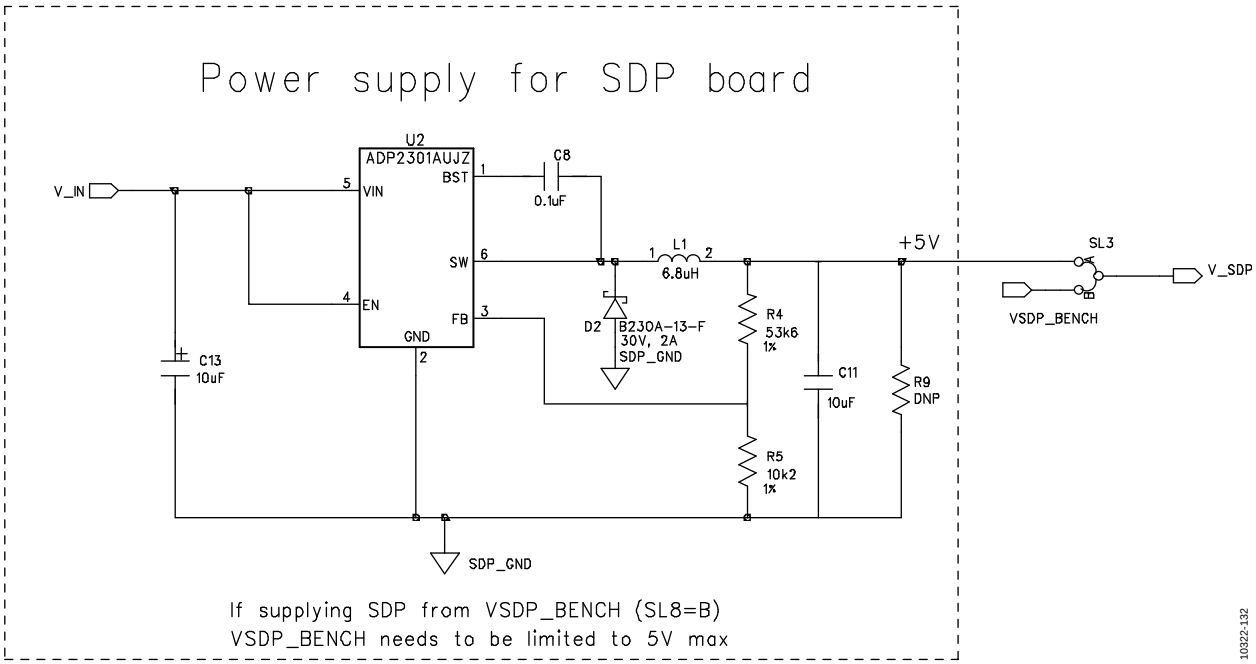

Figure 32. SDP Power Supply

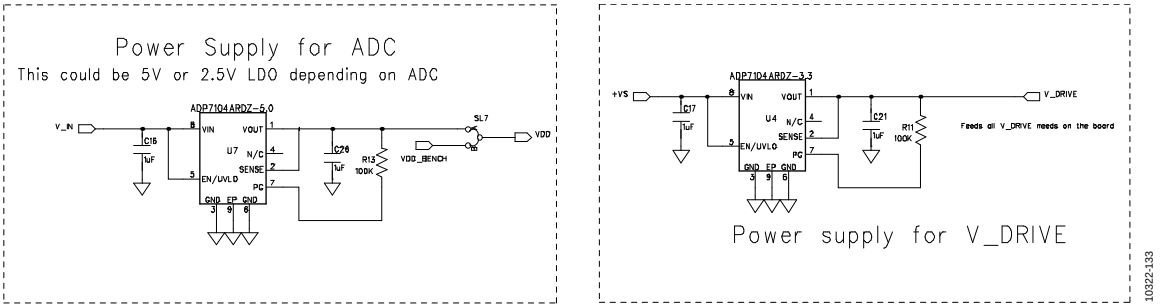

Figure 33. ADC/V_{DRIVE} Power Supply

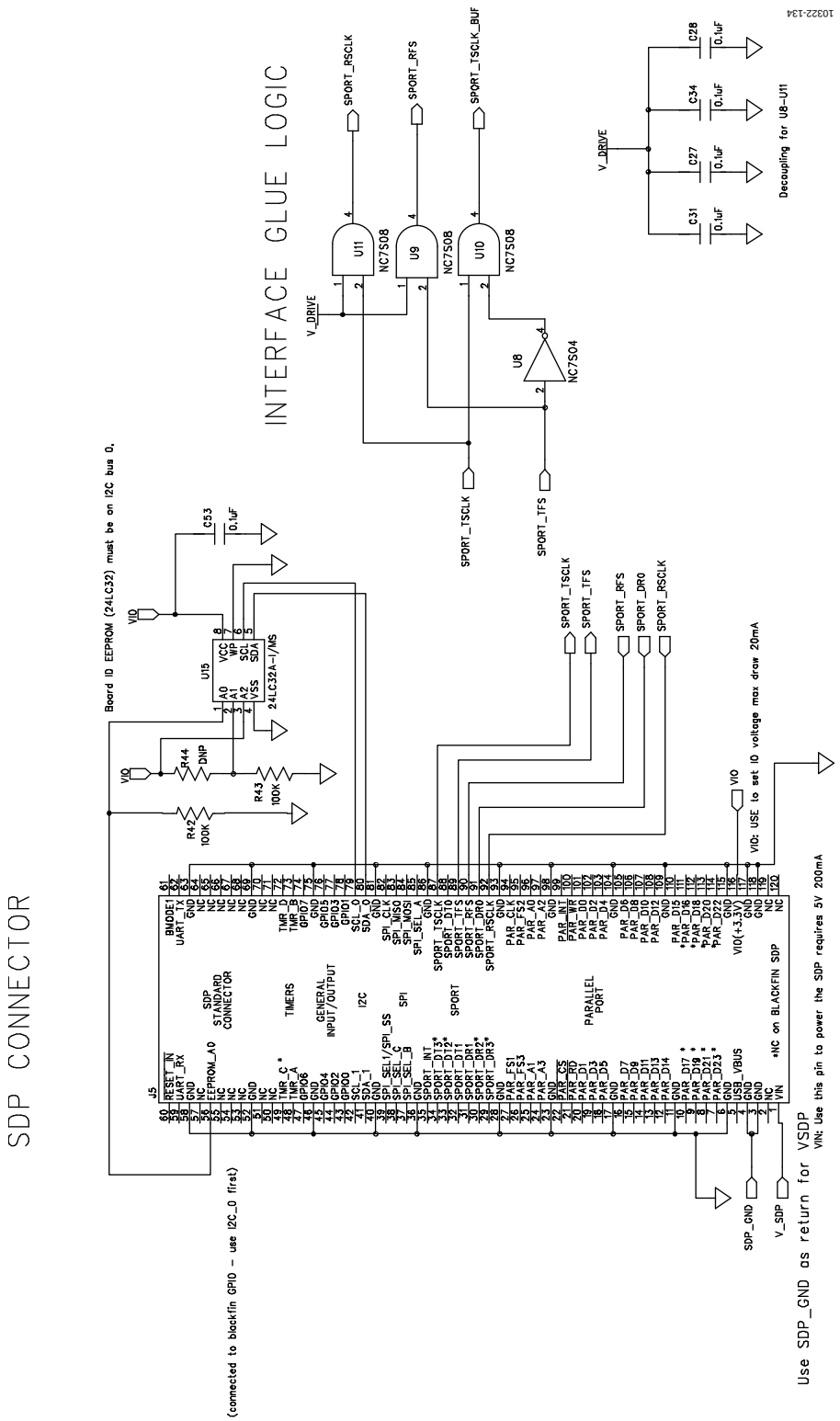

Figure 34. SDP Connector and Glue Logic

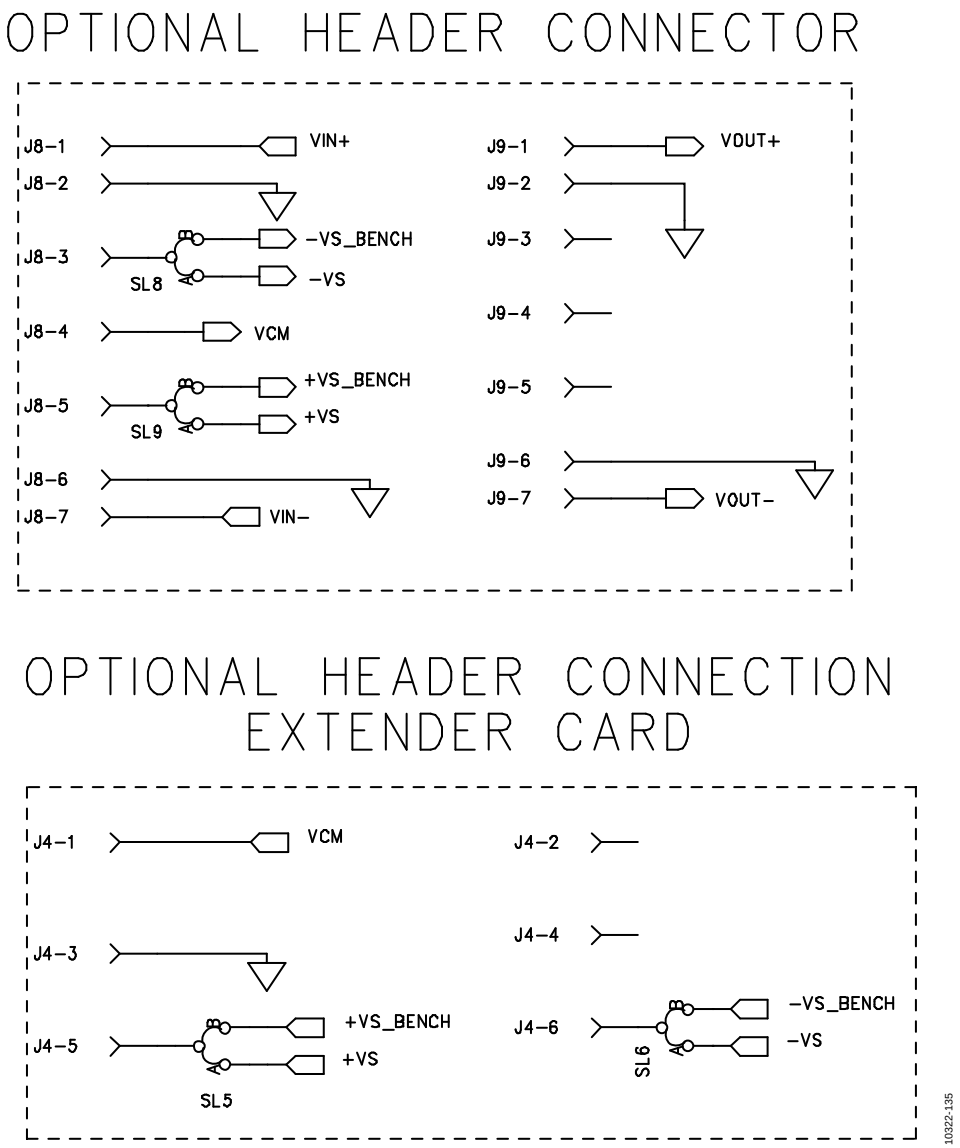

Figure 35. Header Connectors, Optional Connectors for Possible Add-On Boards

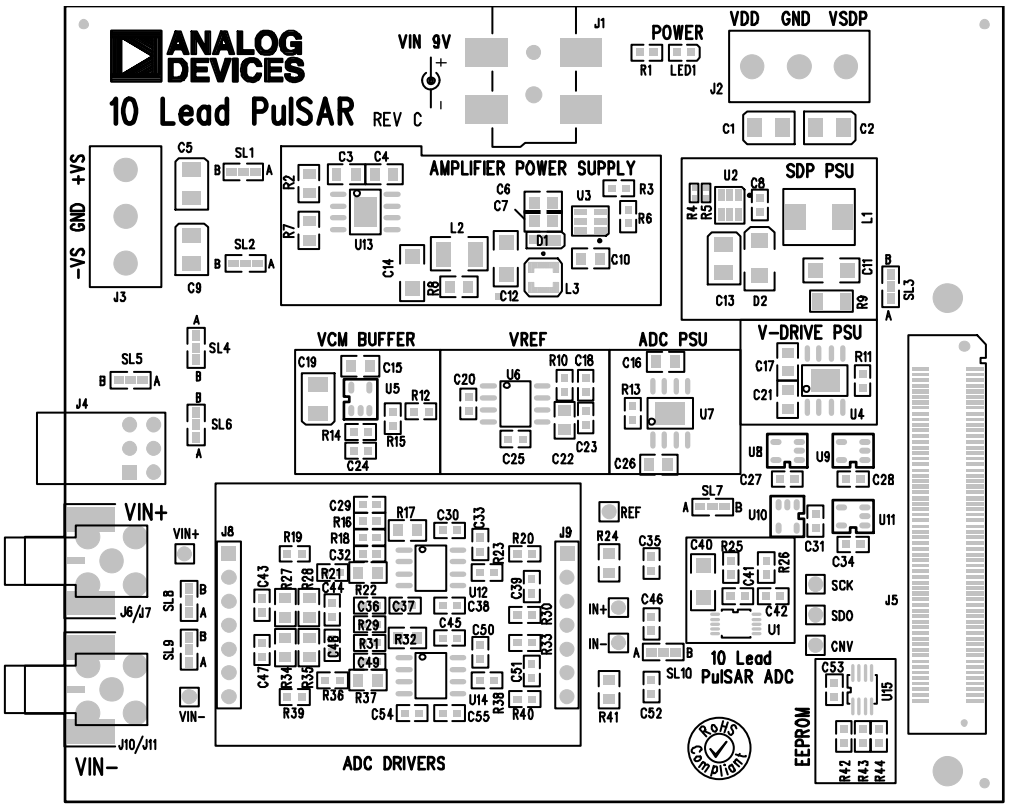

Figure 36. 10-Lead Evaluation Board Silkscreen, Top Layer

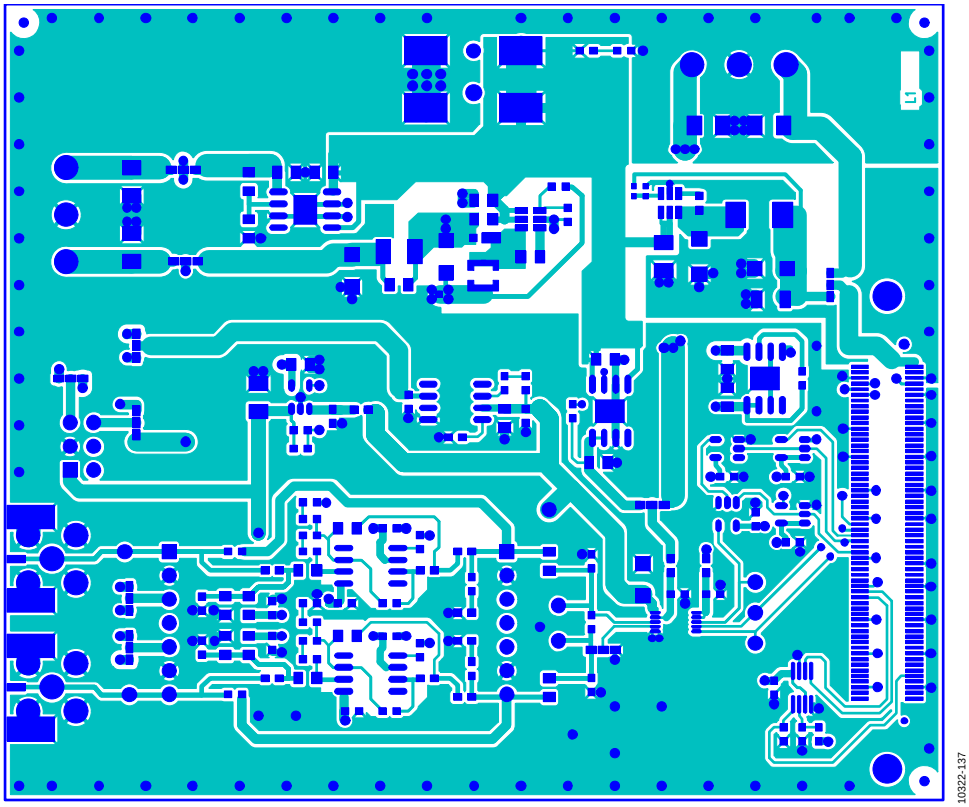

Figure 37. 10-Lead Evaluation Board, Layer 1

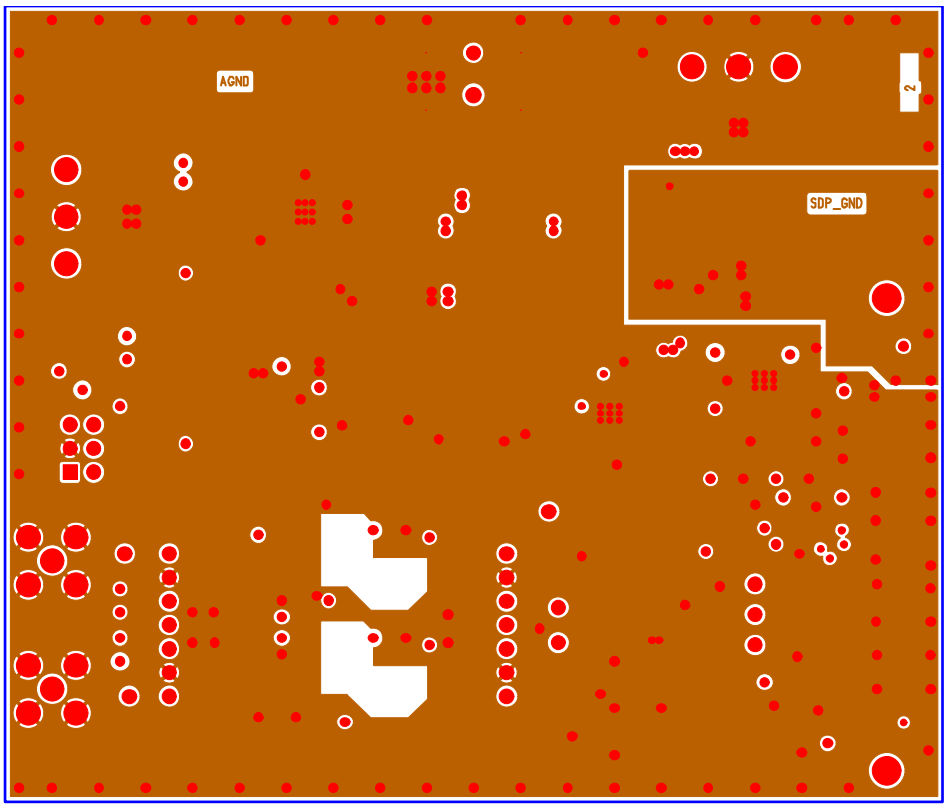

Figure 38. 10-Lead Evaluation Board, Layer 2

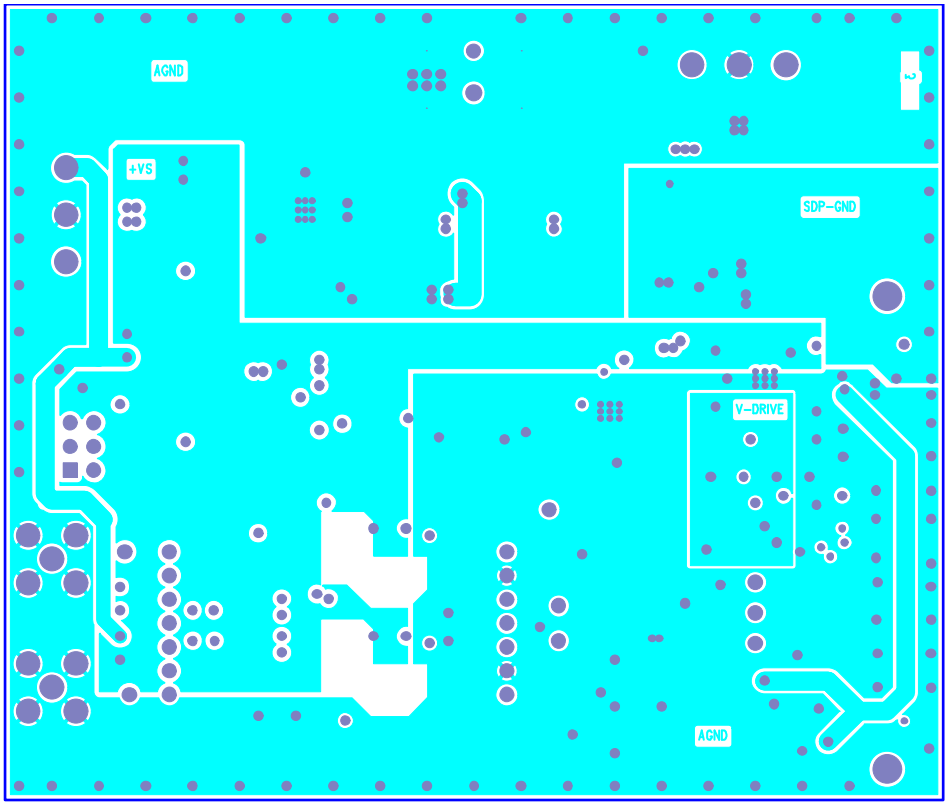

Figure 39. 10-Lead Evaluation Board, Layer 3

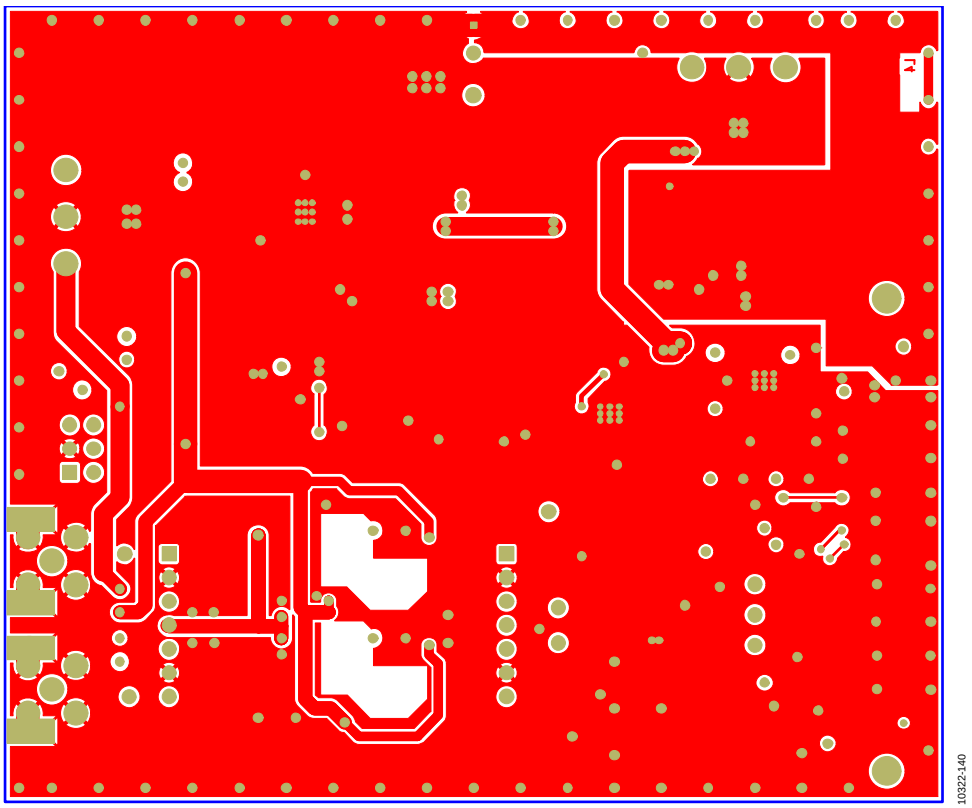

Figure 40. 10-Lead Evaluation Board, Layer 4

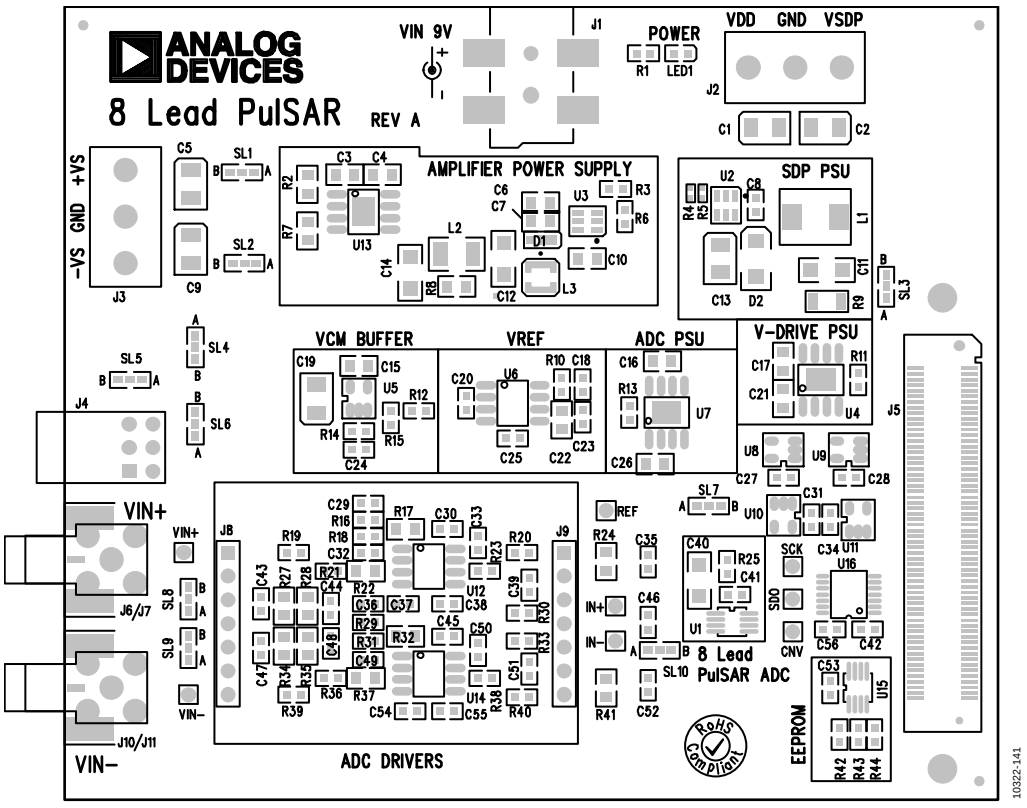

Figure 41. 8-Lead Evaluation Board Silkscreen, Top Layer

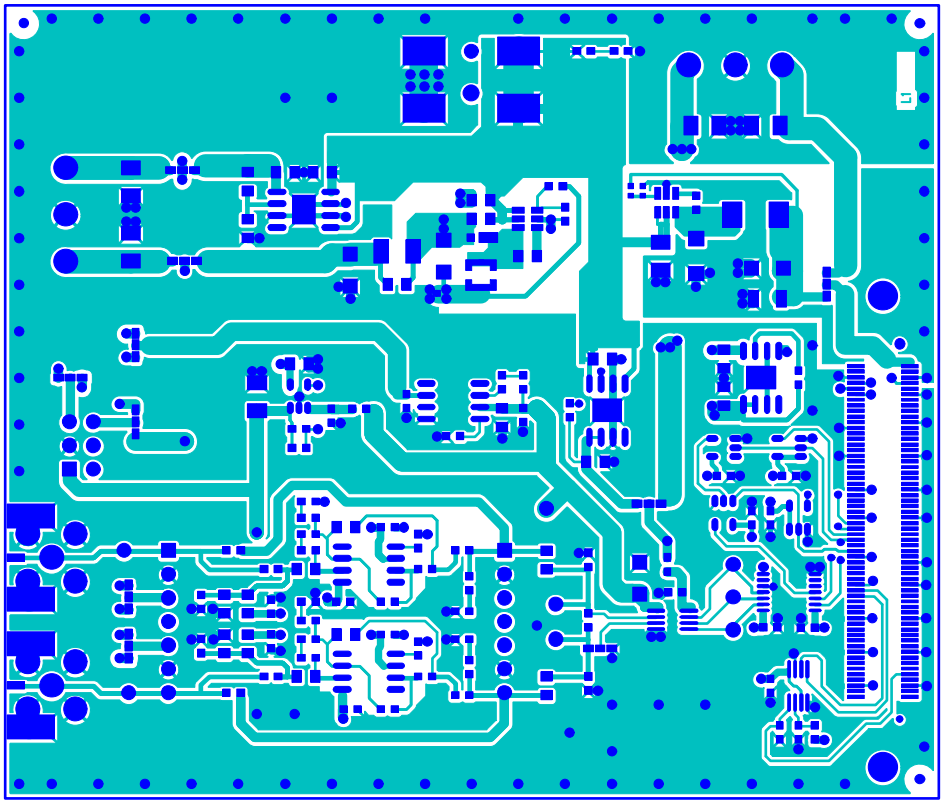

Figure 42. 8-Lead Evaluation Board, Layer 1

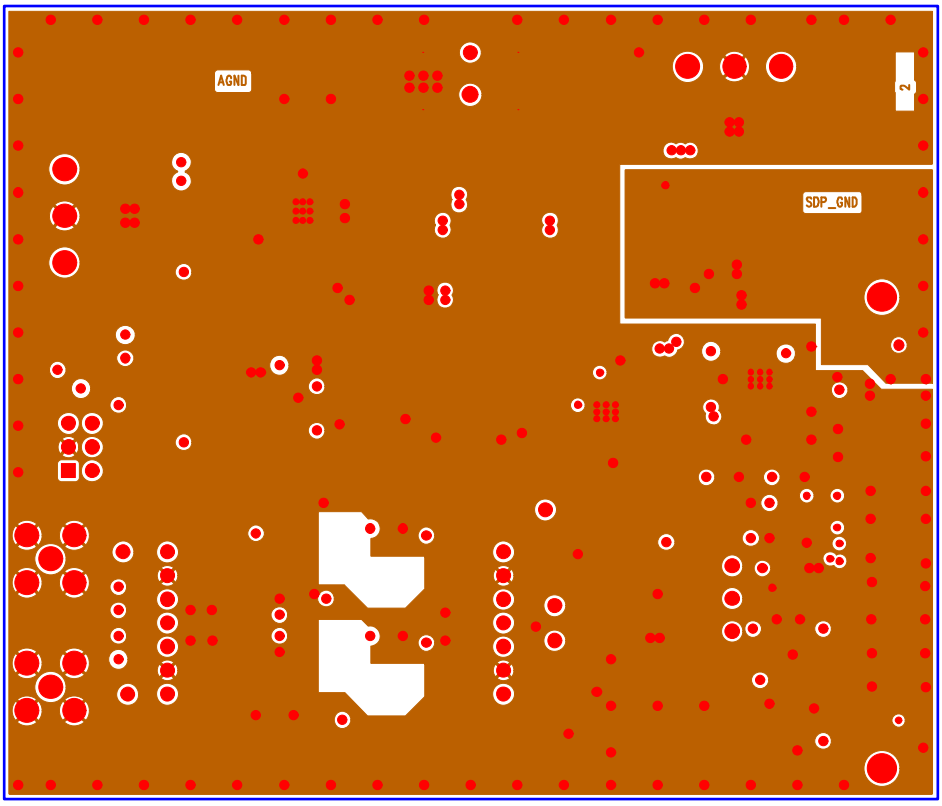

Figure 43. 8-Lead Evaluation Board, Layer 2

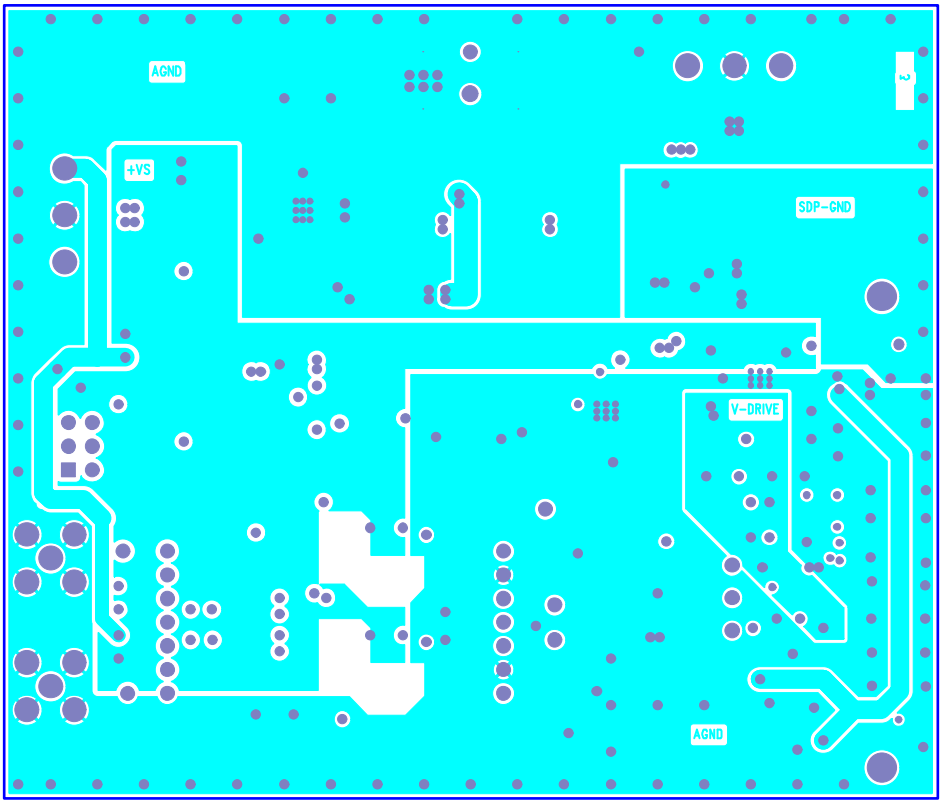

Figure 44. 8-Lead Evaluation Board, Layer 3

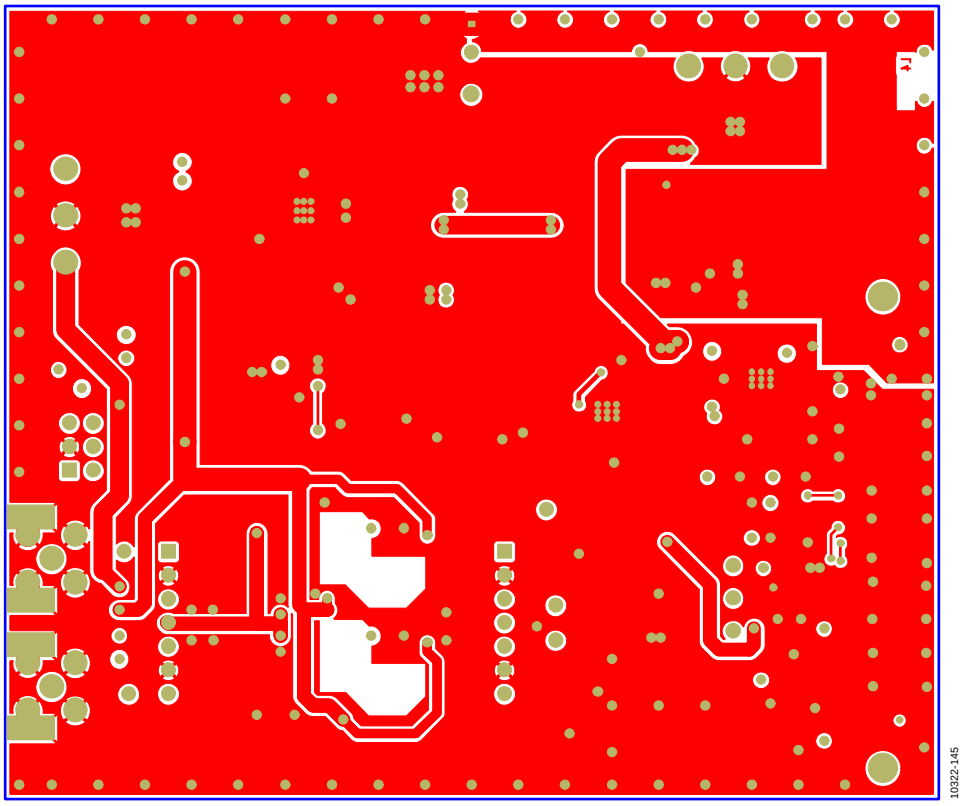

Figure 45. 8-Lead Evaluation Board, Layer 4

TROUBLESHOOTING

SOFTWARE

To troubleshoot the software, take the following steps:

1. Always install the software prior to connecting the hardware to the PC.
2. Always allow the install to fully complete (the software is a 2-part install, the ADC software and the SDP drivers). This may require a restart.
3. When the user first plugs in the SDP board via the USB cable provided, allow the new **Found Hardware Wizard** to run. This may take a little time but allow this to happen prior to starting the software.
4. Where the board does not appear to be functioning, ensure that the ADC evaluation board is connected to the SDP board and that the board is being recognized in the **Device Manager**, as shown in Figure 13.
5. If connected to a slower USB port where the SDP cannot read as quickly as it needs to, a timeout error may result. In this case, it is advised not to read continuously, or alternatively, to lower the number of samples taken.

HARDWARE

To troubleshoot the hardware, take the following steps:

1. If the software does not read any data back, do the following:
   a. Check that the power is applied within the power ranges described in the Power Supplies section.
   b. Using a voltmeter, measure the voltage present at the positive terminal of C3 (it should be 7.5 V) and C14 (it should be −2.5 V) and measure the voltage at the positive terminal of C11 (it should be 5 V). The SDP board, LED1, should be lit.
   c. Launch the software and read the data. If nothing happens, exit the software.
   d. Power down the board and relaunch the software.
   e. If no data is read back, confirm that the ADC evaluation board is connected to the SDP board and that the board is being recognized in the **Device Manager**, as shown in Figure 13.
2. When the user is working with the software in standalone/offline mode (no hardware connected) and later chooses to connect hardware, they must close and relaunch the software.

PRODUCTS ON THIS EVALUATION BOARD

Table 5.

Product	Ordering Model	Sample Rate	Resolution (Bit)	Package Used on Evaluation Board
AD7683BRMZ	EVAL-AD7683SDZ	100 kSPS	16	8-Lead MSOP
AD7684BRMZ	EVAL-AD7684SDZ	100 kSPS	16	8-Lead MSOP
AD7685BRMZ	EVAL-AD7685SDZ	250 kSPS	16	10-Lead MSOP
AD7686BRMZ	EVAL-AD7686SDZ	500 kSPS	16	10-Lead MSOP
AD7687BRMZ	EVAL-AD7687SDZ	250 kSPS	16	10-Lead MSOP
AD7688BRMZ	EVAL-AD7688SDZ	500 kSPS	16	10-Lead MSOP
AD7690BRMZ	EVAL-AD7690SDZ	400 kSPS	18	10-Lead MSOP
AD7691BRMZ	EVAL-AD7691SDZ	250 kSPS	18	10-Lead MSOP
AD7693BRMZ	EVAL-AD7693SDZ	500 kSPS	16	10-Lead MSOP
AD7694BRMZ	EVAL-AD7694SDZ	250 kSPS	16	8-Lead MSOP
AD7942BRMZ	EVAL-AD7942SDZ	250 kSPS	14	10-Lead MSOP
AD7946BRMZ	EVAL-AD7946SDZ	500 kSPS	14	10-Lead MSOP
AD7980BRMZ	EVAL-AD7980SDZ	1 MSPS	16	10-Lead MSOP
AD7982BRMZ	EVAL-AD7982SDZ	1 MSPS	18	10-Lead MSOP
AD7983BRMZ	EVAL-AD7983SDZ	1.33 MSPS	16	10-Lead MSOP
AD7984BRMZ	EVAL-AD7984SDZ	1.33 MSPS	18	10-Lead MSOP
AD7988-5BRMZ	EVAL-AD7988-5SDZ	500 kSPS	16	10-Lead MSOP
AD7915BRMZ	EVAL-AD7915SDZ	1 MSPS	16	10-Lead MSOP
AD7916BRMZ	EVAL-AD7916SDZ	500 kSPS	16	10-Lead MSOP

RELATED LINKS

Resource	Description
AD8031	Product Page, AD8031, 2.7 V, 800 µA, 80 MHz Rail-to-Rail I/O Single Amplifier
ADA4841-1	Product Page, ADA4841-1, Low Power, Low Noise and Distortion, Rail-to-Rail Output Amplifier
ADR435	Product Page, ADR435, Ultralow Noise XFET® Voltage References with Current Sink and Source Capability
ADP7102	Product Page, ADP7102, 20 V, 300 mA, Low Noise, CMOS LDO
ADP7104	Product Page, ADP7104, 20 V, 500 mA, Low Noise, CMOS LDO
ADP2301	Product Page, ADP2301, 1.2 A, 20 V, 1.4 MHz Nonsynchronous Step-Down Switching Regulator
ADG3304	Product Page, ADG3304, Low Voltage 1.15 V to 5.5 V, 4 Channel, Bidirectional, Logic Level Translator
EVAL-SDP-CB1Z	Product Page, System Demonstration Platform (SDP)
AN-931	AN-931 Application Note, *Understanding PulSAR ADC Support Circuitry*