

Evaluation Board for Single, High Speed Op Amps Offered in 5-Lead SOT-23 and 6-Lead SOT-23 Packages

FEATURES

- Enables quick breadboarding/prototyping
- User defined circuit configuration
- Edge mounted SMA connector provisions
- Easy connection to test equipment and other circuits
- RoHS compliant

GENERAL DESCRIPTION

The Analog Devices, Inc., SOT-23 evaluation board evaluates single, high speed op amps offered in 5-lead and 6-lead SOT-23 packages. The evaluation board is a bare board that enables users to quickly prototype a variety of single op amp circuits, which minimizes risk and reduces time to market. Figure 1 shows the component side of the bare evaluation board. Figure 2 shows the circuit side of the bare evaluation board.

The 6-layer evaluation board accepts SMA edge-mounted connectors on the inputs and outputs for efficient connection to test equipment or other circuitry. The ground plane, component placement, and supply bypassing minimize parasitic inductance and capacitance. The evaluation board components are primarily SMT 0603 case size, with the exception of the electrolytic bypass capacitors (C1 and C2), which are 3528 case size.

Figure 3 shows the evaluation board schematic. The printed circuit board (PCB) assembly drawings are shown in Figure 4 and Figure 5. The layout pattern for the PCB is shown in Figure 6 and Figure 7.

EVALUATION BOARD IMAGES

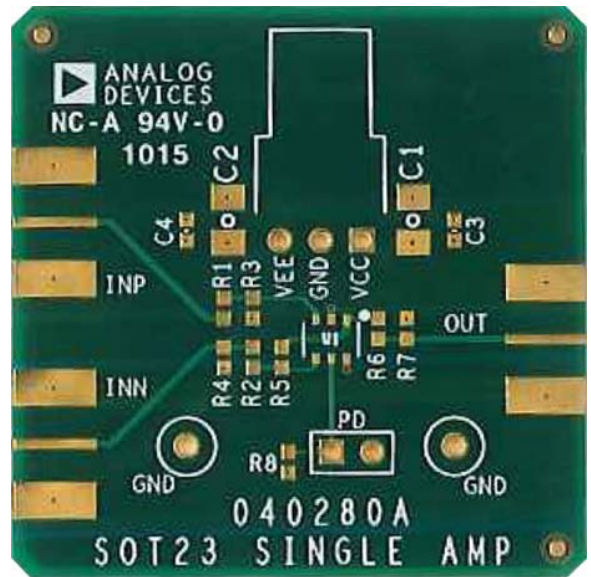


Figure 1. Component Side of Evaluation Board

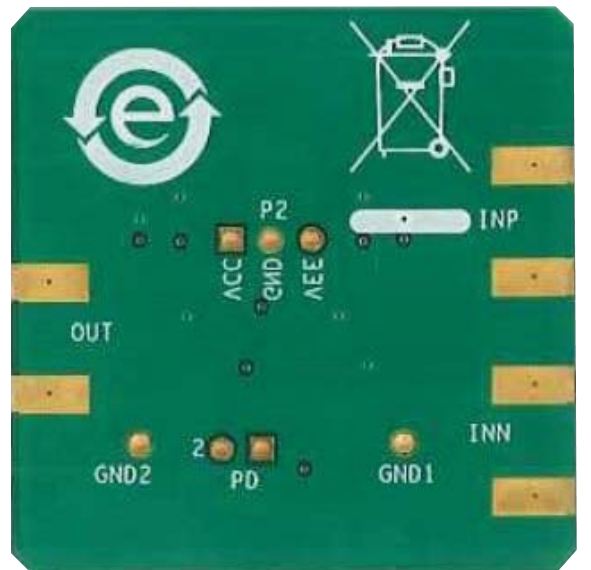


Figure 2. Circuit Side of Evaluation Board

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REVISION HISTORY

10/15—Revision 0: Initial Version

EVALUATION BOARD SCHEMATIC AND ARTWORK

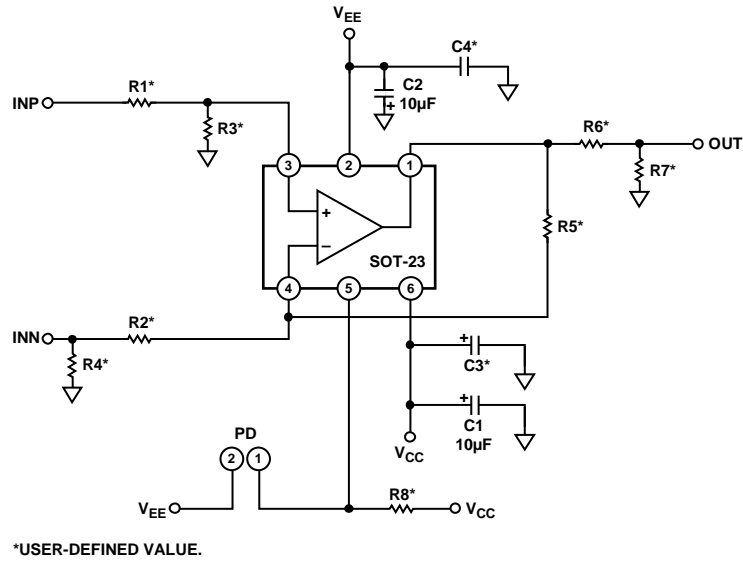


Figure 3. Evaluation Board Schematic

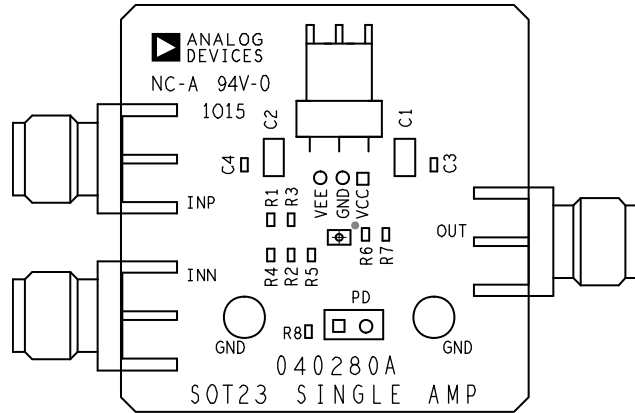


Figure 4. Component Side Assembly Drawing

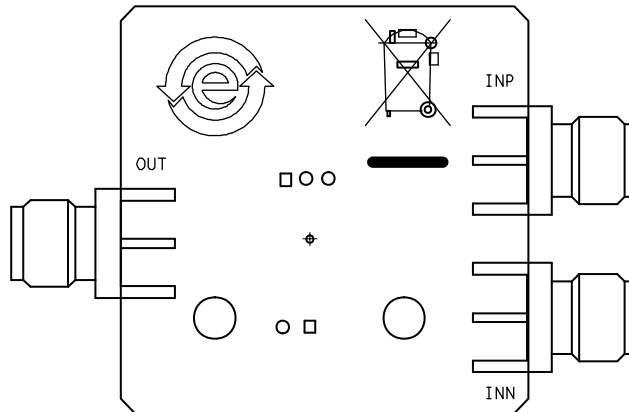
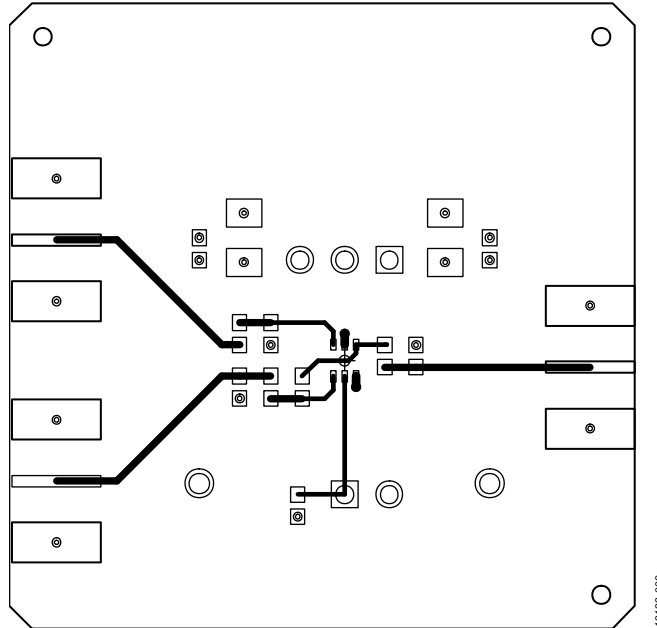
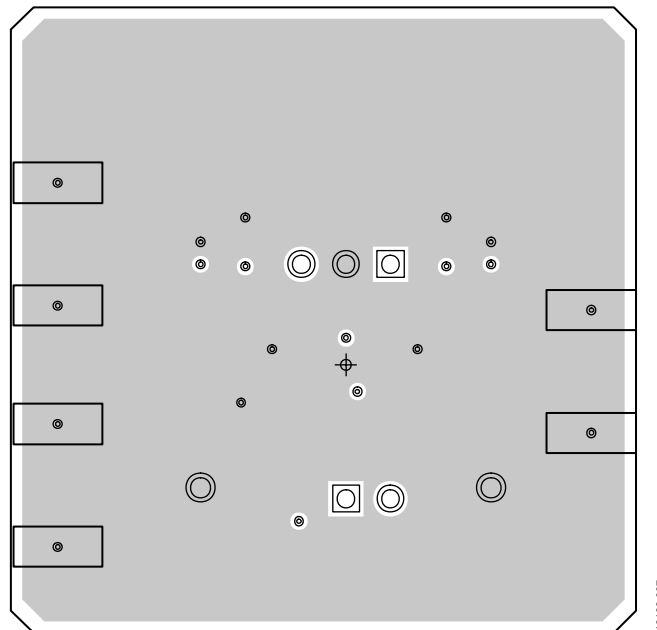


Figure 5. Circuit Side Assembly Drawing



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Figure 6. Component Side Layout Pattern



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Figure 7. Circuit Side Layout Pattern