

IGBT - Field Stop, Trench

1200 V, 40 A

FGH40T120SMD, FGH40T120SMD-F155

Description

Using innovative field stop trench IGBT technology, ON Semiconductor's new series of field stop trench IGBTs offer the optimum performance for hard switching application such as solar inverter, UPS, welder and PFC applications.

Features

- FS Trench Technology, Positive Temperature Coefficient
- High Speed Switching
- Low Saturation Voltage: $V_{CE(sat)} = 1.8 \text{ V @ } I_C = 40 \text{ A}$
- 100% of the Parts tested for $I_{LM}(1)$
- High Input Impedance
- These Devices are Pb-Free and are RoHS Compliant

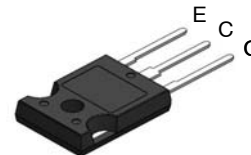
Applications

- Solar Inverter, Welder, UPS & PFC applications



ON Semiconductor®

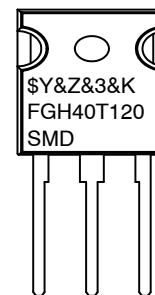
www.onsemi.com



TO-247-3LD
CASE 340CK

TO-247-3LD
CASE 340CH

MARKING DIAGRAM



\$Y	= ON Semiconductor Logo
&Z	= Assembly Plant Code
&3	= Numeric Date Code
&K	= Lot Code
FGH40T120SMD	= Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

FGH40T120SMD, FGH40T120SMD-F155

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

Description		Symbol	Ratings	Unit
Collector to Emitter Voltage		V _{CES}	1200	V
Gate to Emitter Voltage		V _{GES}	±25	V
Transient Gate to Emitter Voltage			±30	V
Collector Current	T _C = 25°C	I _C	80	A
Collector Current	T _C = 100°C		40	A
Clamped Inductive Load Current	T _C = 25°C	I _{LM} (Note 1)	160	A
Pulsed Collector Current		I _{CM} (Note 2)	160	A
Diode Continuous Forward Current	T _C = 25°C	I _F	80	A
Diode Continuous Forward Current	T _C = 100°C		40	A
Diode Maximum Forward Current		I _{FM}	240	A
Maximum Power Dissipation	T _C = 25°C	P _D	555	W
Maximum Power Dissipation	T _C = 100°C		277	W
Operating Junction Temperature		T _J	-55 to +175	°C
Storage Temperature Range		T _{stg}	-55 to +175	°C
Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds		T _L	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- V_{CC} = 600 V, V_{GE} = 15 V, I_C = 160 A, R_G = 10 W, Inductive Load
- Limited by T_{jmax}

THERMAL CHARACTERISTICS

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance, Junction to Case	R _{θJC} (IGBT)	-	0.27	°C/W
Thermal Resistance, Junction to Case	R _{θJC} (Diode)	-	0.89	°C/W
Thermal Resistance, Junction to Ambient	R _{θJA}	-	40	°C/W

PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGH40T120SMD	FGH40T120SMD	TO-247-3 (Pb-Free)	-	-	30
FGH40T120SMD	FGH40T120SMD-F155	TO-247-3 (Pb-Free)	-	-	30

ELECTRICAL CHARACTERISTICS OF THE IGBT (T_C = 25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
-----------	--------	-----------------	-----	-----	-----	------

OFF CHARACTERISTICS

Collector to Emitter Breakdown Voltage	BV _{CES}	V _{GE} = 0 V, I _C = 250 μA	1200	-	-	V
Collector Cut-Off Current	I _{CES}	V _{CE} = V _{CES} , V _{GE} = 0 V	-	-	250	μA
G-E Leakage Current	I _{GES}	V _{GE} = V _{GES} , V _{CE} = 0 V	-	-	±400	nA

ON CHARACTERISTICS

G-E Threshold Voltage	V _{GE(th)}	I _C = 40 mA, V _{CE} = V _{GE}	4.9	6.2	7.5	V
Collector to Emitter Saturation Voltage	V _{CE(sat)}	I _C = 40 A, V _{GE} = 15 V, T _C = 25°C	-	1.8	2.4	V
		I _C = 40 A, V _{GE} = 15 V, T _C = 175°C	-	2.0	-	V

FGH40T120SMD, FGH40T120SMD-F155

ELECTRICAL CHARACTERISTICS OF THE IGBT ($T_C = 25^\circ\text{C}$ unless otherwise noted) (continued)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
DYNAMIC CHARACTERISTICS						
Input Capacitance	C_{ies}	$V_{CE} = 30\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	-	4300	-	pF
Output Capacitance	C_{oes}		-	180	-	pF
Reverse Transfer Capacitance	C_{res}		-	100	-	pF
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	$t_{d(on)}$	$V_{CC} = 600\text{ V}, I_C = 40\text{ A},$ $R_G = 10\ \Omega, V_{GE} = 15\text{ V},$ Inductive Load, $T_C = 25^\circ\text{C}$	-	40	-	ns
Rise Time	t_r		-	47	-	ns
Turn-Off Delay Time	$t_{d(off)}$		-	475	-	ns
Fall Time	t_f		-	10	-	ns
Turn-On Switching Loss	E_{on}		-	2.7	-	mJ
Turn-Off Switching Loss	E_{off}		-	1.1	-	mJ
Total Switching Loss	E_{ts}		-	3.8	-	mJ
Turn-On Delay Time	$t_{d(on)}$	$V_{CC} = 600\text{ V}, I_C = 40\text{ A},$ $R_G = 10\ \Omega, V_{GE} = 15\text{ V},$ Inductive Load, $T_C = 175^\circ\text{C}$	-	40	-	ns
Rise Time	t_r		-	55	-	ns
Turn-Off Delay Time	$t_{d(off)}$		-	520	-	ns
Fall Time	t_f		-	50	-	ns
Turn-On Switching Loss	E_{on}		-	3.4	-	mJ
Turn-Off Switching Loss	E_{off}		-	2.5	-	mJ
Total Switching Loss	E_{ts}		-	5.9	-	mJ
Total Gate Charge	Q_g	$V_{CE} = 600\text{ V}, I_C = 40\text{ A}, V_{GE} = 15\text{ V}$	-	370	-	nC
Gate to Emitter Charge	Q_{ge}		-	23	-	nC
Gate to Collector Charge	Q_{gc}		-	210	-	nC

ELECTRICAL CHARACTERISTICS OF THE DIODE ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Diode Forward Voltage	V_{FM}	$I_F = 40\text{ A}, T_C = 25^\circ\text{C}$	-	3.8	4.8	V
		$I_F = 40\text{ A}, T_C = 175^\circ\text{C}$	-	2.7	-	V
Diode Reverse Recovery Time	t_{rr}	$V_R = 600\text{ V}, I_F = 40\text{ A},$ $di_F/dt = 200\text{ A}/\mu\text{s}, T_C = 25^\circ\text{C}$	-	65	-	ns
Diode Peak Reverse Recovery Current	I_{rr}		-	7.2	-	A
Diode Reverse Recovery Charge	Q_{rr}		-	234	-	nC
Diode Reverse Recovery Time	t_{rr}	$V_R = 600\text{ V}, I_F = 40\text{ A},$ $di_F/dt = 200\text{ A}/\mu\text{s}, T_C = 175^\circ\text{C}$	-	200	-	ns
Diode Peak Reverse Recovery Current	I_{rr}		-	18.0	-	A
Diode Reverse Recovery Charge	Q_{rr}		-	1800	-	nC

FGH40T120SMD, FGH40T120SMD-F155

TYPICAL PERFORMANCE CHARACTERISTICS

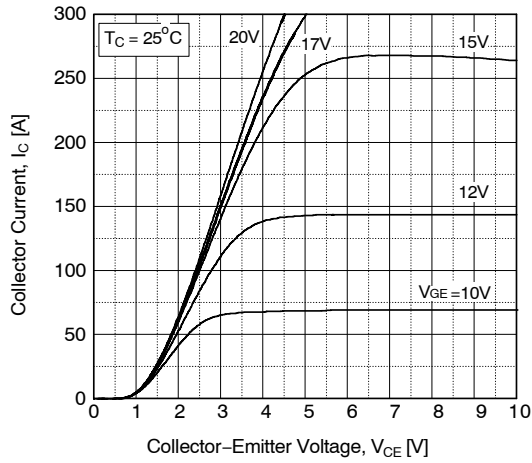


Figure 1. Typical Output Characteristics

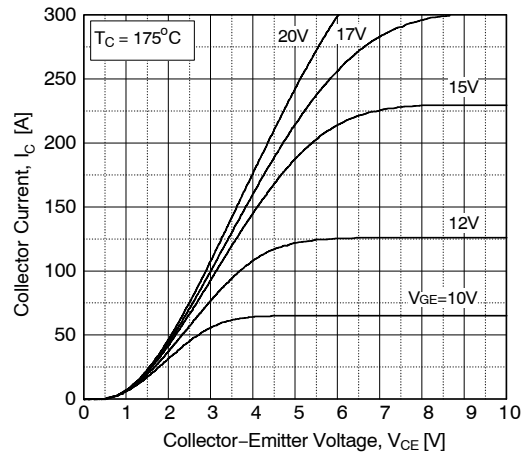


Figure 2. Typical Output Characteristics

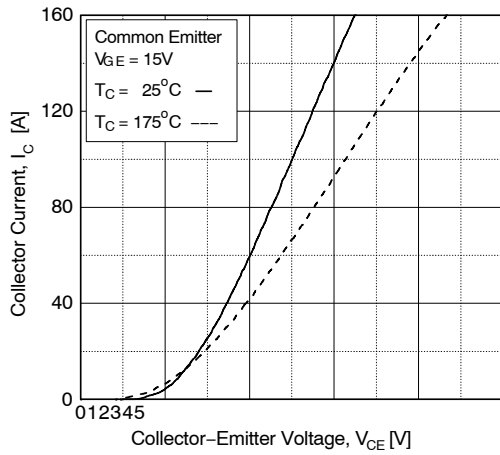


Figure 3. Typical Saturation Voltage Characteristics

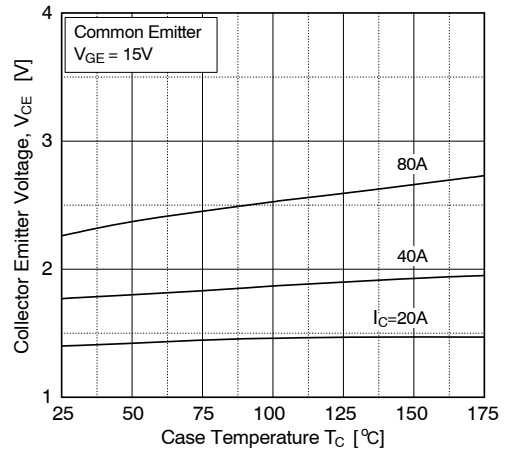


Figure 4. Saturation Voltage vs. Case Temperature at Variant Current Level

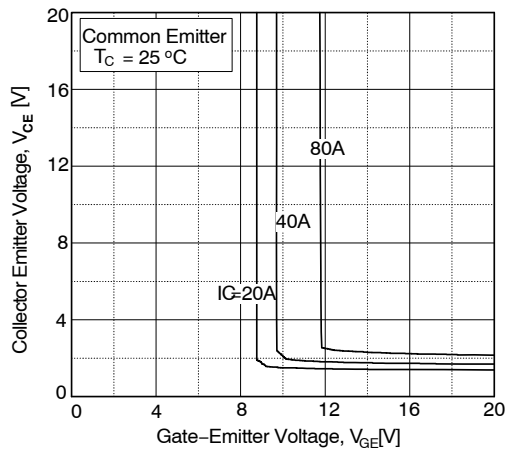


Figure 5. Saturation Voltage vs V_{GE}

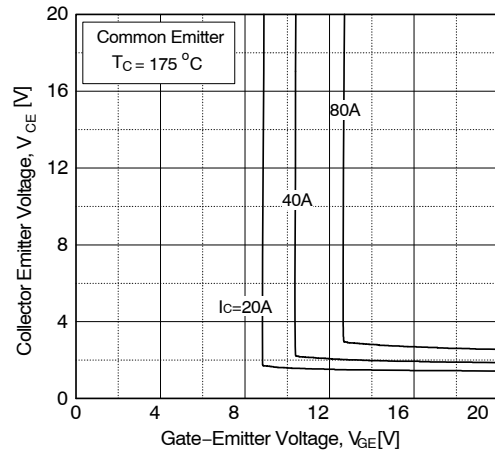


Figure 6. Saturation Voltage vs V_{GE}

FGH40T120SMD, FGH40T120SMD-F155

TYPICAL PERFORMANCE CHARACTERISTICS

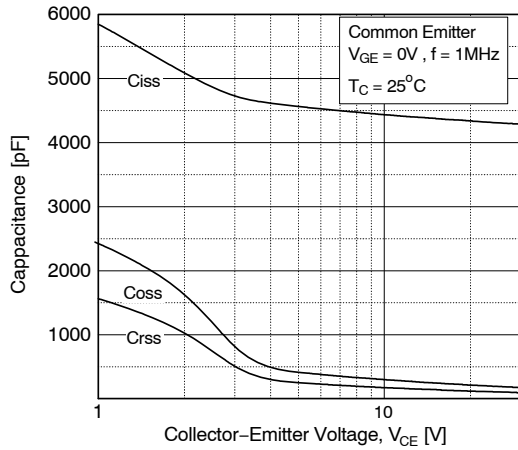


Figure 7. Capacitance Characteristics

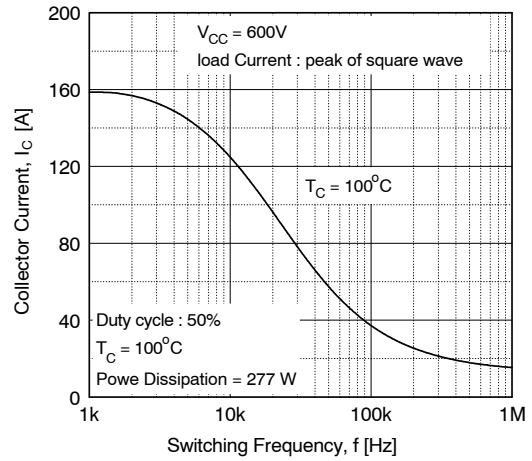


Figure 8. Load Current vs. Frequency

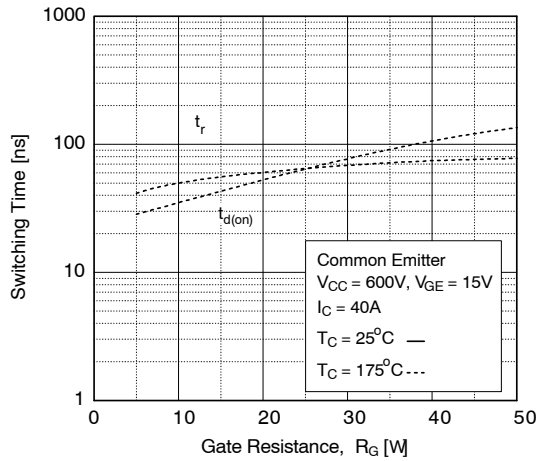


Figure 9. Turn-On Characteristics vs. Gate Resistance

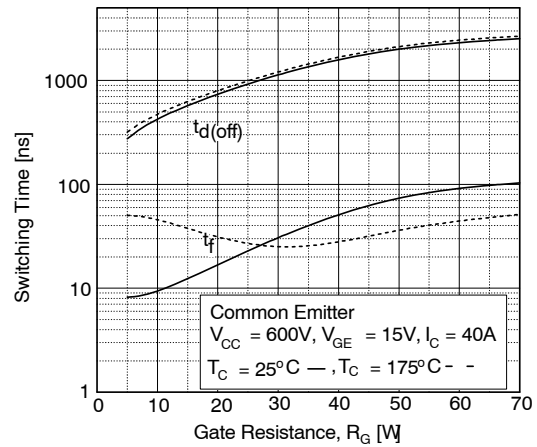


Figure 10. Turn-Off Characteristics vs. Collector Current

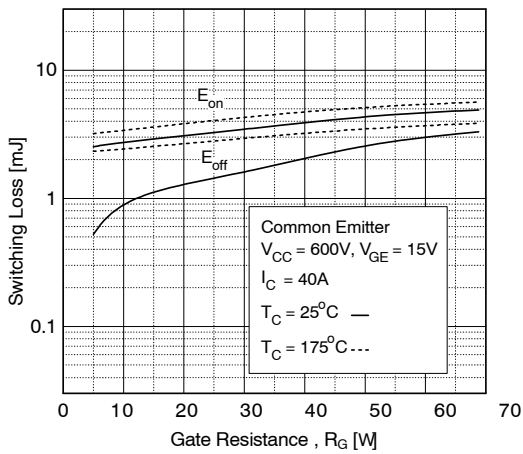


Figure 11. Switching Loss vs. Gate Resistance

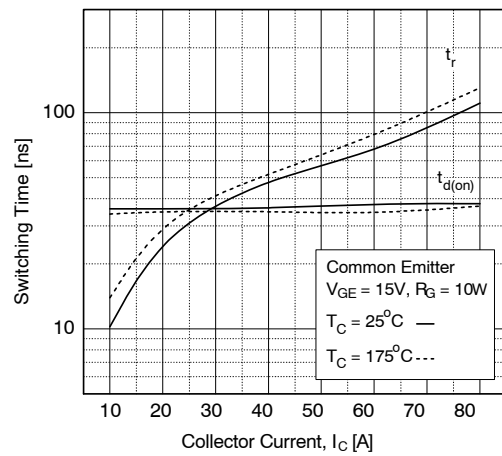


Figure 12. Turn-On Characteristics vs. Collector Current

FGH40T120SMD, FGH40T120SMD-F155

TYPICAL PERFORMANCE CHARACTERISTICS

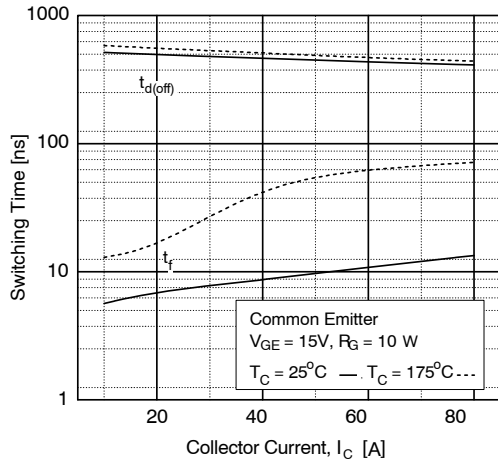


Figure 13. Turn-Off Characteristics vs. Collector Current

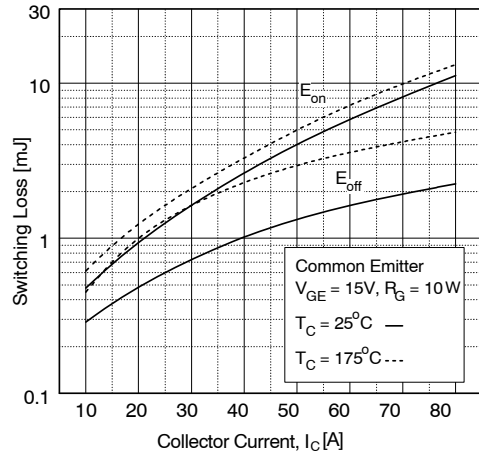


Figure 14. Switching Loss vs. Collector Current

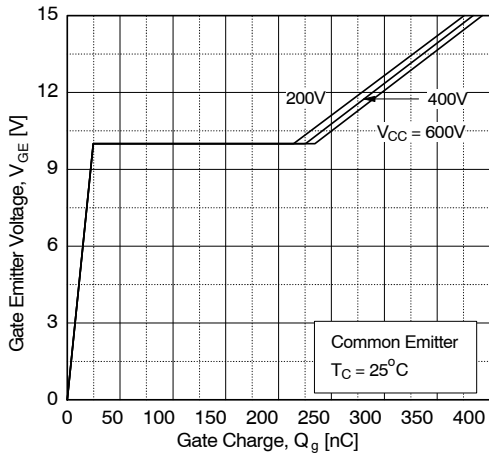


Figure 15. Gate Charge Characteristics

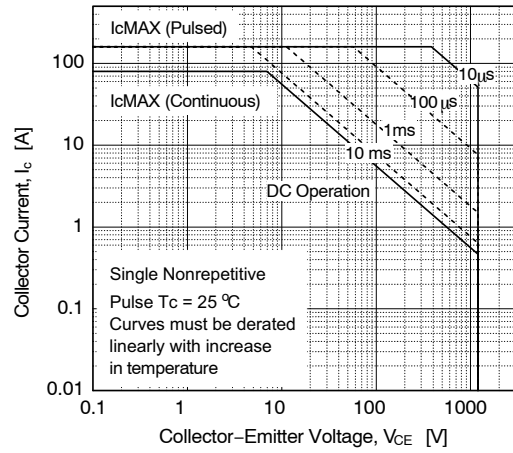


Figure 16. SOA Characteristics

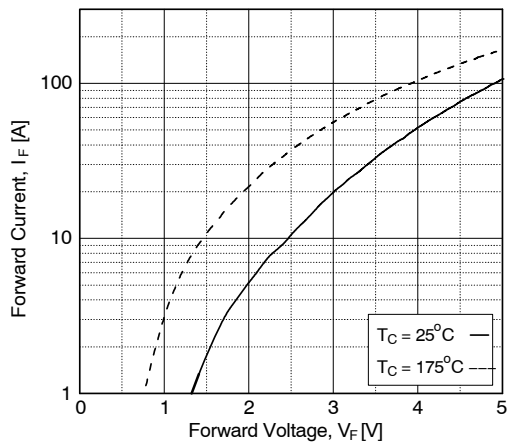


Figure 17. Forward Characteristics

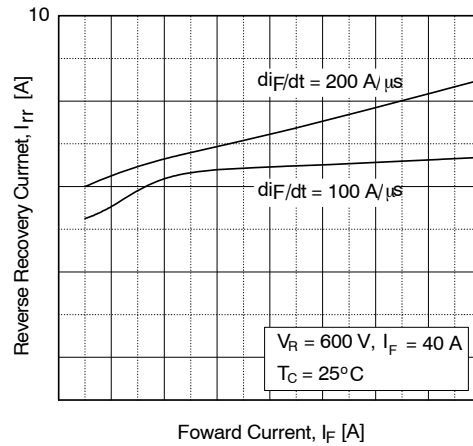


Figure 18. Reverse Recovery Current

FGH40T120SMD, FGH40T120SMD-F155

TYPICAL PERFORMANCE CHARACTERISTICS

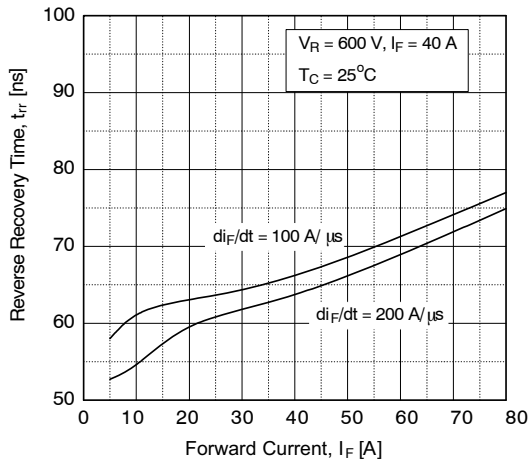


Figure 19. Reverse Recovery Time

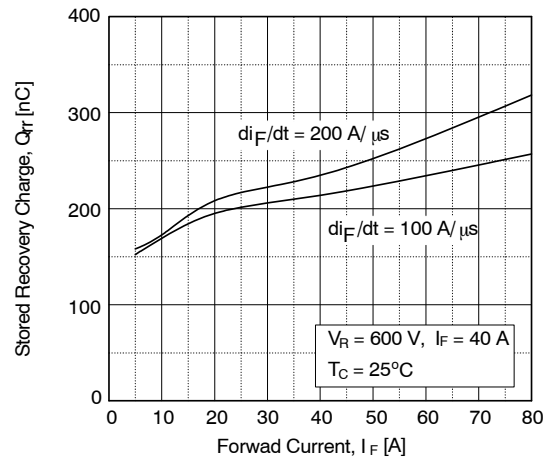


Figure 20. Stored Charge

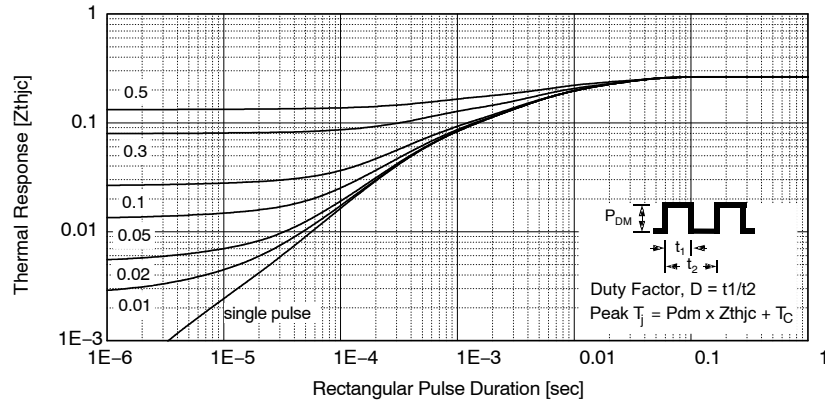


Figure 21. Transient Thermal Impedance of IGBT

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

ON Semiconductor®



TO-247-3LD
CASE 340CH
ISSUE A

DATE 09 OCT 2019



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.58	4.70	4.82
A1	2.29	2.475	2.66
A2	1.40	1.50	1.60
D	20.32	20.57	20.82
E	15.37	15.62	15.87
E2	4.96	5.08	5.20
e	~	5.56	~
L	19.75	20.00	20.25
L1	3.69	3.81	3.93
∅P	3.51	3.58	3.65
Q	5.34	5.46	5.58
S	5.34	5.46	5.58
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
b4	2.42	2.54	2.66
c	0.51	0.61	0.71
D1	13.08	~	~
D2	0.51	0.93	1.35
E1	12.81	~	~
∅P1	6.61	6.73	6.85

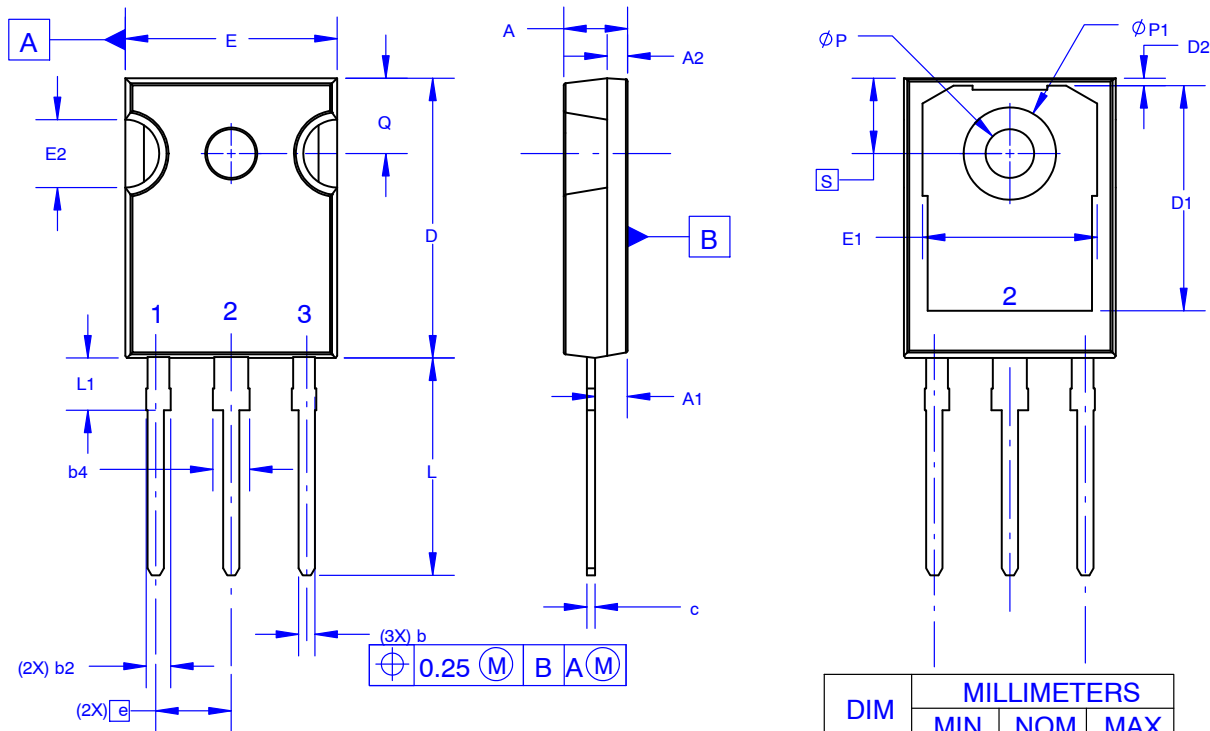
DOCUMENT NUMBER:	98AON13853G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	TO-247-3LD	PAGE 1 OF 1

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.



TO-247-3LD SHORT LEAD
CASE 340CK
ISSUE A

DATE 31 JAN 2019



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code
 A = Assembly Location
 Y = Year
 WW = Work Week
 ZZ = Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.58	4.70	4.82
A1	2.20	2.40	2.60
A2	1.40	1.50	1.60
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
b4	2.42	2.54	2.66
c	0.51	0.61	0.71
D	20.32	20.57	20.82
D1	13.08	~	~
D2	0.51	0.93	1.35
E	15.37	15.62	15.87
E1	12.81	~	~
E2	4.96	5.08	5.20
e	~	5.56	~
L	15.75	16.00	16.25
L1	3.69	3.81	3.93
∅P	3.51	3.58	3.65
∅P1	6.60	6.80	7.00
Q	5.34	5.46	5.58
S	5.34	5.46	5.58

DOCUMENT NUMBER:	98AON13851G	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	TO-247-3LD SHORT LEAD	PAGE 1 OF 1

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.