

HBS410

4.0A SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

Product Summary (@TA = +25°C)

VRRM (V)	lo (A)	V _F (V)	l _R (μA)
1000	4.0	0.98	5

Description and Applications

General purpose use in AC-to-DC bridge full wave rectification for Fast Charging, Switching Power Supply, USB PD, Adapter and 3-in-1 DTV Power Board, etc.

Features and Benefits

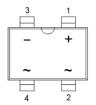
- Glass Passivated Die Construction
- Miniature Surface Mount Package Saves Space on PC Boards
- High Current Capability
- High Forward Current Capability up to 4.0A
- High Heat Dissipation Capability
- Low Profile Package
- Low Forward Voltage Drop
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

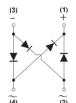
- Case: HBS
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Lead Free Plating (Matte Tin Finish). Solderable per MIL-STD-202, Method 208 (23)
- Polarity: As Marked on Body
- Weight: 0.384grams (Approximate)



Top View



Pin Diagram



Internal Schematic

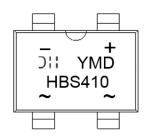
Ordering Information (Note 4)

Part Number	Compliance	Case	Packaging
HBS410-13	Commercial	HBS	2,500/Tape & Reel

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



HBS410 = Product Type Marking Code

Old = Manufacturers' Code Marking

YMD = Date Code Marking

Y = Last Digit of Year (ex: 0 = 2020)

M = See Month/Code Table Below

D = Day 1 to 9 = 1 to 9; Day 10 to 31 = A to V

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	1,000	>
RMS Reverse Voltage	V _{R(RMS)}	700	V
Average Rectified Output Current (Note 5) @ T _A = +25°C	lo	4.0	Α
Non-Repetitive Peak Forward Surge Current, 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	120	Α
Non-Repetitive Peak Forward Surge Current, 1.0ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	240	Α
I ² t Rating for Fusing (1ms < t < 8.3ms)	l ² t	60	A ² S

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance, Junction to Ambient (Note 5) (Per Element)	R _{θJA}	75	°C/W
Typical Thermal Resistance, Junction to Lead (Per Element)	Røjl	21	°C/W
Typical Thermal Resistance, Junction to Case (Per Element)	Rejc	13	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	$V_{(BR)R}$	1,000	_	_	V	$I_R = 10\mu A$
Forward Voltage (Per Element)	VF		0.84 0.88 0.93	0.89 0.93 0.98		IF = 1A, T _A = +25°C IF = 2A, T _A = +25°C I _F = 4A, T _A = +25°C
Leakage Current (Note 6) (Per Element)	IR	-	0.15 20	5 100	μA	V _R = 1,000V, T _A = +25°C V _R = 1,000V, T _A = +125°C
Total Capacitance (Per Element)	Ст	_	40	_	pF	$V_R = 4V, f = 1.0MHz$

5. Device mounted on 15mmx12mmx1.6mm AL Pad attached on 100mmx75mmx27mm Fin heatsink. Thermal resistance test performed in accordance with

JESD-51. 6. Short duration pulse test used to minimize self-heating effect.

Notes:



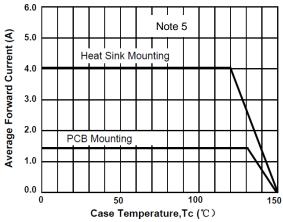


Figure 1. Forward Current Derating

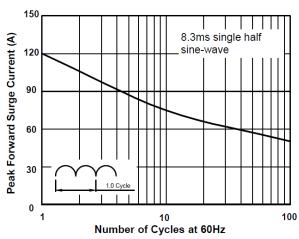


Figure 3. Maximum Non-Repetitive Forward Surge Current

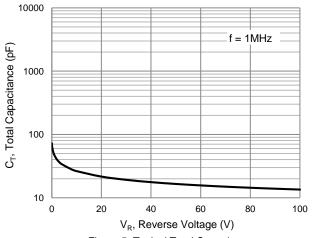


Figure 5. Typical Total Capacitance

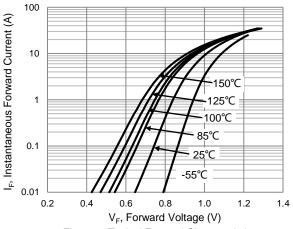


Figure 2. Typical Forward Characteristics

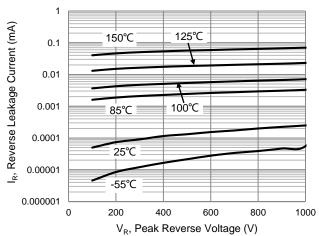


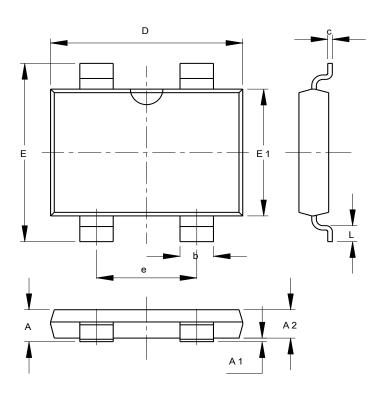
Figure 4. Typical Reverse Characteristics



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

HBS

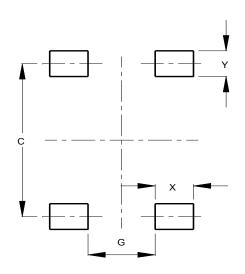


HBS							
Dim	Min	Тур					
Α	1.45	1.80					
A1	0.00	0.20					
A2	1.45	1.65					
b	1.70	1.90					
С	0.15	0.35					
D	10.05	10.35					
Е	9.75	10.05					
E1	6.85	7.15					
е	5.25	5.60					
L	0.45	0.95					
All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.





Dimensions	Value (in mm)		
С	8.92		
G	3.50		
Х	2.00		
Υ	1.50		