



Legend:
 IO: IACK device
 SA: Single address DMA transfer
 DA: Dual address DMA transfer
 DACK set to active-high

Figure 22.46 DRAM Burst Bus Cycle (Fast Page Mode, RCD[1:0] = 01, AnW[2:0] = 001, TPC[2:0] = 001, 2-Cycle CAS Negate Pulse Width)

