

AEDR-9820 & 9830

Evaluation Board User Guide

User Guide

Version 1.0

Footer

AEDR-9820 & 9830 User Guide Evaluation Board User Guide

Broadcom, the pulse logo, Connecting everything, Avago Technologies, Avago, and the A logo are among the trademarks of Broadcom and/or its affiliates in the United States, certain other countries and/or the EU.

Copyright © 2017 by Broadcom. All Rights Reserved.

The term "Broadcom" refers to Broadcom Limited and/or its subsidiaries. For more information, please visit www.broadcom.com.

Broadcom reserves the right to make changes without further notice to any products or data herein to improve reliability, function, or design. Information furnished by Broadcom is believed to be accurate and reliable. However, Broadcom does not assume any liability arising out of the application or use of this information, nor the application or use of any product or circuit described herein, neither does it convey any license under its patent rights nor the rights of others.

Footer Document_Number

Table of Contents

Top and Bottom Views	4
	Top and Bottom Views

Footer Document_Number

1.0 Top and Bottom Views

Figure 1 Bottom Side of the PCB

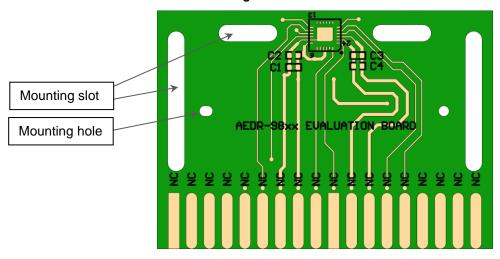
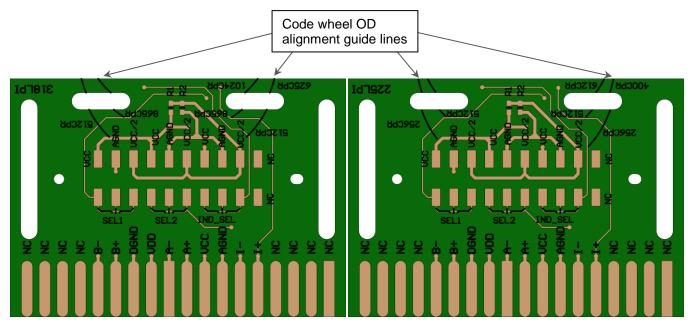


Figure 2 Top Side of the PCB (Left 318 LPI & Right 225 LPI)



NOTE Please remove the protective kapton tape covering the encoder ASIC before use.

The silk screen printed guide line on the PCB is to help in providing visual alignment of the code wheel edge (outer diameter) for each of the different Rop (CPR) track. A sample diagram showing the position when encoder is aligned to 625 CPR track is as shown in Figure 3 below.

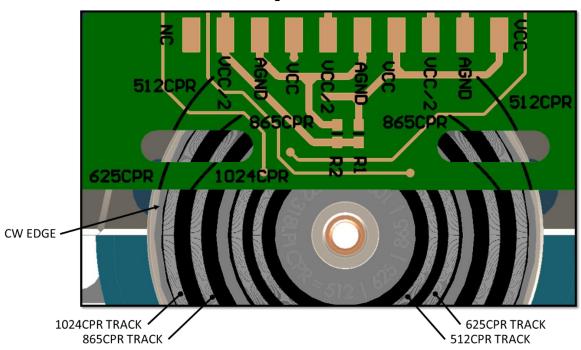
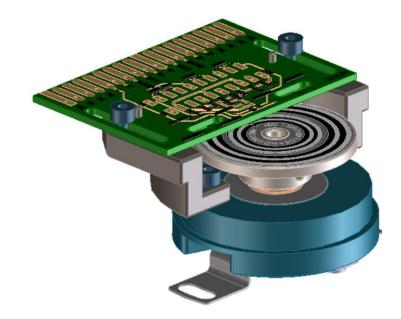


Figure 3 Bottom Side of the PCB

Figure 4 Sample Evaluation Board Mounting with reference to Code Wheel



2.0 Select Options

2.1 Selection Table

Table 1 Selection Table for 318 LPI AEDR-9830 Variant

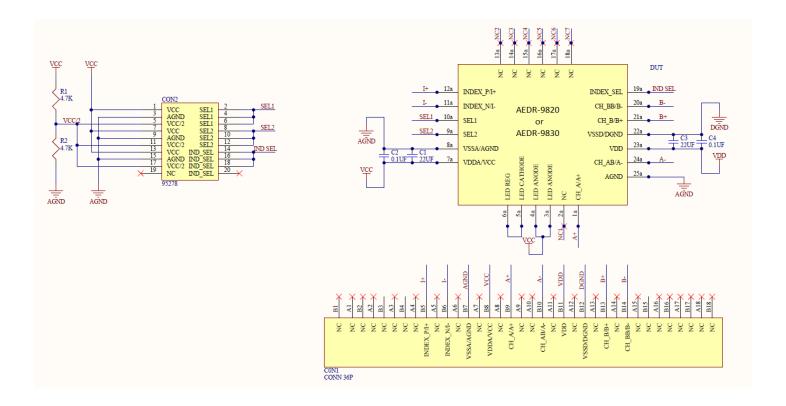
SEL1	SEL 2	INDEX SEL	Interpolation Factor	Index	Max output frequency	CPR @ROP 7.95mm	CPR @ROP 11 mm
		Low		Gated 90 deg			
Open	Open	High	1X	Gated 180 deg	200 kHz	625	865
		Open		Ungated raw			
		Low		Gated 90 deg			
Open	Low	High	2X	Gated 180 deg	400 kHz	1250	1730
		Open		Gated 360 deg			
		Low		Gated 90 deg			
High	High	High	4X	Gated 180 deg	800 kHz	2500	3460
		Open		Gated 360 deg			
		Low		Gated 90 deg			
Low	Low	High	8X	Gated 180 deg	1.6 MHz	5000	6920
		Open		Gated 360 deg			
		Low		Gated 90 deg			
High	Low	High	16X	Gated 180 deg	2.0 MHz	10000	13840
		Open		Gated 360 deg			
Open	High	NA	Analog (500mVpp)	Analog	200 kHz	NA	NA
Low	High	NA	Analog 1Vpp	Ungated Digital			
High / Low	Open	NA	Analog 1Vpp	Analog			

Table 2 Selection Table for 225 LPI AEDR-9820 Variant

SEL1	SEL 2	INDEX SEL	Interpolation Factor	Index	Max output frequency	CPR @ROP 4.6 mm	CPR @ROP 11 mm
		Low		Gated 90 deg			
Open	Open	High	1X	Gated 180 deg	200 kHz	256	612
		Open		Ungated raw			
		Low		Gated 90 deg			
Open	Low	High	2X	Gated 180 deg	400 kHz	512	1224
		Open		Gated 360 deg			
		Low		Gated 90 deg			
High	High	High	4X	Gated 180 deg	800 kHz	1024	2448
		Open		Gated 360 deg			
		Low		Gated 90 deg			
Low	Low	High	8X	Gated 180 deg	1.6 MHz	2048	4896
		Open		Gated 360 deg			
		Low		Gated 90 deg			
High	Low	High	16X	Gated 180 deg	2.0 MHz	4096	9792
		Open		Gated 360 deg			
Open	High	NA	Analog (500mVpp)	Analog	200 kHz	NA	NA
Low	High	NA	Analog 1Vpp	Ungated Digital			
High / Low	Open	NA	Analog 1Vpp	Analog			

3.0 Board Schematic & Pin Assignment

Figure 5 Evaluation Board Schematic



3.1 Connector Assignment

Table 3: Connector 1 Pin Assignment

Connector 1 (Top Side)	Label		
1	NC		
2	NC		
3	NC		
4	NC		
5	INDEX_P/I+		
6	INDEX_P/I-		
7	VSSA/AGND		
8	VDDA/VCC		
9	CH_A/A+		
10	CH_AB/A-		
11	VDD		
12	VSSD/DGND		
13	CH_B/B+		
14	CH_BB/B-		
15	NC		
16	NC		
17	NC		
18	NC		

The finger design of Connector 1 is match to the following connectors:

- 1. EDAC, CONN EDGE DUAL FMALE 36POS 0.100, P/N# 395-036-520-202 or,
- 2. SULLINS, CONN EDGE DUAL FMALE 36POS 0.100, P/N# EBC18DREH

The use of the above mentioned card edge connector is not needed if necessary connections can be made via manual soldering to the relevant card edge fingers.

Table 4 Connector 2 Pin Assignment

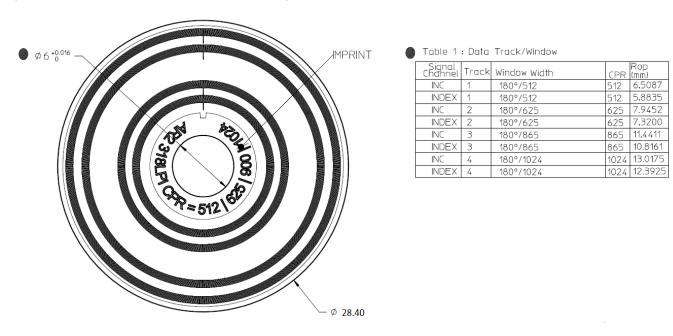
Connector 1 (Top Side)	Label	State
1		VCC
2	SEL1	AGND
3		OPEN
4		VCC
5	SEL2-	AGND
6		OPEN
7		VCC
8	INDEX_SEL	AGND
9		OPEN
10	NC	

NOTE Please refer to Table 1 (AEDR-9830 318 LPI) or Table 2 (AEDR-9820 225 LPI) for the various selection options available by changing the respective jumper position.

4.0 Code Wheel Drawing

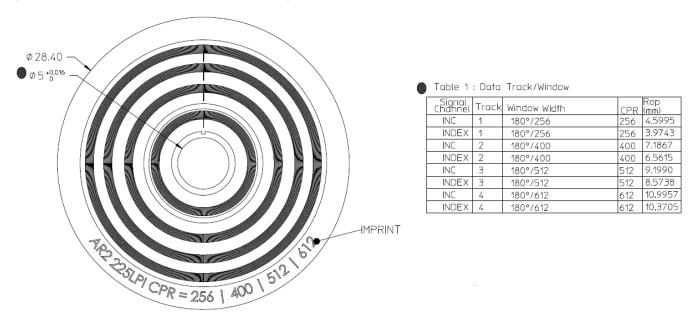
For AEDR-9830 evaluation board sample, the matching code wheel sample drawing is as shown in Figure 4 below.

Figure 6 318 LPI 4-track (CPR) code wheel drawing



For AEDR-9820 evaluation board sample, the matching code wheel sample drawing is as shown in Figure 5 below.

Figure 7 225 LPI 4-track (CPR) code wheel drawing



For the detailed drawing of the sample code wheel, please do request from regional FAE.