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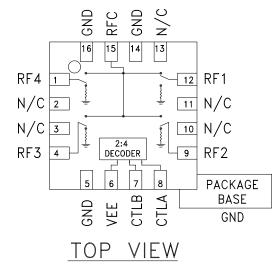


Typical Applications

This switch is suitable for usage in DC - 8.0 GHz 50-Ohm or 75-Ohm systems:

- Broadband
- Fiber Optics
- Switched Filter Banks
- Wireless below 8 GHz

Functional Diagram



HMC344LP3 / 344LP3E

GaAs MMIC SP4T NON-REFLECTIVE SWITCH, DC - 8 GHz

Features

Broadband Performance: DC - 8 GHz High Isolation: 40 dB@ 6 GHz Low Insertion Loss: 1.8 dB@ 6 GHz Integrated 2:4 TTL Decoder 16 Lead 3x3mm QFN Package: 9 mm²

General Description

The HMC344LP3 & HMC344LP3E are broadband non-reflective GaAs MESFET SP4T switches in low cost leadless surface mount packages. Covering DC to 8 GHz, this switch offers high isolation and low insertion loss and extends the frequency coverage of Hittite's SP4T switch product line. This switch also includes an on board binary decoder circuit which reduces the required logic control lines to two. The switch operates using a negative control voltage of 0/-5V, and requires a fixed bias of -5V.

Electrical Specifications, $T_A = +25^{\circ}$ C, With 0/-5V Control, 50 Ohm System

Parameter		Frequency	Min.	Тур.	Max.	Units
Insertion Loss		DC - 2.0 GHz DC - 6.0 GHz DC - 8.0 GHz		1.6 1.8 2.1	2.0 2.2 2.5	dB dB dB
Isolation		DC - 2.0 GHz DC - 4.0 GHz DC - 6.0 GHz DC - 8.0 GHz	43 36 34 31	48 41 40 36		dB dB dB dB
Return Loss	"On State"	DC - 2.0 GHz DC - 4.0 GHz DC - 6.0 GHz DC - 8.0 GHz	12 9 8 5	15 12 11 8		dB dB dB dB
Return Loss	"Off State"	DC - 8.0 GHz	7	10		dB
Input Power for 1 dB Compression		0.5 - 8.0 GHz	17	21		dBm
Input Third Order Intercept (Two-Tone Input Power = +7 dBm Each Tone)		0.5 - 8.0 GHz	37	40		dBm
Switching Characteristics tRISE, tFALL (10/90% RF) tON, tOFF (50% CTL to 10/90% RF)		DC - 8.0 GHz		35 150		ns ns

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SWITCH, DC - 8 GHz

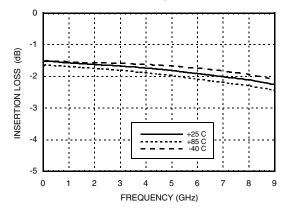
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GaAs MMIC SP4T NON-REFLECTIVE

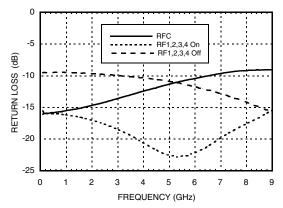
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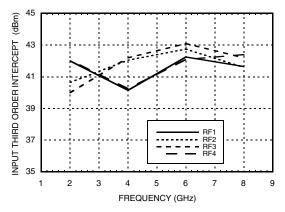
Insertion Loss vs. Temperature

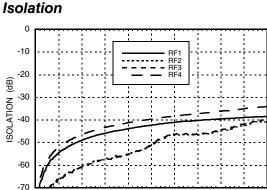


Return Loss



Input Third Order Intercept Point

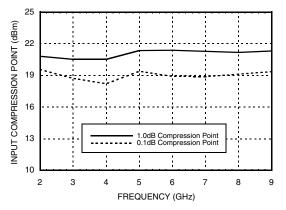




2 4 5 8 6 FREQUENCY (GHz)

3

0.1 and 1 dB Input Compression Point



Bias Voltage & Current

Vee Range = -5.0 Vdc ± 10%			
Vee (Vdc)	lee (Typ.) (mA)	lee (Max.) (mA)	
-5.0	3.0	6.0	

Control Voltages

State	Bias Condition
Low	-3V to 0 Vdc @ 60 μA Typical
High	-5 to -4.2 Vdc @ 5 µA Typical

0

1

SWITCHES - MULTI-THROW - SMT

15

* Isolation is recorded above insertion loss & measured at output of switch.

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GaAs MMIC SP4T NON-REFLECTIVE SWITCH, DC - 8 GHz

EARTH FRIENDLY

Absolute Maximum Ratings

Bias Voltage Range (Vee)	-7.0 Vdc
Control Voltage Range (A & B)	Vee -0.5V to +1.0 Vdc
Channel Temperature	150 °C
Thermal Resistance (Insertion Loss Path)	143 °C/W
Thermal Resistance (Terminated Path)	1,030 °C/W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C
Maximum Input Power	+24 dBm
ESD Sensitivity (HBM)	Class 1A

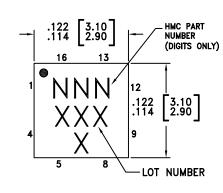
Truth Table

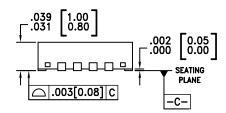
Control Input		Signal Path State	
А	В	RFCOM to:	
High	High	RF1	
Low	High	RF2	
High	Low	RF3	
Low	Low	RF4	



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

Outline Drawing





BOTTOM VIEW -.016 [0.40] REF 16 0.30 .008 [0.20] MIN .012 .007 $\cup \cup \cup \cup$ PIN 1 0.56 0.44 .061 .057 1.56 .017 Г Π EXPOSED GROUND PADDLE .077 .059 MUST BE CONNECTED TO 1.50 RF/DC GROUND SQUARE

NOTES:

1. LEADFRAME MATERIAL: COPPER ALLOY

- 2. DIMENSIONS ARE IN INCHES [MILLIMETERS].
- 3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE
- 4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM.
- PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
- 5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.

6. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.

7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED PCB LAND PATTERN.

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[3]
HMC344LP3	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	344 XXXX
HMC344LP3E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	<u>344</u> XXXX

[1] Max peak reflow temperature of 235 °C

[2] Max peak reflow temperature of 260 °C

[3] 4-Digit lot number XXXX

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ROHS

GaAs MMIC SP4T NON-REFLECTIVE SWITCH, DC - 8 GHz

Pin Descriptions

Pin Number	Function	Description	Interface Schematic	
1, 4, 9, 12, 15	RF4, RF3, RF2, RF1, RFC	This pin is DC coupled and matched to 50 Ohm. Blocking capacitors are required if RF line potential is not equal to 0V.		
2, 3, 10, 11, 13	N/C	This pin should be connected to PCB RF ground to maximize isolation.		
5, 14, 16	GND	Package bottom has exposed metal paddle that must also be connected to PCB RF ground.		
6	VEE	Supply Voltage -5V ± 10%	VEE 0 5рF 2К 	
7	CTLB	See truth table and control voltage table.	100К	
8	CTLA	See truth table and control voltage table.		

SWITCHES - MULTI-THROW - SMT

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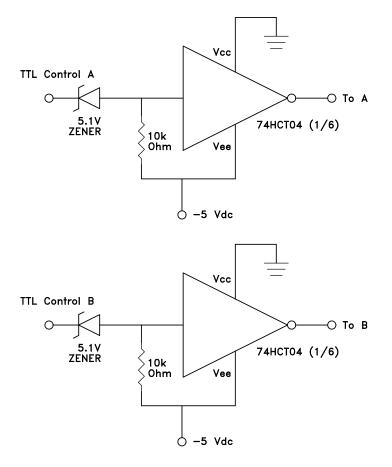


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TTL Interface Circuit



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