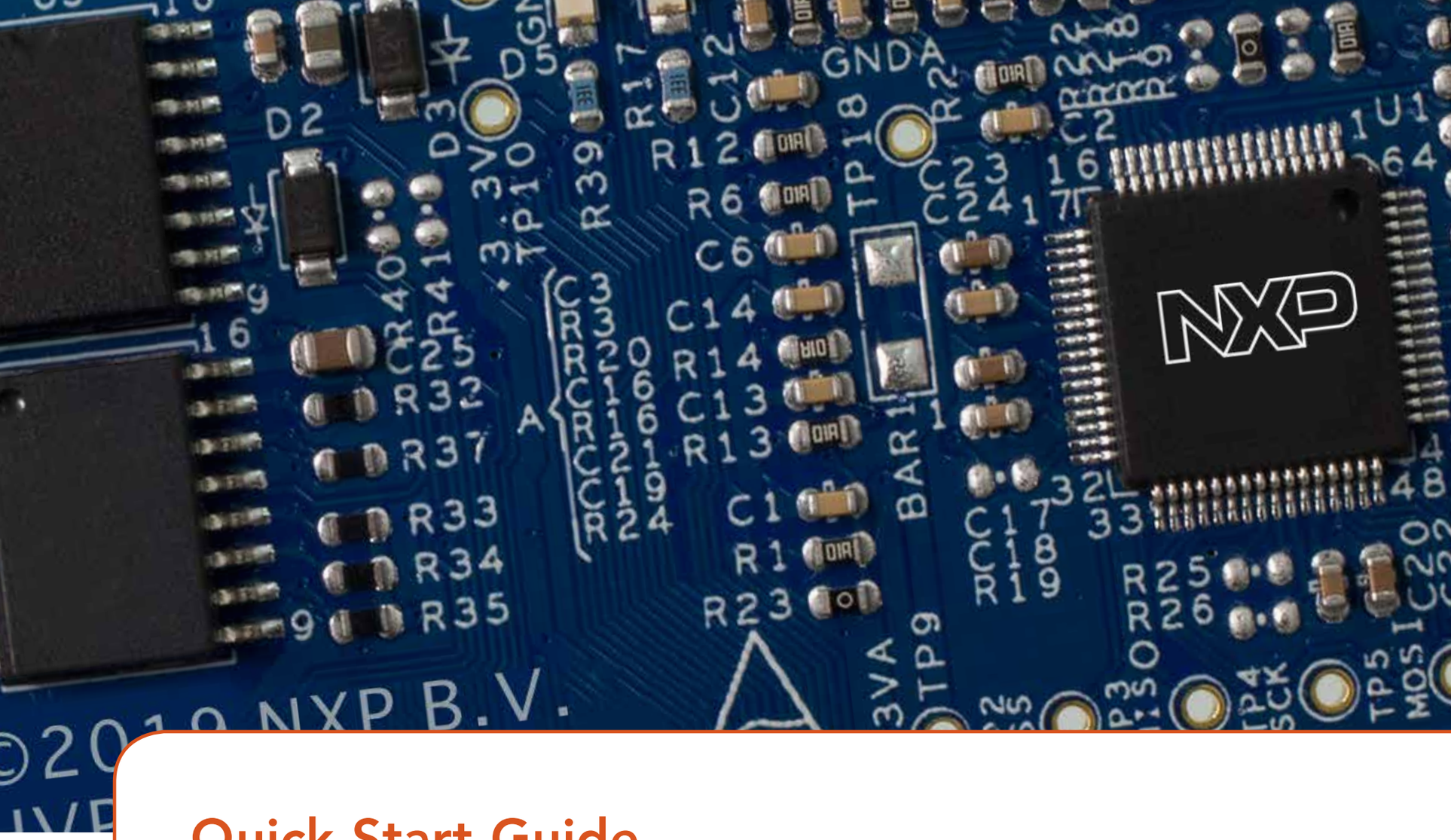

Quick Start Guide

HVP-56F83783

GET TO KNOW THE HVP-56F83783

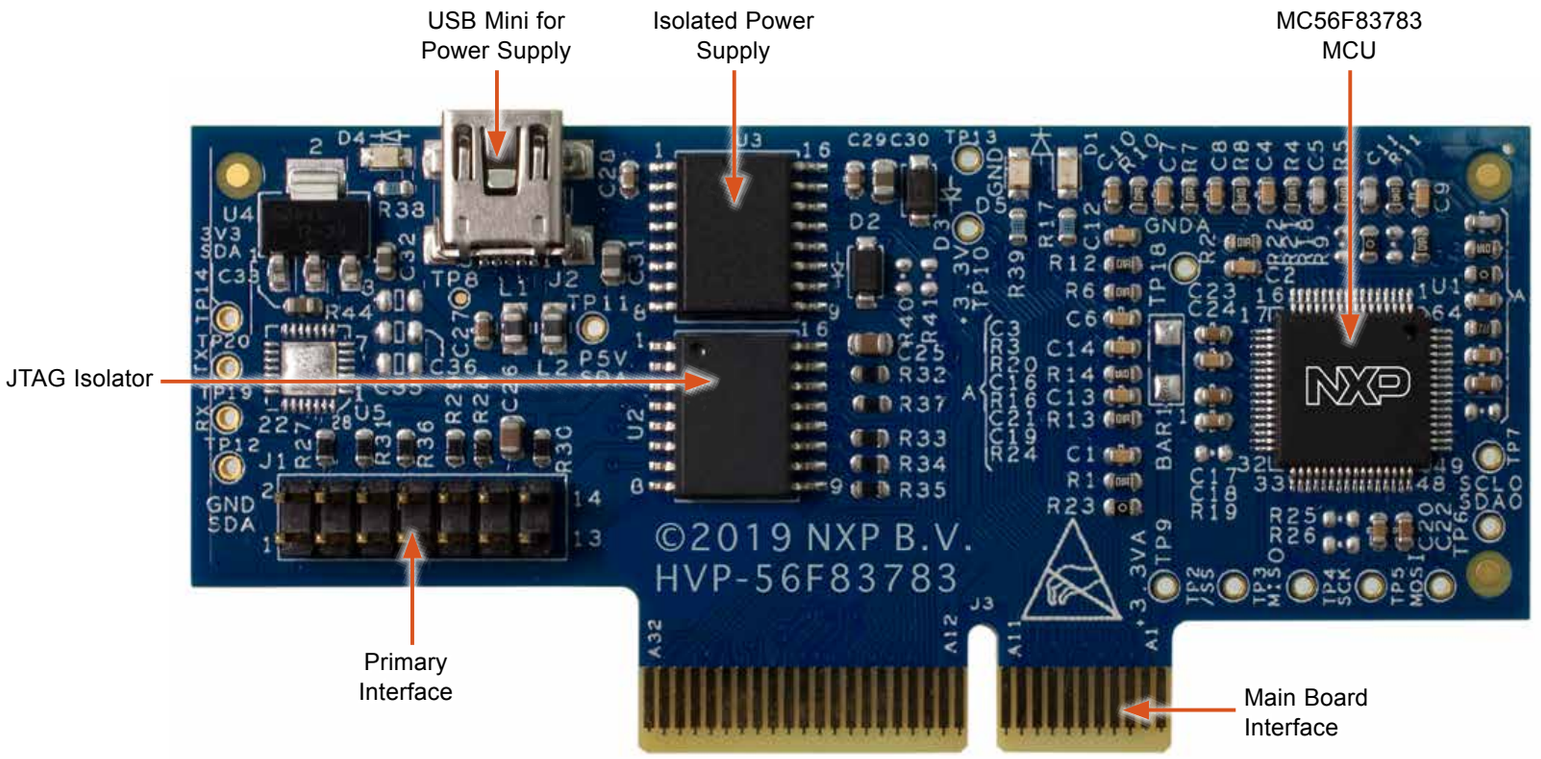

Figure 1: HVP-56F83783 Callouts

HVP-56F83783 HIGH-VOLTAGE DEVELOPMENT PLATFORM

The NXP High-Voltage-Development Platform is a set of software and hardware tools for evaluation and development of high-voltage motor control and power conversion algorithms. It is ideal for rapid prototyping of high-voltage microcontroller-based applications.

INTRODUCTION TO HVP-56F83783 HIGH-VOLTAGE DEVELOPMENT PLATFORM

The HVP-56F83783 controller card is a development platform for the DSC 56F8xxxx family which in combination with one of HVP-MC3PH High-Voltage Development Platform provides ready-made software and hardware development for high-voltage motor control and power conversion applications.

HVP-56F83783 Controller Card Features

- Accommodates target MC56F83783VLH MCU (32-bit DSP core with single-cycle math computation, 100 MHz, 256 KB Flash, 2x12-bit ADCs, high-resolution PWM, 64 LQFP) JTAG isolation up to 5KV
- Galvanic Isolation
- Design optimized for low noise
- On-board isolated power supply, allowing safe debugging
- Controller card allows stand-alone operation

Tools Required

- CodeWarrior Development Studio for MCU version 11.1 or later