

#### **General Description**

The IH5040 family consists of seven CMOS analog switches that are intended for general-purpose applications. These switches are latch-up proof, break-beforemake single, dual, and quad versions of the popular switch formats SPST, SPDT, DPST, and 4PST. Key features of the family include a low, 1nA leakage current and a quiescent current of less than 1µA.

Maxim's IH5040 family has faster switching times than the original manufacturer's devices. All devices are bidirectional and maintain almost constant on resistance throughout their operating range. These switches are guaranteed to operate from ±4.5V to ±18V, and will switch input signals that include the supplies.

#### **Applications**

PBX, PABX

Guidance and Control Systems

Test Equipment

Sample-and-Holds

Military Radios

#### ♦ Improved Second Source

- ♦ Guaranteed ±4.5V to ±18V Operation
- ♦ Input Voltage Range Includes Supplies
- **♦ Latchup-Proof Construction**
- ♦ TTL/CMOS Logic Compatible
- ♦ >1µA Quiescent Current
- ♦ Monolithic, Low-Power CMOS Design

#### **Ordering Information**

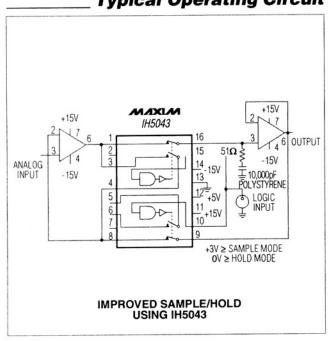
Features

PART TEMP. RANGE		PIN	-PACKAGE
INGLE THRO	W (SPST	)	
0°C to +	70°C	16	Plastic DIP
0°C to +	70°C	16	Wide SO
0°C to +	70°C	16	CERDIP
0°C to +	70°C	Dic	e*
-55℃ to +	125°C	16	CERDIP**
	0°C to + 0°C to + 0°C to +	0°C to +70°C 0°C to +70°C 0°C to +70°C 0°C to +70°C 0°C to +70°C -55°C to +125°C	0°C to +70°C 16 0°C to +70°C 16 0°C to +70°C Dic

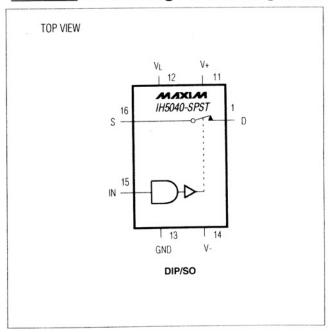
#### Ordering Information continued at end of data sheet.

- \* Contact factory for dice specifications.
- \*\* Contact factory for availability and processing to MIL-STD-883.

#### **Typical Operating Circuit**



#### Pin Configurations & Switching-State Diagrams



/U/IXI/U

Maxim Integrated Products 1

#### **ABSOLUTE MAXIMUM RATINGS**

V+ to V
V+ to V <sub>D</sub>
V <sub>D</sub> to V
V <sub>D</sub> to V <sub>S</sub>
V <sub>L</sub> to V
V <sub>L</sub> to V <sub>IN</sub>
V <sub>L</sub> to GND
VIN to GND
Digital Inputs (V+ + 0.3V) to (V+ - 44V)
Vs or V <sub>D</sub> (Note 1)0.3V to (V+ + 0.3V)
Current (any terminal)

Continuous Power Dissipation (1A = +70°C)
Plastic DIP (derate 10.53mW/°C above +70°C) 842mW
Wide SO (derate 9.52mW/°C above +70°C) 762mW
CERDIP (derate 10.00mW/°C above +70°C) 800mW
TO-100 (derate 6.67mW/°C above +70°C) 533mW
Operating Temperature Ranges:
IH504_C
IH504_M
Storage Temperature Range65°C to +150°C
Lead Temperature (soldering, 10sec) +300°C

Note 1: Signals on S, D, and digital inputs that exceed V- or V+ will be clamped by internal diodes. Limit forward diode current to 30mA maximum.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

#### **ELECTRICAL CHARACTERISTICS**

 $(V+ = 15V, V- = -15V, V_L = 5V, T_A = +25^{\circ}C, unless otherwise noted.)$ 

PARAMETER	SYMBOL	CONDITIONS			IH504_	М		H504_0		UNITS	
PARAMETER	STWIBOL	CONDI	TIONS	MIN TYP MAX		MIN	TYP	MAX	55		
	lawon		T <sub>A</sub> = +25°C	-1	1	1	-1		1		
Input Logic Current	lin(on)	V <sub>IN</sub> = 2.4V	TA = TMAX	-10		10	-10		10	μА	
input Logio Guiront	luvoss	V 0.9V	T <sub>A</sub> = +25°C	-1		1	-1		1	pr.	
	lin(OFF)	VIN = 0.8V	TA = TMAX	-10		10	10 -10	10			
Input Logic Low	VIL	TA = TMIN to TMAX				0.8			0.8	V	
Input Logic High	VIH	TA = TMIN to	Тмах	2.4			2.4			V	
D : 0	*DO(O)	Is = 10mA,	T <sub>A</sub> = +25°C			75			80	Ω	
Drain-Source On Resistance	rDS(ON)	VANALOG = -10V to 10V	TA = TMAX			150			130	52	
Channel-to-Channel rDS(ON) Match	ΔrDS(ON)				3			5		Ω	
Minimum Analog-Signal Handling Capability	VANALOG			-15		15	-15		15	V	
Switch-Off Leakage Current	ackage Current Inflaces	$V_{ANALOG} = T_{A} = +25$	Leglace Current $I_D/I_{DOSS}$ $V_{ANALOG} = T_A = +25^{\circ}C$ -1	VANALOG =	-1		1	-5		5	nA
Switch-Off Leakage Cufferit	ID/IS(OFF)	-10V to 10V	TA = TMAX	-100		100	-100		100	IIA	

#### **ELECTRICAL CHARACTERISTICS (continued)**

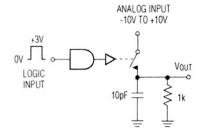
(V+ = 15V, V- = -15V,  $V_L$  = 5V,  $T_A$  = +25°C, unless otherwise noted.)

PARAMETER	SYMBOL	L CONDITIONS			IH504_	М	1	H504_0		UNITS
PARAMETER	SYMBOL	CONI	CONDITIONS		TYP	MAX	MIN	TYP	MAX	UNITS
Cuitab On Lankaga Current	Invovo	VD = VS =	T <sub>A</sub> = +25°C	-2		2	-10		10	nA
Switch-On Leakage Current	ID(ON)	-10V to 10V	TA = TMAX	-200		200	-100		100	) nA
Switch-On Time	ton	Figure 1				400			400	ns
Switch-Off Time	toff	Figure 1	Figure 1			200			200	ns
Charge Injection	Q(INJ)	Figure 2 (Note 2)			15			20		mV
Minimum Off-Isolation Rejection Ratio	OIRR	Figure 3, C <sub>L</sub>	< 5pF		54			50		dB
V+ Quiescent Current	I+Q	VIN = 0V	T <sub>A</sub> = +25°C			1			10	
	I+Q	and 5V	TA = TMAX			10		100	μА	
V- Quiescent Current		VIN = 0V	T <sub>A</sub> = +25°C	-1			-10			μА
v- Quiescent Current	I-Q	and 5V	TA = TMAX	-10			-100			μΑ
Vi Quiescent Current		VIN = 0V	T <sub>A</sub> = +25°C			1			10	
VL Quiescent Current	ILQ	and 5V	TA = TMAX			10			100	μΑ
Ground Quiescent Current	t IGND	VIN = 0V	T <sub>A</sub> = +25°C	-1			-10			
		and 5V	TA = TMAX	-10			-100			μА
Minimum Channel-to-Channel Cross-Coupling Rejection Ratio	CCRR	One channel off (Note 2)			54			50		dB
Power-Supply Range for Continuous Operation	VOP	(Notes 2, 3)		±4.5		±18	±4.5		±18	V

Note 2: Not production tested.

Note 3: Electrical characteristics, such as on resistance, will change when power supplies other than ±15V are used.

#### \_Test Circuits



ANALOG INPUT

OV LOGIC
INPUT

10,000pF

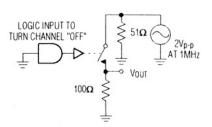
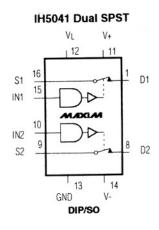


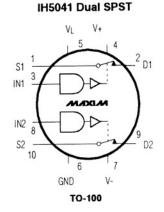
Figure 1. Switching Time

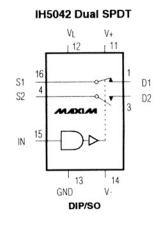
Figure 2. Charge Injection

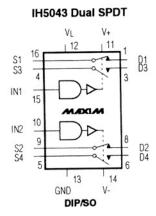
Figure 3. Off-Isolation Rejection Ratio

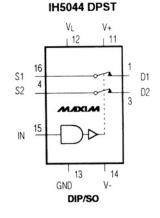
### Pin Configurations & Switching-State Diagrams (continued)

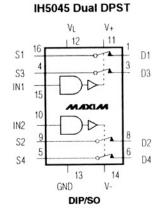












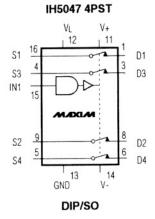
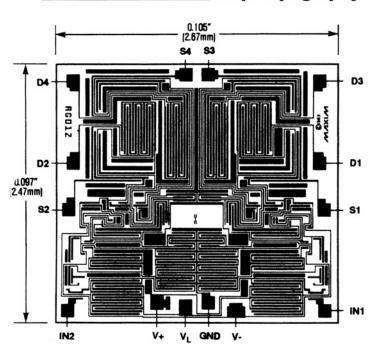


Table 1. Using the IH5040 Family with Only Two Supplies

SUPPLY VOLTAGES (V)	MINIMUM LOGIC I/P FOR "1" STATE (V)
±15	12.6
±12	9.6
±10	7.6
±5	2.6

#### **Chip Topography**



### Ordering Information (continued)

PART	TEMP. RANGE	PIN-PACKAGE
DUAL, SINGLE	POLE, SINGLE THRO	OW (DUAL SPST)
IH5041CPE	0°C to +70°C	16 Plastic DIP
IH5041CWE	0°C to +70°C	16 Wide SO
IH5041CJE	0°C to +70°C	16 CERDIP
IH5041CTW	0°C to +70°C	16 TO-100 <sup>†</sup>
IH5041C/D	0°C to +70°C	Dice*
IH5041MJE	-55°C to +125°C	16 CERDIP**
IH5041MTW	-55°C to +125°C	16 TO-100 <sup>†</sup>
SINGLE POLE,	DOUBLE THROW (SE	PDT)
IH5042CPE	0°C to +70°C	16 Plastic DIP
IH5042CWE	0°C to +70°C	16 Wide SO
IH5042CJE	0°C to +70°C	16 CERDIP
IH5042C/D	0°C to +70°C	Dice*
IH5042MJE	-55°C to +125°C	16 CERDIP**
DUAL, SINGLE	POLE, DOUBLE THR	OW (DUAL SPDT)
IH5043CPE	0°C to +70°C	16 Plastic DIP
IH5043CWE	0°C to +70°C	16 Wide SO
IH5043CJE	0°C to +70°C	16 CERDIP
IH5043C/D	0°C to +70°C	Dice*
IH5043MJE	-55°C to +125°C	16 CERDIP**
DOUBLE POLE,	, SINGLE THROW (DR	PST)
IH5044CPE	0°C to +70°C	16 Plastic DIP
IH5044CWE	0°C to +70°C	16 Wide SO
IH5044CJE	0°C to +70°C	16 CERDIP
IH5044C/D	0°C to +70°C	Dice*
IH5044MJE	-55°C to +125°C	16 CERDIP**
DUAL, DOUBLE	POLE, SINGLE THR	OW (DUAL DPST)
IH5045CPE	0°C to +70°C	16 Plastic DIP
IH5045CWE	0°C to +70°C	16 Wide SO
IH5045CJE	0°C to +70°C	16 CERDIP
IH5045C/D	0°C to +70°C	Dice*
IH5045MJE	-55°C to +125°C	16 CERDIP**
QUAD POLE, SI	NGLE THROW (4PST	)
IH5047CPE	0°C to +70°C	16 Plastic DIP
IH5047CWE	0°C to +70°C	16 Wide SO
IH5047CJE	0°C to +70°C	16 CERDIP
IH5047C/D	0°C to +70°C	Dice*
IH5047MJE	-55°C to +125°C	16 CERDIP**

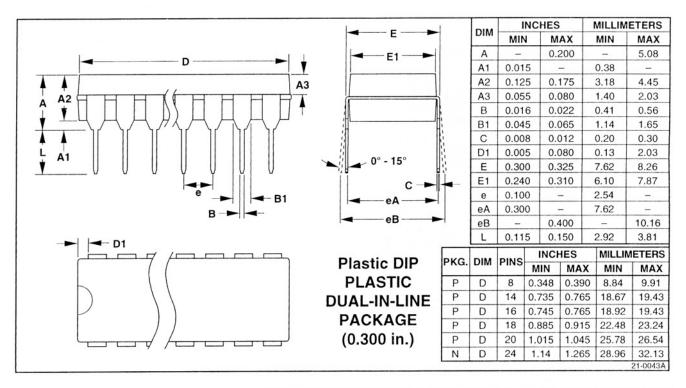
<sup>\*</sup> Contact factory for dice specifications.

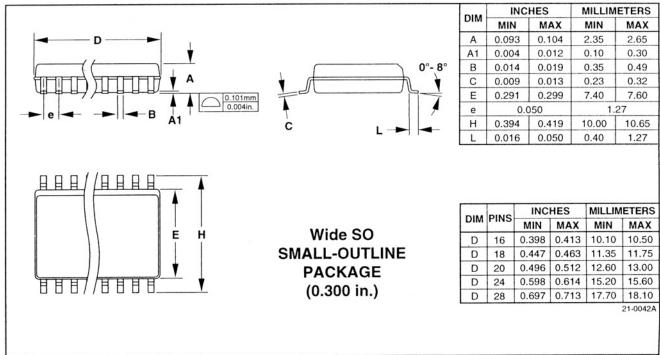
<sup>\*\*</sup> Contact factory for availability and processing to MIL-STD-883.

<sup>&</sup>lt;sup>†</sup> Contact factory for availability.

#### **Package Information**

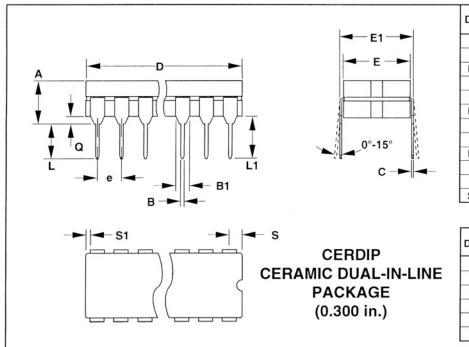
(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information .go to <a href="https://www.maxim-ic.com/packages">www.maxim-ic.com/packages</a>.)





#### Package Information (continued)

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to <a href="https://www.maxim-ic.com/packages">www.maxim-ic.com/packages</a>.)



ДІМ	INC	HES	MILLIM	ETERS
DIIVI	MIN	MAX	MIN	MAX
Α	-	0.200	-	5.08
В	0.014	0.023	0.36	0.58
B1	0.038	0.065	0.97	1.65
С	0.008	0.015	0.20	0.38
Е	0.220	0.310	5.59	7.87
E1	0.290	0.320	7.37	8.13
е	0.1	00	2.5	54
L	0.125	0.200	3.18	5.08
L1	0.150	-	3.81	-
Q	0.015	0.070	0.38	1.78
S	_	0.098	-	2.49
S1	0.005	-	0.13	_

DING	INCHES		MILLIMETERS			
PINS	MIN	MAX	MIN	MAX		
8	-	0.405	_	10.29		
14	-	0.785	-	19.94		
16	-	0.840	_	21.34		
18	-	0.960	-	24.38		
20	-	1.060	-	26.92		
24	_	1.280	_	32.51		
	14 16 18 20	8 - 14 - 16 - 18 - 20 -	MIN         MAX           8         -         0.405           14         -         0.785           16         -         0.840           18         -         0.960           20         -         1.060	MIN         MAX         MIN           8         -         0.405         -           14         -         0.785         -           16         -         0.840         -           18         -         0.960         -           20         -         1.060         -		