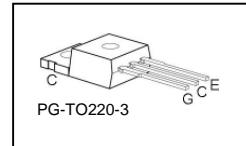
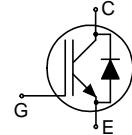


Low Loss DuoPack : IGBT in TRENCHSTOP™ and Fieldstop technology with soft, fast recovery anti-parallel Emitter Controlled HE diode


Features

- Very low $V_{CE(sat)}$ 1.5V (typ.)
- Maximum Junction Temperature 175°C
- Short circuit withstand time 5μs
- Designed for :
 - Variable Speed Drive for washing machines, air conditioners and induction cooking
 - Uninterrupted Power Supply
- TRENCHSTOP™ and Fieldstop technology for 600V applications offers :
 - very tight parameter distribution
 - high ruggedness, temperature stable behaviour
- Low EMI
- Very soft, fast recovery anti-parallel Emitter Controlled HE diode
- Qualified according to JEDEC¹ for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



Type	V_{CE}	$I_C; T_C = 100^\circ C$	$V_{CE(sat)}, T_j = 25^\circ C$	$T_{j,max}$	Marking	Package
IKP06N60T	600V	6A	1.5V	175°C	K06T60	PG-T0220-3

Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage, $T_j \geq 25^\circ C$	V_{CE}	600	V
DC collector current, limited by $T_{j,max}$ $T_C = 25^\circ C$ $T_C = 100^\circ C$	I_C	12 6	A
Pulsed collector current, t_p limited by $T_{j,max}$	I_{Cpuls}	18	
Turn off safe operating area, $V_{CE} = 600V$, $T_j = 175^\circ C$, $t_p = 1\mu s$	-	18	
Diode forward current, limited by $T_{j,max}$ $T_C = 25^\circ C$ $T_C = 100^\circ C$	I_F	12 6	
Diode pulsed current, t_p limited by $T_{j,max}$	I_{Fpuls}	18	
Gate-emitter voltage	V_{GE}	± 20	V
Short circuit withstand time ²⁾ $V_{GE} = 15V$, $V_{CC} \leq 400V$, $T_j \leq 150^\circ C$	t_{sc}	5	μs
Power dissipation $T_C = 25^\circ C$	P_{tot}	88	W
Operating junction temperature	T_j	-40...+175	
Storage temperature	T_{stg}	-55...+150	$^\circ C$
Soldering temperature wavesoldering, 1.6 mm (0.063 in.) from case for 10s		260	

¹ J-STD-020 and JESD-022

²⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Thermal Resistance

Parameter	Symbol	Conditions	Max. Value	Unit
Characteristic				
IGBT thermal resistance, junction – case	R_{thJC}		1.7	K/W
Diode thermal resistance, junction – case	R_{thJCD}		2.6	
Thermal resistance, junction – ambient	R_{thJA}		62	

Electrical Characteristic, at $T_j = 25^\circ\text{C}$, unless otherwise specified

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
Static Characteristic						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}$, $I_C=0.25\text{mA}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$V_{GE} = 15\text{V}$, $I_C=6\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	1.5	2.05	
Diode forward voltage	V_F	$V_{GE}=0\text{V}$, $I_F=6\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	1.6	2.05	
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	$I_C=0.18\text{mA}$, $V_{CE}=V_{GE}$	4.1	4.6	5.7	
Zero gate voltage collector current	I_{CES}	$V_{CE}=600\text{V}$, $V_{GE}=0\text{V}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	-	40 700	μA
Gate-emitter leakage current	I_{GES}	$V_{CE}=0\text{V}$, $V_{GE}=20\text{V}$	-	-	100	nA
Transconductance	g_{fs}	$V_{CE}=20\text{V}$, $I_C=6\text{A}$	-	3.6	-	S
Integrated gate resistor	R_{Gint}		none			Ω

Dynamic Characteristic

Input capacitance	C_{iss}	$V_{CE}=25\text{V}$,	-	368	-	pF
Output capacitance	C_{oss}	$V_{GE}=0\text{V}$,	-	28	-	
Reverse transfer capacitance	C_{rss}	$f=1\text{MHz}$	-	11	-	
Gate charge	Q_{Gate}	$V_{CC}=480\text{V}$, $I_C=6\text{A}$ $V_{GE}=15\text{V}$	-	42	-	nC
Internal emitter inductance measured 5mm (0.197 in.) from case	L_E		-	7	-	nH
Short circuit collector current ¹⁾	$I_{C(\text{SC})}$	$V_{GE}=15\text{V}$, $t_{SC}\leq 5\mu\text{s}$ $V_{CC} = 400\text{V}$, $T_j = 25^\circ\text{C}$	-	55	-	A

¹⁾ Allowed number of short circuits: <1000; time between short circuits: >1s.

Switching Characteristic, Inductive Load, at $T_j=25^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=25^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=6\text{A}$, $V_{GE}=0/15\text{V}$, $r_G=23\Omega$, $L_\sigma=60\text{nH}$, $C_\sigma=40\text{pF}$ L_σ , C_σ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	9	-	ns
Rise time	t_r		-	6	-	
Turn-off delay time	$t_{d(off)}$		-	130	-	
Fall time	t_f		-	58	-	
Turn-on energy	E_{on}		-	0.09	-	mJ
Turn-off energy	E_{off}		-	0.11	-	
Total switching energy	E_{ts}		-	0.2	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t_{rr}	$T_j=25^\circ\text{C}$, $V_R=400\text{V}$, $I_F=6\text{A}$, $di_F/dt=550\text{A}/\mu\text{s}$	-	123	-	ns
Diode reverse recovery charge	Q_{rr}		-	190	-	nC
Diode peak reverse recovery current	I_{rrm}		-	5.3	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	450	-	A/ μs

Switching Characteristic, Inductive Load, at $T_j=175^\circ\text{C}$

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
IGBT Characteristic						
Turn-on delay time	$t_{d(on)}$	$T_j=175^\circ\text{C}$, $V_{CC}=400\text{V}$, $I_C=6\text{A}$, $V_{GE}=0/15\text{V}$, $r_G=23\Omega$, $L_\sigma=60\text{nH}$, $C_\sigma=40\text{pF}$ L_σ , C_σ from Fig. E Energy losses include "tail" and diode reverse recovery.	-	9	-	ns
Rise time	t_r		-	8	-	
Turn-off delay time	$t_{d(off)}$		-	165	-	
Fall time	t_f		-	84	-	
Turn-on energy	E_{on}		-	0.14	-	mJ
Turn-off energy	E_{off}		-	0.18	-	
Total switching energy	E_{ts}		-	0.335	-	
Anti-Parallel Diode Characteristic						
Diode reverse recovery time	t_{rr}	$T_j=175^\circ\text{C}$, $V_R=400\text{V}$, $I_F=6\text{A}$, $di_F/dt=550\text{A}/\mu\text{s}$	-	180	-	ns
Diode reverse recovery charge	Q_{rr}		-	500	-	nC
Diode peak reverse recovery current	I_{rrm}		-	7.6	-	A
Diode peak rate of fall of reverse recovery current during t_b	di_{rr}/dt		-	285	-	A/ μs

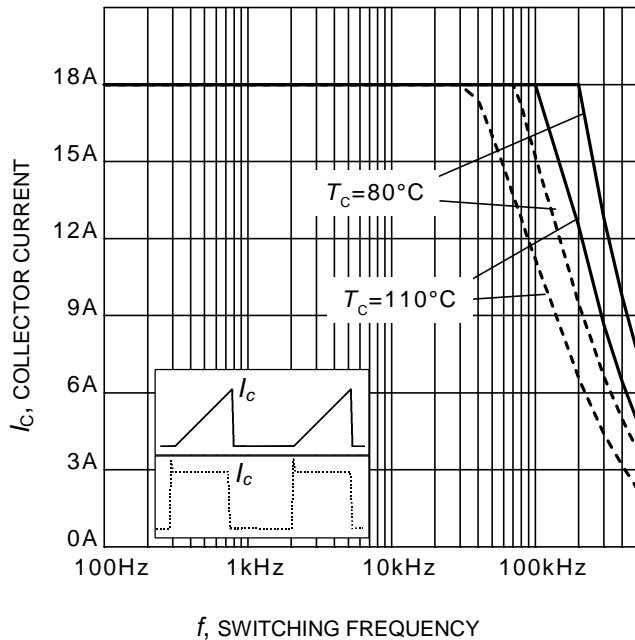

 f , SWITCHING FREQUENCY

Figure 1. Collector current as a function of switching frequency

($T_j \leq 175^\circ\text{C}$, $D = 0.5$, $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $r_G = 23\Omega$)

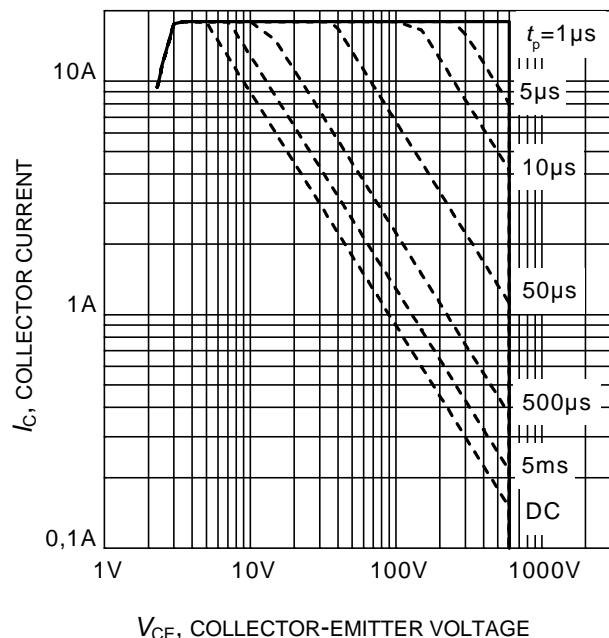

 V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 2. Safe operating area

($D = 0$, $T_c = 25^\circ\text{C}$, $T_j \leq 175^\circ\text{C}$; $V_{GE}=0/15\text{V}$)

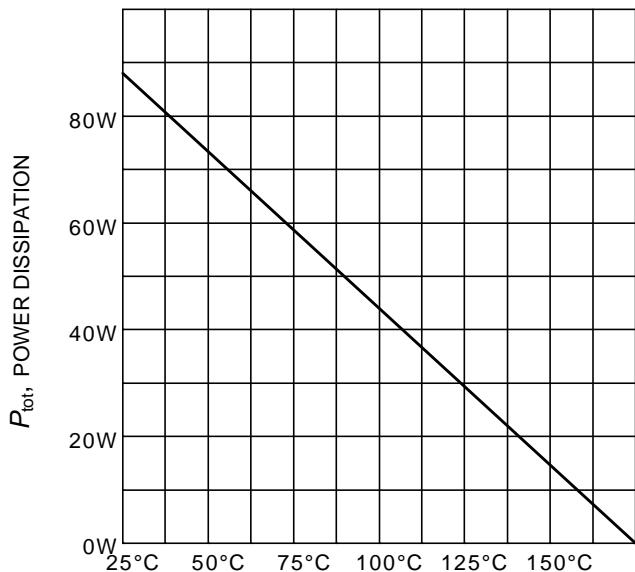

 T_c , CASE TEMPERATURE

Figure 3. Power dissipation as a function of case temperature

($T_j \leq 175^\circ\text{C}$)

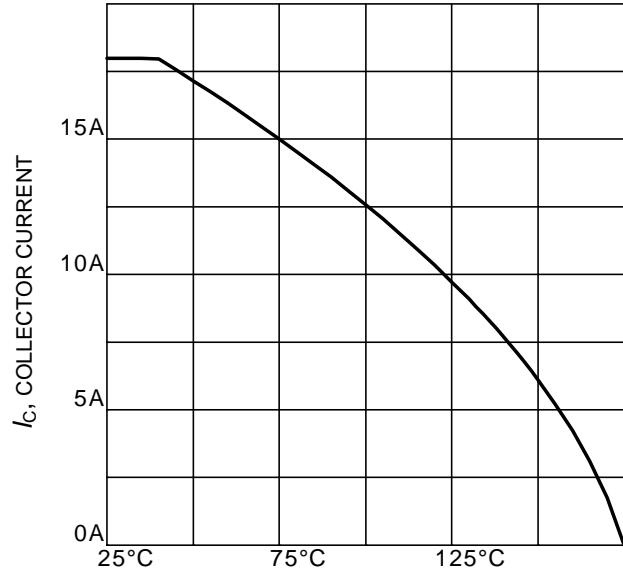

 T_c , CASE TEMPERATURE

Figure 4. Collector current as a function of case temperature

($V_{GE} \geq 15\text{V}$, $T_j \leq 175^\circ\text{C}$)

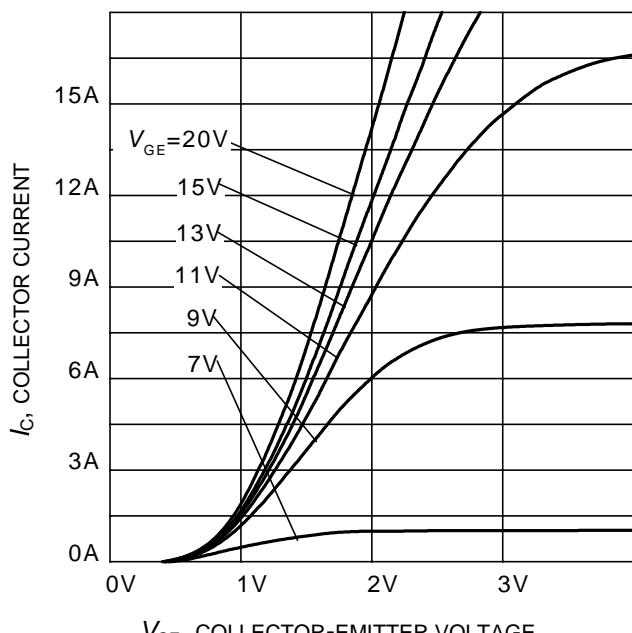

 V_{CE} , COLLECTOR-EMITTER VOLTAGE

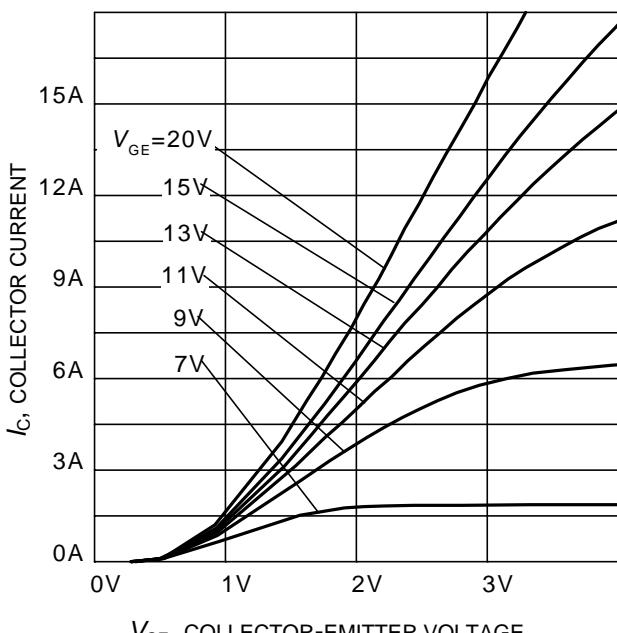
Figure 5. Typical output characteristic
 $(T_j = 25^\circ\text{C})$

 V_{CE} , COLLECTOR-EMITTER VOLTAGE

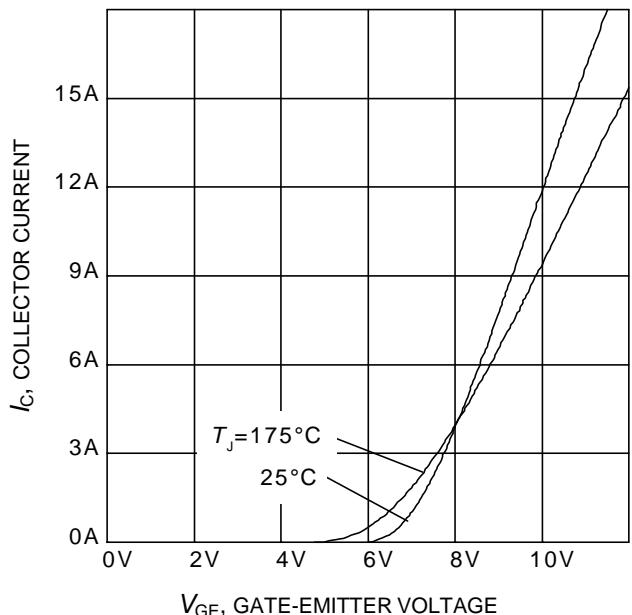
Figure 6. Typical output characteristic
 $(T_j = 175^\circ\text{C})$

 V_{GE} , GATE-EMITTER VOLTAGE

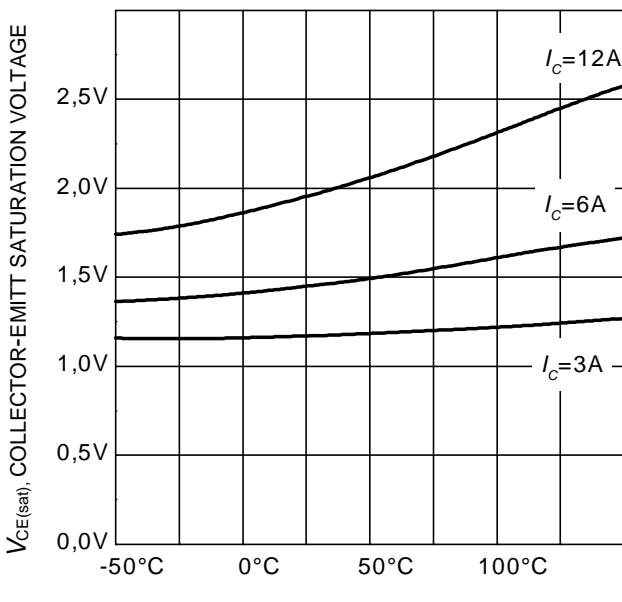
Figure 7. Typical transfer characteristic
 $(V_{CE}=20\text{V})$

 T_j , JUNCTION TEMPERATURE

Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature
 $(V_{GE} = 15\text{V})$

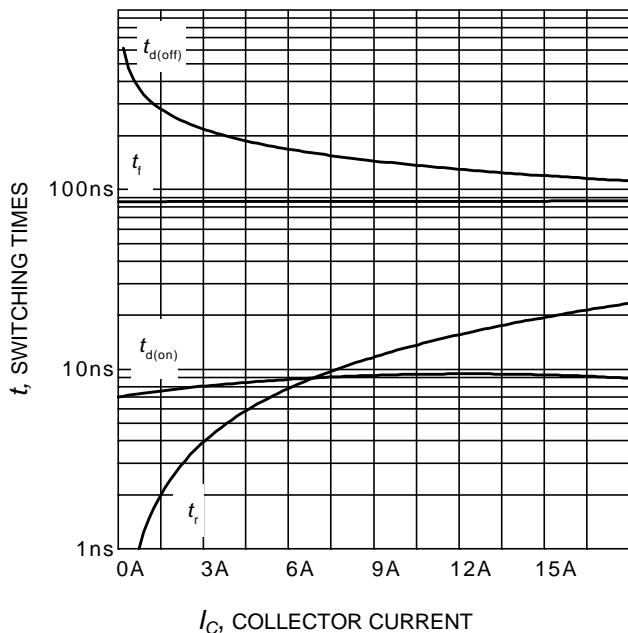


Figure 9. Typical switching times as a function of collector current
(inductive load, $T_J=175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $r_G = 23\Omega$,
Dynamic test circuit in Figure E)

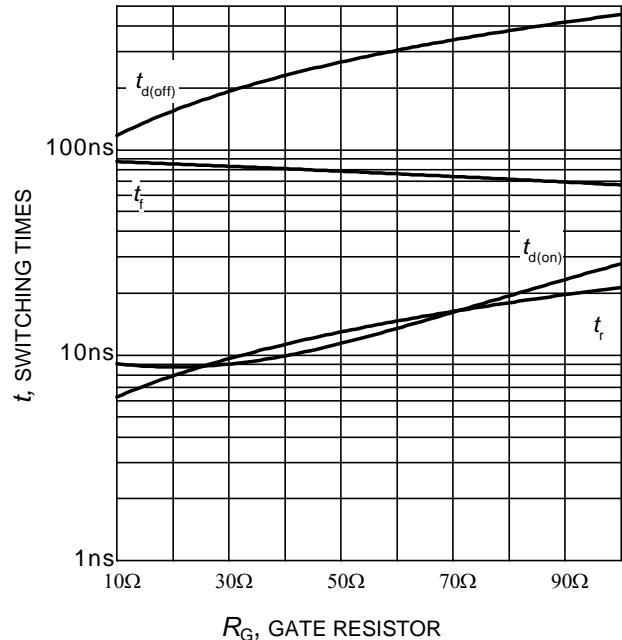


Figure 10. Typical switching times as a function of gate resistor
(inductive load, $T_J=175^\circ\text{C}$,
 $V_{CE} = 400\text{V}$, $V_{GE} = 0/15\text{V}$, $I_C = 6\text{A}$,
Dynamic test circuit in Figure E)

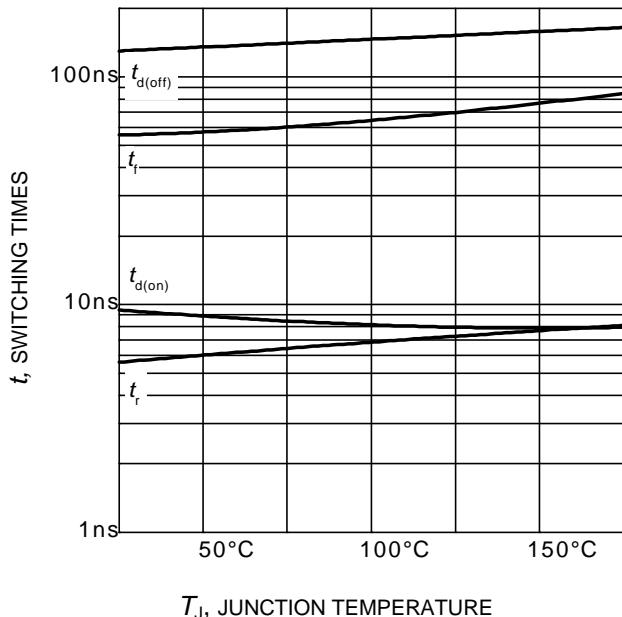


Figure 11. Typical switching times as a function of junction temperature
(inductive load, $V_{CE} = 400\text{V}$,
 $V_{GE} = 0/15\text{V}$, $I_C = 6\text{A}$, $r_G = 23\Omega$,
Dynamic test circuit in Figure E)

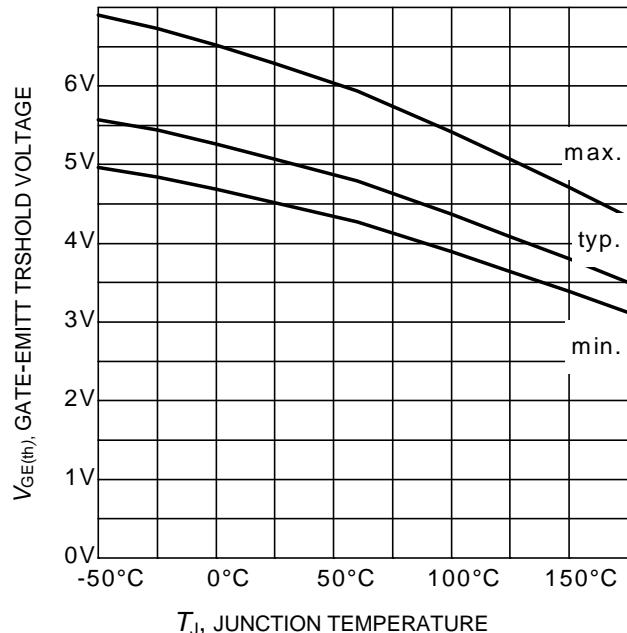


Figure 12. Gate-emitter threshold voltage as a function of junction temperature
($I_C = 0.18\text{mA}$)

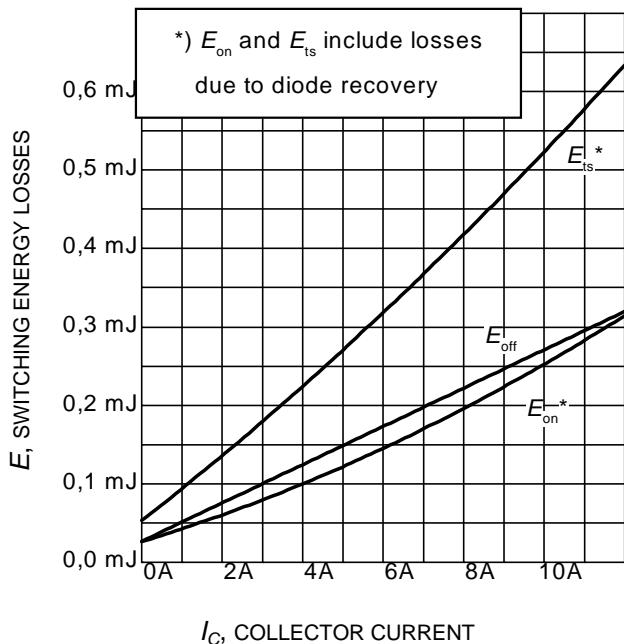
TRENCHSTOP™ Series


Figure 13. Typical switching energy losses as a function of collector current
(inductive load, $T_J=175^\circ\text{C}$,
 $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $r_G=23\Omega$,
Dynamic test circuit in Figure E)

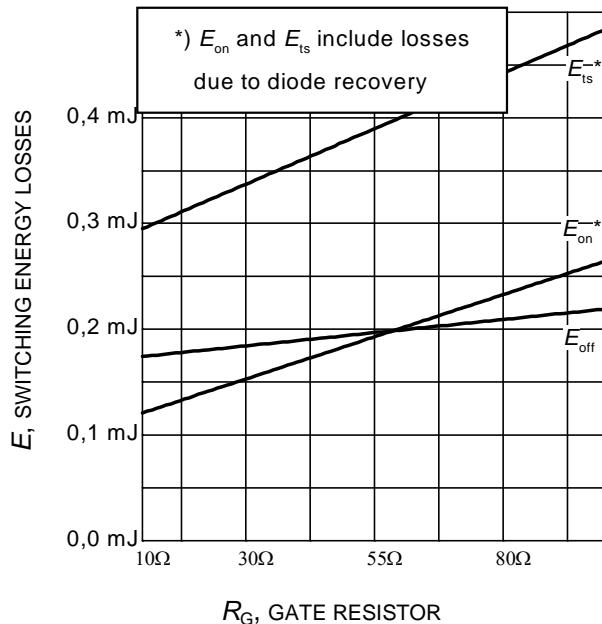


Figure 14. Typical switching energy losses as a function of gate resistor
(inductive load, $T_J=175^\circ\text{C}$,
 $V_{CE}=400\text{V}$, $V_{GE}=0/15\text{V}$, $I_C=6\text{A}$,
Dynamic test circuit in Figure E)

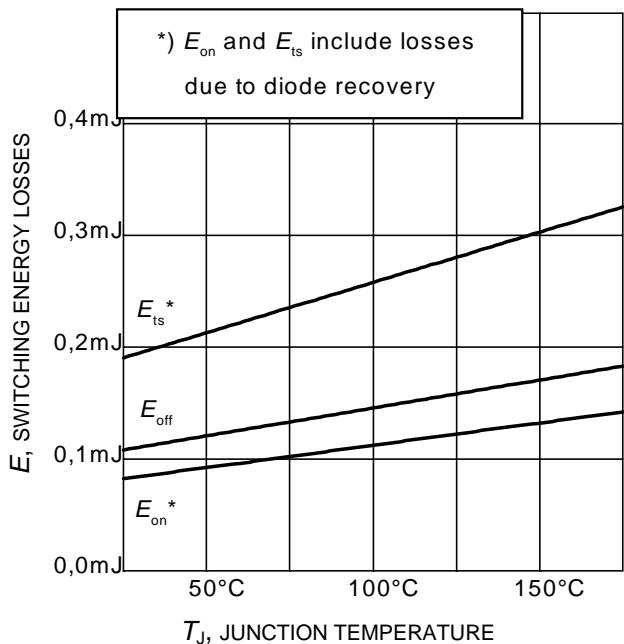


Figure 15. Typical switching energy losses as a function of junction temperature
(inductive load, $V_{CE}=400\text{V}$,
 $V_{GE}=0/15\text{V}$, $I_C=6\text{A}$, $r_G=23\Omega$,
Dynamic test circuit in Figure E)

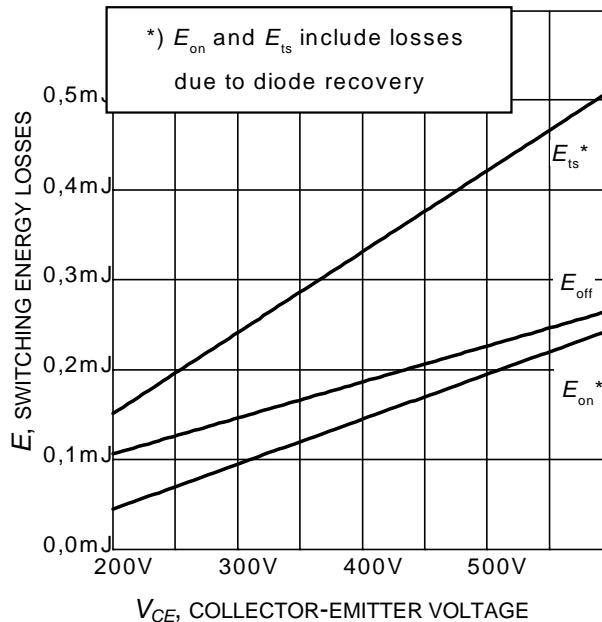
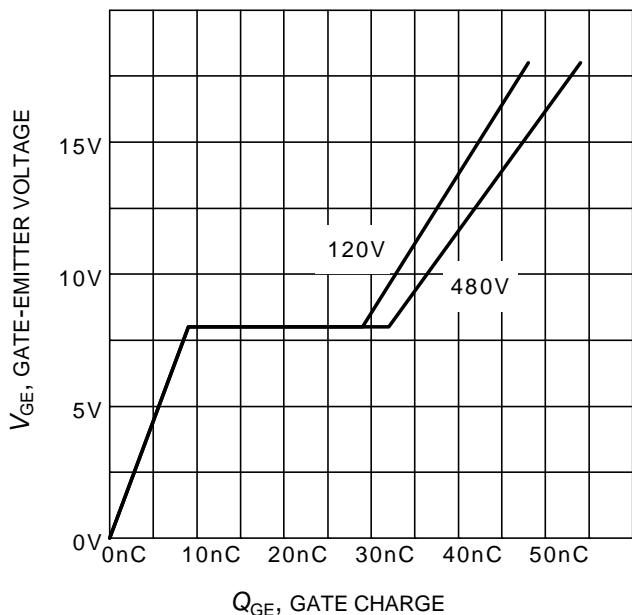
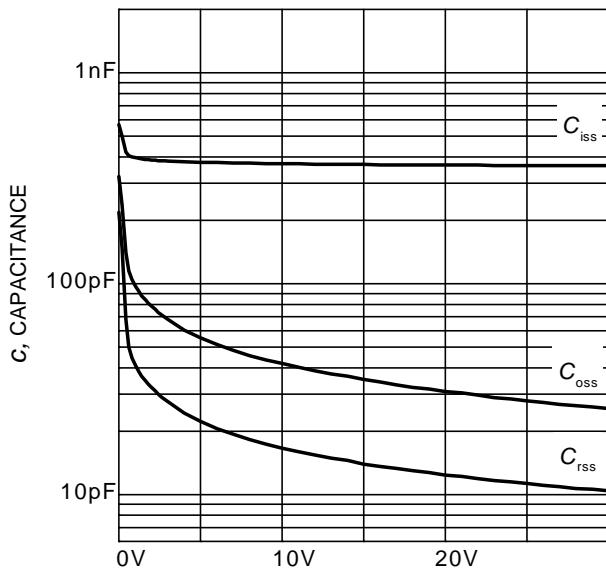


Figure 16. Typical switching energy losses as a function of collector-emitter voltage
(inductive load, $T_J=175^\circ\text{C}$,
 $V_{GE}=0/15\text{V}$, $I_C=6\text{A}$, $r_G=23\Omega$,
Dynamic test circuit in Figure E)



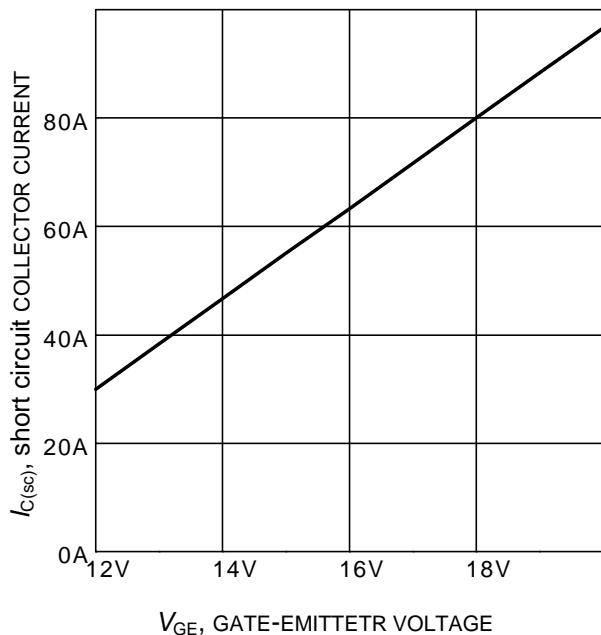
Q_{GE} , GATE CHARGE

Figure 17. Typical gate charge
($I_C = 6$ A)



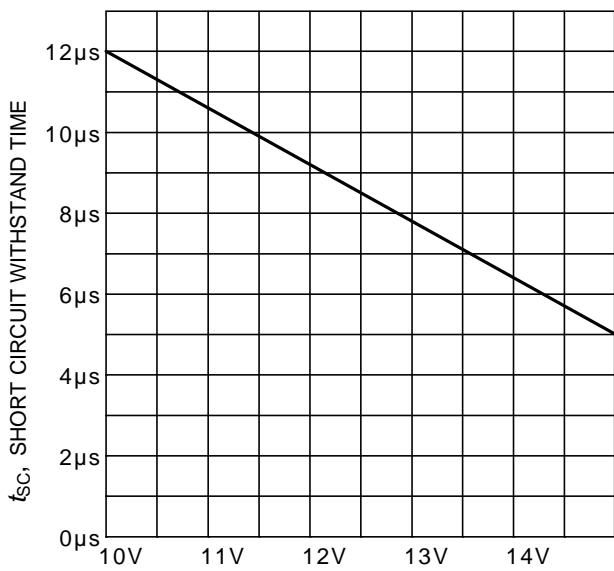
V_{CE} , COLLECTOR-EMITTER VOLTAGE

Figure 18. Typical capacitance as a function of collector-emitter voltage
($V_{GE}=0V$, $f = 1$ MHz)



V_{GE} , GATE-EMITTER VOLTAGE

Figure 19. Typical short circuit collector current as a function of gate-emitter voltage
($V_{CE} \leq 400$ V, $T_j \leq 150^\circ\text{C}$)



V_{GE} , GATE-EMITTER VOLTAGE

Figure 20. Short circuit withstand time as a function of gate-emitter voltage
($V_{CE}=400$ V, start at $T_j=25^\circ\text{C}$, $T_{jmax}<150^\circ\text{C}$)

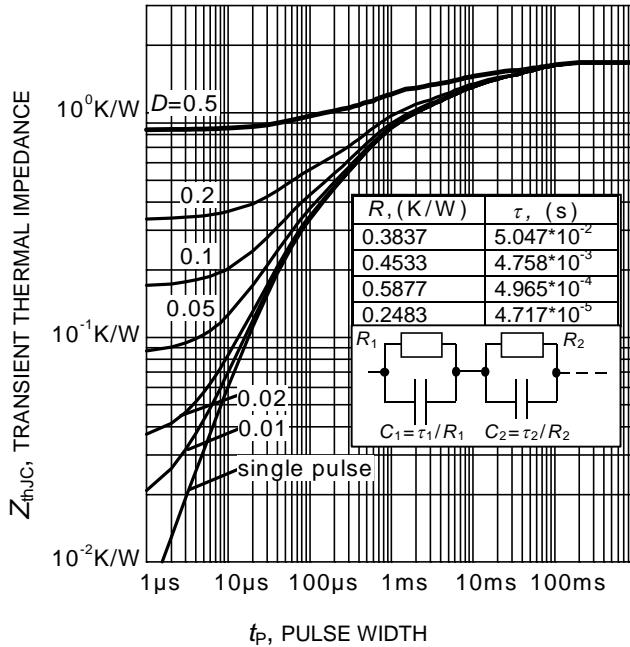


Figure 21. IGBT transient thermal impedance
($D = t_p / T$)

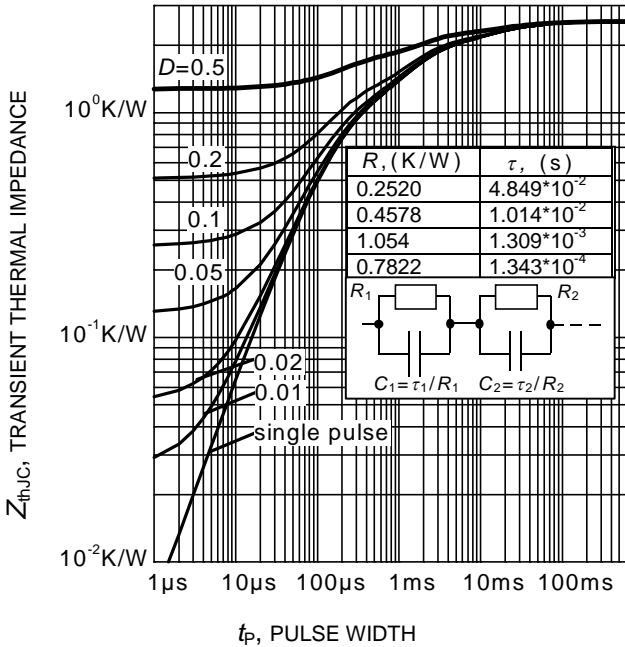


Figure 22. Diode transient thermal impedance as a function of pulse width
($D=t_p/T$)

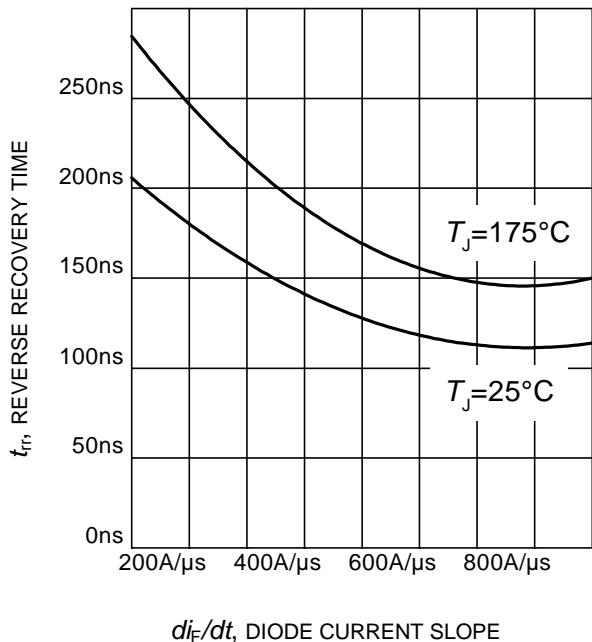


Figure 23. Typical reverse recovery time as a function of diode current slope
($V_R = 400\text{V}$, $I_F = 6\text{A}$,
Dynamic test circuit in Figure E)

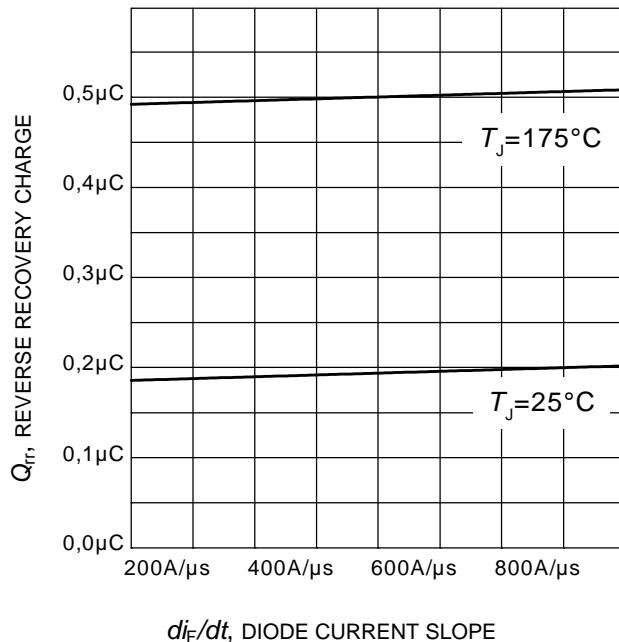
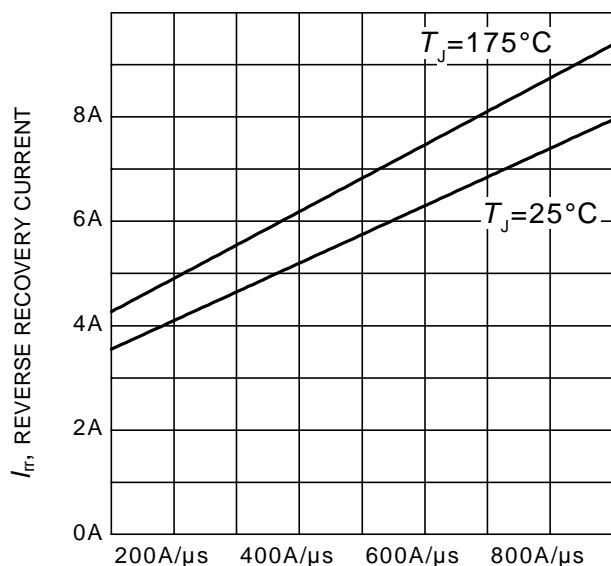


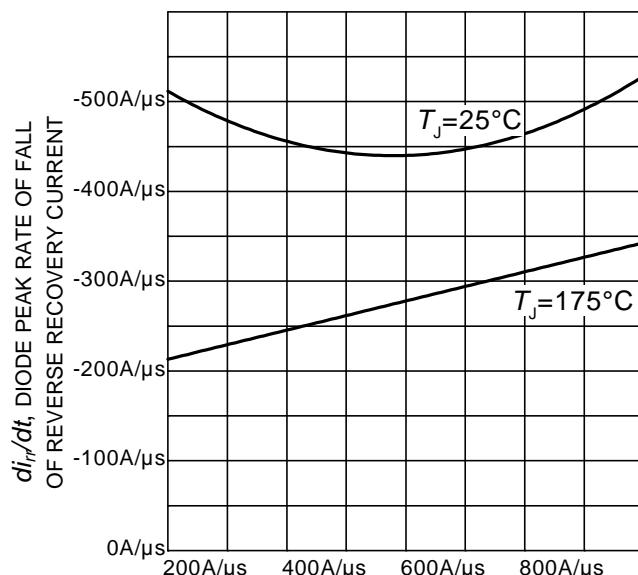
Figure 24. Typical reverse recovery charge as a function of diode current slope
($V_R=400\text{V}$, $I_F=6\text{ A}$,
Dynamic test circuit in Figure E)



dI_r/dt , DIODE CURRENT SLOPE

Figure 25. Typical reverse recovery current as a function of diode current slope

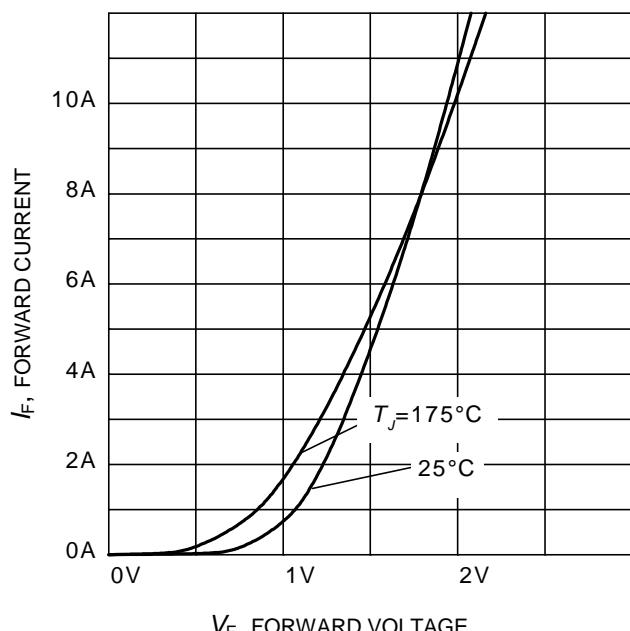
($V_R = 400\text{V}$, $I_F = 6\text{A}$,
Dynamic test circuit in Figure E)



dI_r/dt , DIODE CURRENT SLOPE

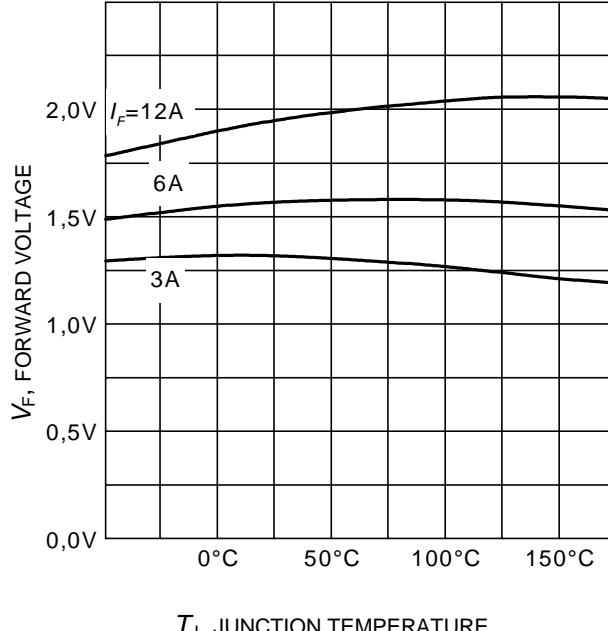
Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope

($V_R = 400\text{V}$, $I_F = 6\text{A}$,
Dynamic test circuit in Figure E)



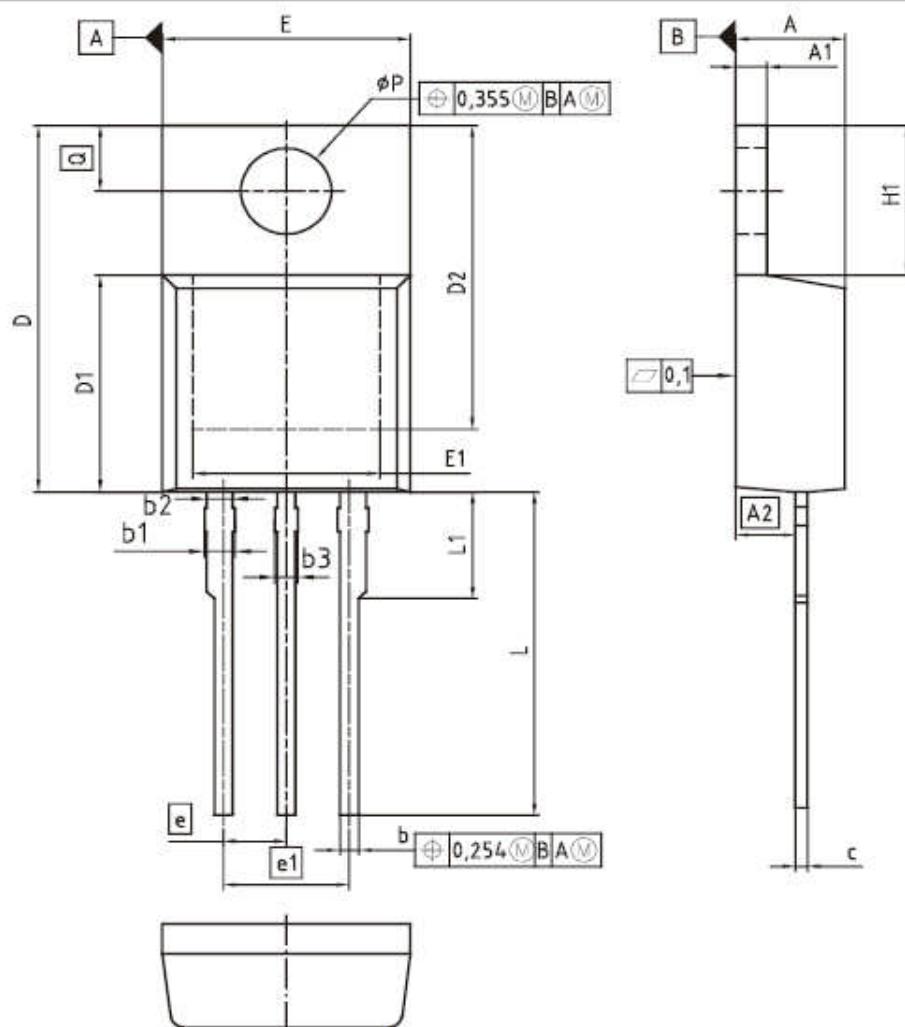
V_F , FORWARD VOLTAGE

Figure 27. Typical diode forward current as a function of forward voltage



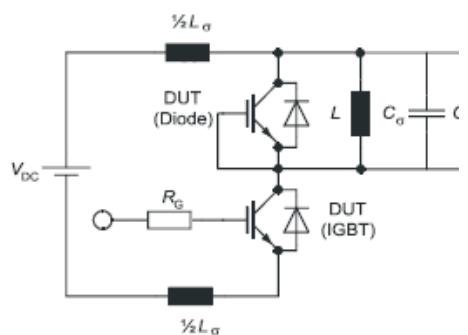
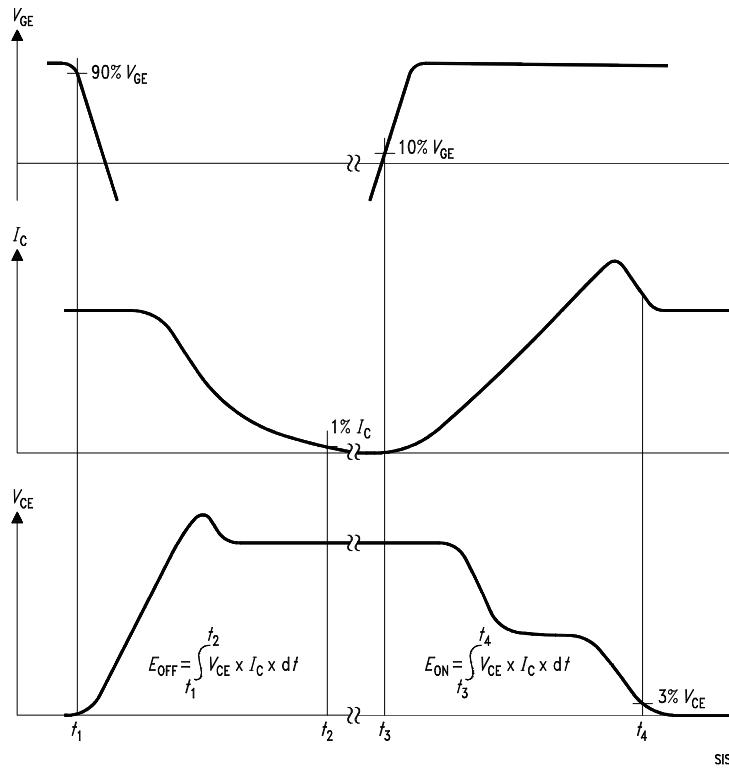
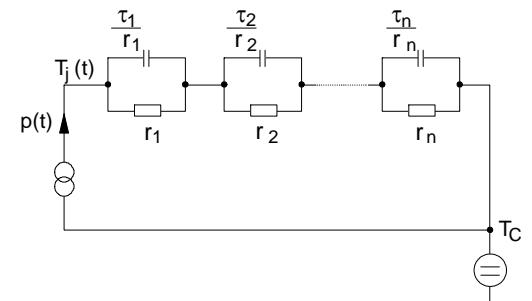
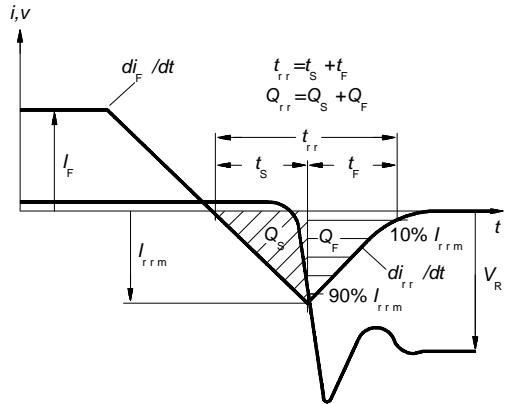
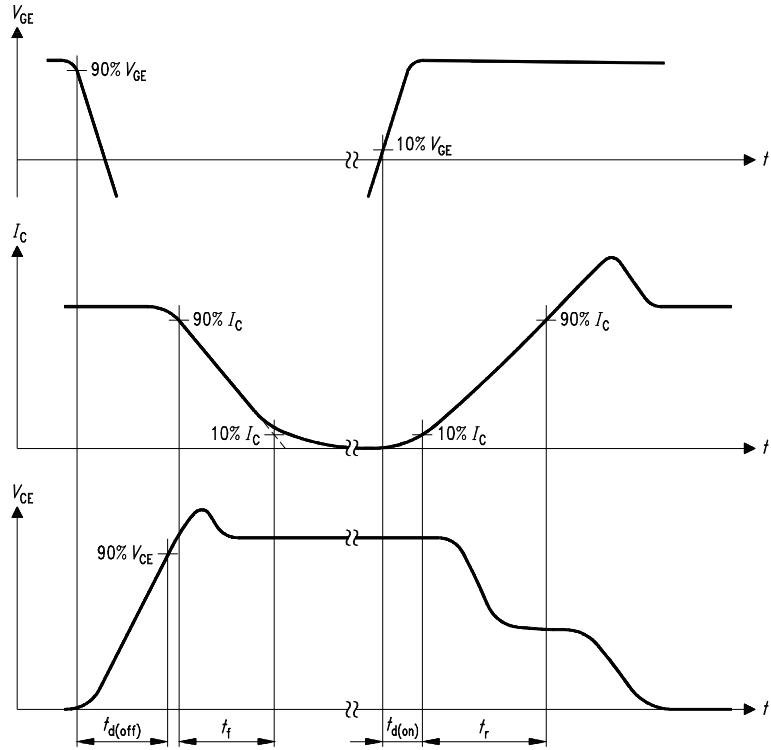
T_J , JUNCTION TEMPERATURE

Figure 28. Typical diode forward voltage as a function of junction temperature

PG-T0220-3


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.30	4.57	0.169	0.180
A1	1.17	1.40	0.046	0.055
A2	2.15	2.72	0.085	0.107
b	0.65	0.86	0.026	0.034
b1	0.95	1.40	0.037	0.055
b2	0.95	1.15	0.037	0.045
b3	0.65	1.15	0.026	0.045
c	0.33	0.60	0.013	0.024
D	14.81	15.95	0.583	0.628
D1	8.51	9.45	0.335	0.372
D2	12.19	13.10	0.480	0.516
E	9.70	10.36	0.382	0.408
E1	6.50	8.60	0.256	0.339
e	2.54		0.100	
e1	5.08		0.200	
N	3		3	
H1	5.90	6.90	0.232	0.272
L	13.00	14.00	0.512	0.551
L1	-	4.80	-	0.189
φP	3.80	3.89	0.142	0.153
Q	2.60	3.00	0.102	0.118

DOCUMENT NO.	ZBB00003318
SCALE	0 2.5 0 2.5 5mm
EUROPEAN PROJECTION	
ISSUE DATE	30-07-2009
REVISION	06



Parasitic inductance L_α ,
Parasitic capacitor C_α ,
Relief capacitor C_r
(only for ZVT switching)