Talaria TWO^{TM} EVB-A Evaluation Boards
(INP3010 / INP3011 / INP3012 / INP3013 / INP3014 / INP3015)

Extreme Low Power Wireless Platform

IEEE 802.11 b/g/n, BLE5

User Guide for Talaria TWO™ Evaluation & Development Kit

Version 2.1

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BLE	Bluetooth Low Energy
DMM	Digital Multimeter
DPDT	Double Pole Double Throw
FTDI	Future Technology Devices International
GPIO	General Purpose Input/Output
IO	Input Output
JTAG	Joint Test Action Group
LED	Light Emitting Diode
MPSEE	Multi-Protocol Synchronous Serial Engine
SCL	Serial Clock
SDA	Serial Data
SPI	Serial Peripheral Interface
SPDT	Single Pole Double Throw
UART	Universal Asynchronous Receiver-Transmitter
USB	Universal Serial Bus

4   Introduction

INP301x Talaria TWO evaluation board is designed as an evaluation platform for the INP101x modules. This user guide provides an overview of the evaluation board explaining its key features and functions.

5   INP301x Package Contents

The package contains:

1.   INP3010, INP3011, INP3012, INP3013 INP3014 or INP3015 board
2.   Micro USB cable
3.   Antenna (INP3011, INP3012 and INP3015 boards)
4.   Battery box

INP3010
(Includes INP1010 w/ PCB Antenna)
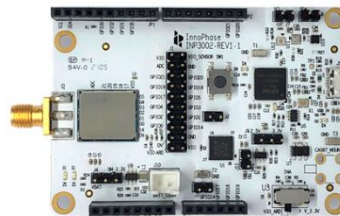
INP3012
(Includes INP1012 w/ RF Pad)

INP3014
(Includes INP1014 w/ PCB Antenna)

INP3011
(Includes INP1011 w/ U.FL Connector)

INP3013
(Includes INP1013 w/ Chip Antenna)

INP3015
(Includes INP1015 w/ U.FL Connector)

Figure 1: INP301x EVB-A Board with INP101x module board installed

6 Description of the board

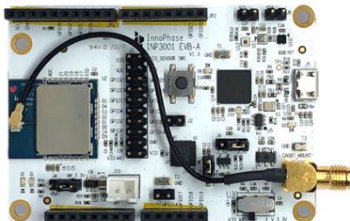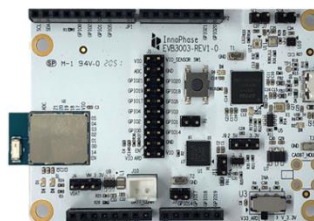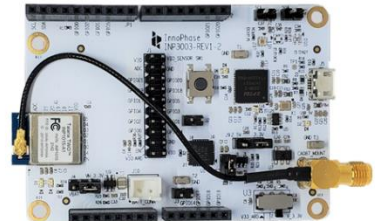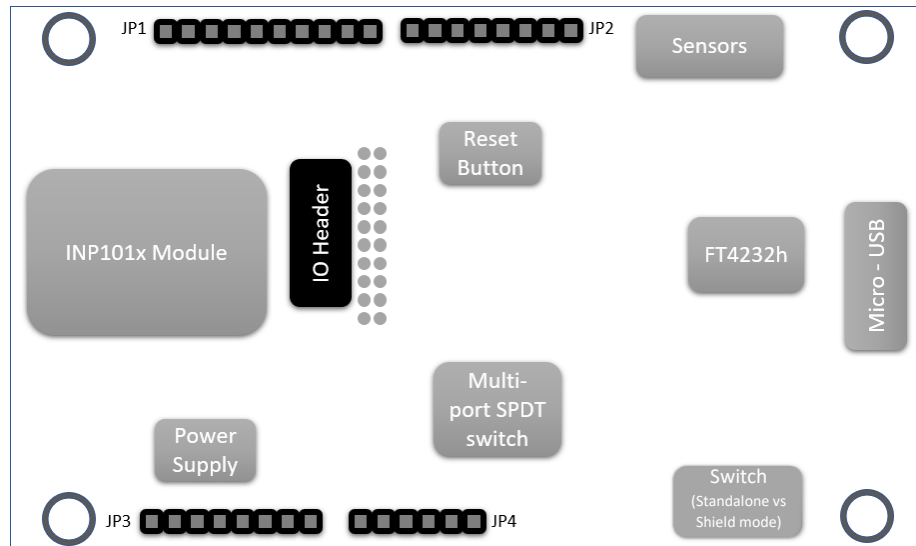## 6.1 Block Diagram

Figure 2: Block diagram of INP301x Evaluation Board

The block diagram of the INP301x Evaluation board is shown in Figure 2. The main component on the board is the INP101x module. Key features of the evaluation board are:

1. Standalone mode vs. Shield mode: Using the mode switch U3, either standalone mode or shield mode can be selected.

   a. In standalone mode, the INP101x can be accessed via micro-USB cable for programming and debugging. This mode is recommended for standalone application development.

   b. In shield mode, the INP101x module can be interfaced with any host CPU and can provide serial to Wi-Fi capabilities.

2. A peripheral IO header (J1) is available using which all the IO's of the INP101x module can be accessed.

3. Power supply section: Based on the mode, the power for module is derived from either USB or shield header. A battery header is available which can be used as power source as well.

   a. J4 at VM_3.3V for USB power

   b. J4 at VBAT and battery connect to J10 for Battery power

4. On board sensors are available to develop sensor to cloud applications.

5. Switch:

   a. Set U3 switch to V33_ARD for Shield mode

   b. Set U3 switch to V_3.3V for Standalone mode

6.2   Jumpers on the board
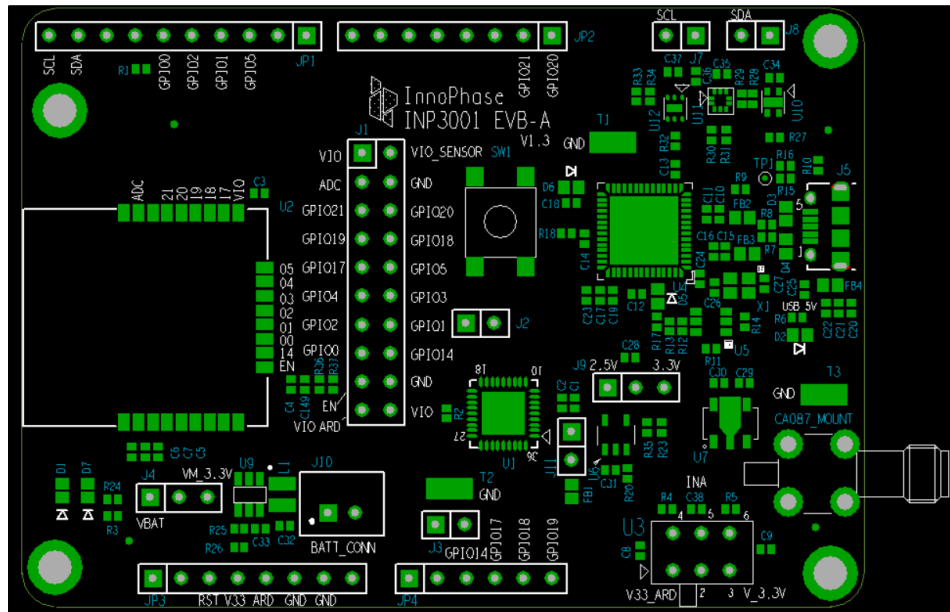
Figure 3: INP301x EVB-A control and connectivity points

Jumper	Mode and Operation
J1	IO header
J2	Used to enable Console logging in shield mode
J3	Connect LED D1 to GPIO14, for debug purposes
J4	Select power from USB or battery connector, also used for current measurements
J7	Connect SCL to GPIO4
J8	Connect SDA to GPIO3
J9	Select IO voltage for FTDI IOs
J10	Battery terminal
J11	Enable the multi-port SPDT switch
JP1 to JP4	Arduino UNO shield compatible header (3.3V support only)
U3	Switch between Stand-alone mode and Arduino Shield Mode

Table 1: Jumper Information

6.3 Power Supply and Mode Switch

The INP301x board is designed to supply power to the INP101x module in following ways:

1. In standalone mode, power is drawn from USB connector

2. In shield mode, power is drawn from shield connector

3. A battery header is also available to provide power to the module
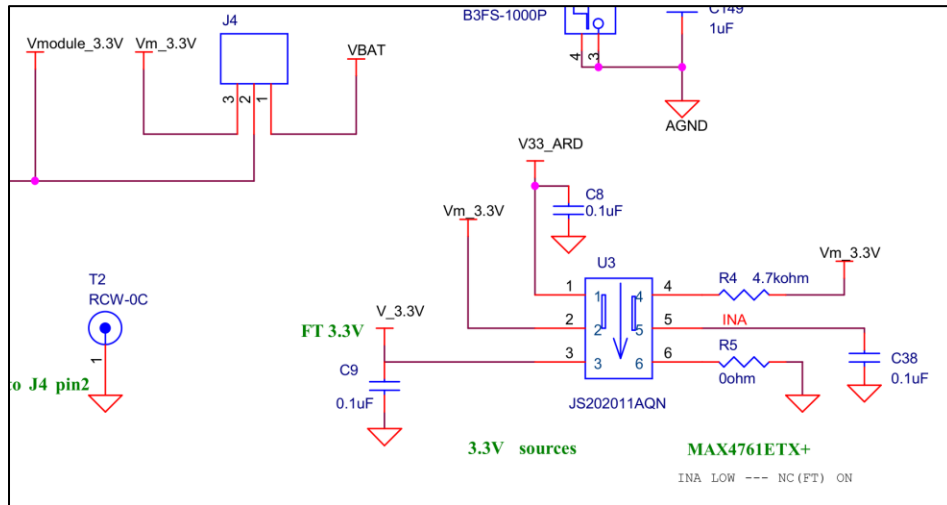

Figure 4: Power supply section

The power supply section is shown in the Figure 4. The INP101x module requires 3.3V supply. The DPDT switch (U3) selects between USB and Arduino header supply using the common net Vm_3.3V. The jumper J4 is used to select between battery supply and Vm_3.3V. The same jumper can be used for measuring current consumption of the module.

6.4 IO Header (J1)

The J1 header brings out all the IOs from INP101x module. These IOs can be used for debug, and/or any external interfacing needs. The pinout of this header is shown in Figure 5. To work with on board sensor, pins 1 & 2 needs to be shorted.
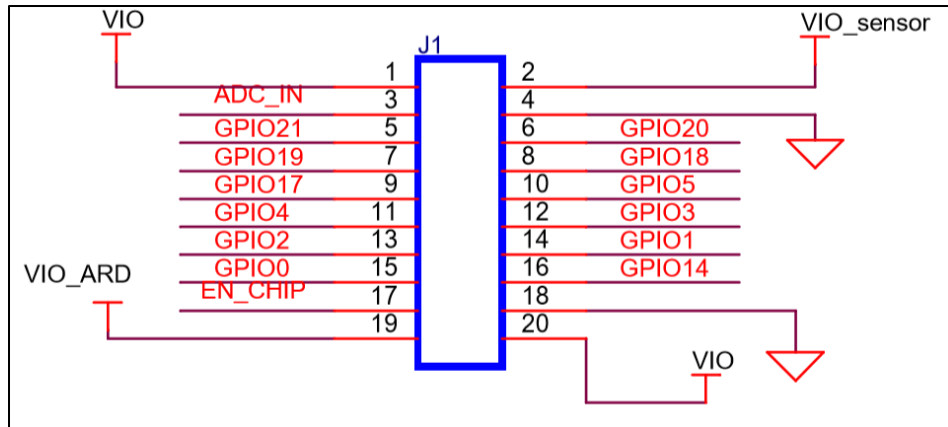
Figure 5: IO Header

6.5 Shield Headers (JP1 to JP4)

Arduino UNO compatible headers are available in the INP301x board to interface with any compatible host micro-controller.

The GPIOs assigned to shield headers are carefully chosen to achieve following capability:

1. INP101x's SPI slave pins available on JP1

2. INP101x's I2C master pins available on JP1

3. Remaining GPIOs are available on JP2 and JP4

Note that INP301x supports 2.5V IO as the default configuration. The shield header connections are as shown in JP2 of Figure 6.
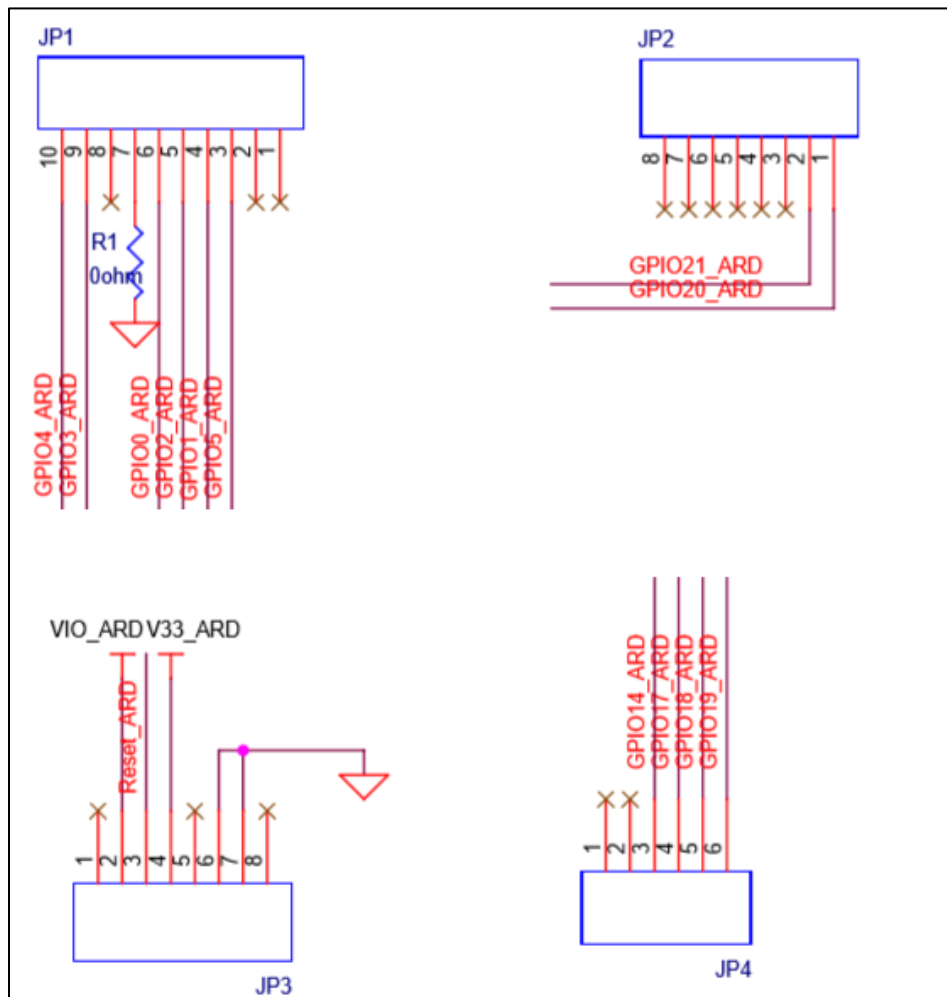
Figure 6: Arduino UNO shield compatible jumpers
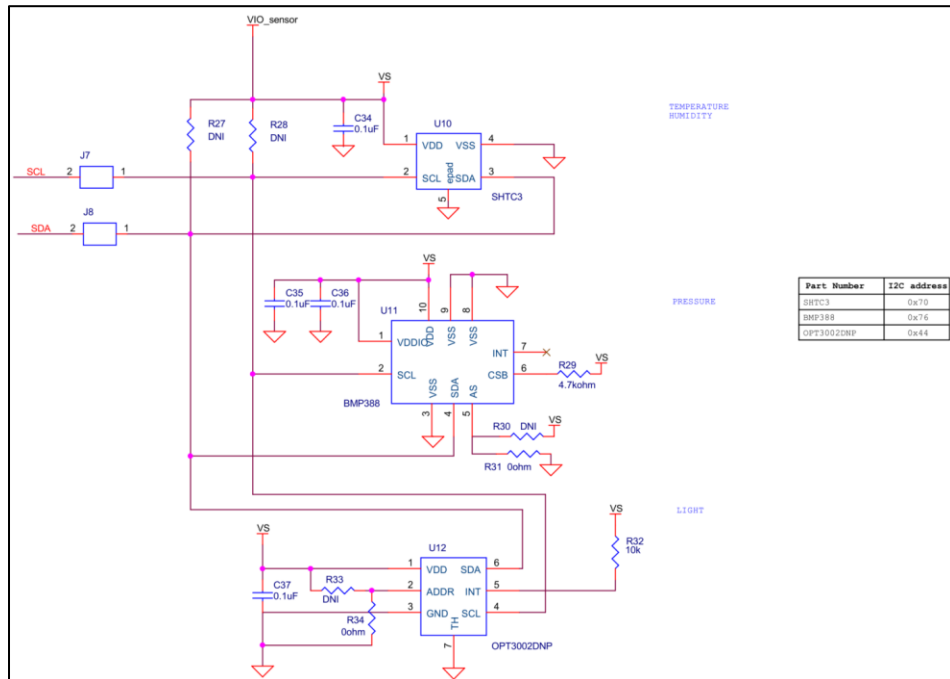
6.6 On Board Sensors

Figure 7: On board sensors

The INP301x board has following sensors available on board for quick prototyping/testing:

1. Temperature/Humidity (Sensirion SHTC3)

2. Pressure (Bosch BMP388)

3. Light (TI OPT3002)

To use the sensors, J7, J8, pins 1 & 2 of J1 should be connected. This enables power connection to the sensors on board, I2C connection on GPIOs 3 & 4.

7   Functional Description

Following are the functional modes that can be achieved in the INP301x board:

1. Stand-alone mode (host-less)
2. Shield mode (hosted)

More details about each mode are available in subsequent sections. Switching between the modes is handled by DPDT switch U3 for power, and multi-port SPDT switch U1 for the GPIOs.

7.1   Stand-alone mode

The stand-alone mode is intended for following use cases:

1. Host-less application development on INP101x modules

2. Programing access to INP101x modules

In stand-alone mode, the U3 switch is pushed towards pin 3, which disconnects power and IO from shield headers and connects them to FTDI. The FTDI port layout is shown in Table 2.

FTDI Bus	Interface to Talaria TWO™
A	JTAG
B	RESET
C	UART
D	CONSOLE (UART)

Table 2: FTDI Layout

The A & B bus of FTDI device supports MPSEE protocol, hence JTAG is assigned to A-bus. The BDBUS7 is connected EN_CHIP of the INP101x module. The C & D bus of FTDI device used as UARTs, with C-bus connected to peripheral UART of INP101x module and D-bus connected is CONSOLE port (GPIO17) of INP101x module.

The JTAG on A-bus is used for debugging applications on the INP101x module. The UART on C-bus is used for programming the INP101x module. The CONSOLE port is a unidirectional UART from INP101x module that operates at high baud rate of 2457600, used for debug prints.

7.1.1 Driver Installation for Windows OS

On Windows OS, `libusbK` driver needs to be installed to communicate and control the Talaria TWO^{TM} module via the FTDI device on the evaluation board. The tools/applications provided by InnoPhase will use this driver. Install/uninstall instructions for this driver is given subsequent sections.

7.1.1.1 Installation instructions for libusbK driver

Download the free software Zadig, available here: - https://zadig.akeo.ie/. Connect your Windows PC or Laptop to the evaluation board using the provided USB cable. Now, open Zadig and click on `Options`. Select `List All Devices` and deselect `Ignore Hubs or Composite Parents` as shown in Figure 8.
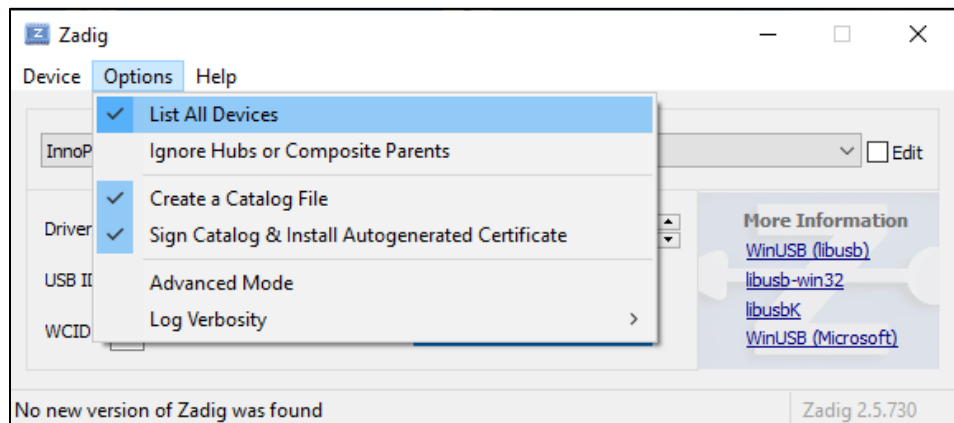

Figure 8: Listing devices in Zadig

To establish communication with Talaria TWO^{TM} module via the FTDI device on the InnoPhase Evaluation Board, the Talaria TWO^{TM} USB driver must be `libusbK`. In case the current driver is not `libusbK`, use the drop-down menu to select `libusbK` and click on `Replace  Driver` which will update the drivers to `libusbK`.
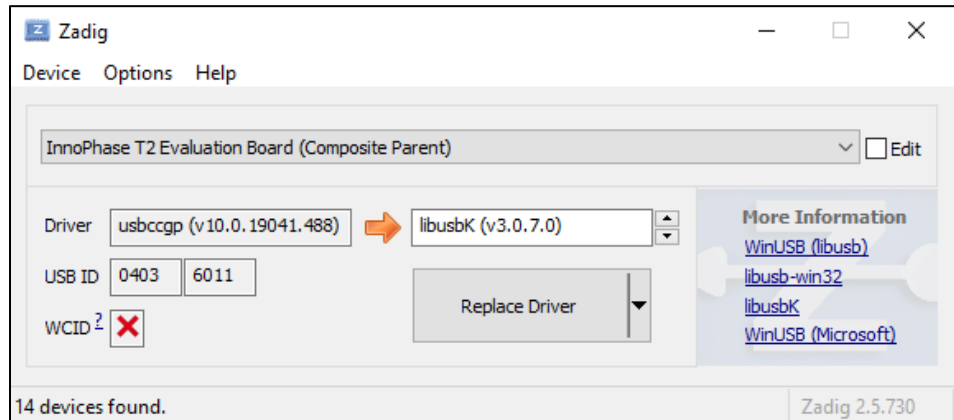

Figure 9: Updating Talaria TWO^{TM} USB driver to libusbK

7.1.1.2 Uninstall instructions for libusbK driver

To uninstall `libusbK` and retrieve COM ports, follow the following steps:

1. Open `Device Manager`. Expand the `libusbK USB Devices` and right click on the `InnoPhase T2 Evaluation Board (Composite Parent)`. Click on `Update Driver` as shown in Figure 10.
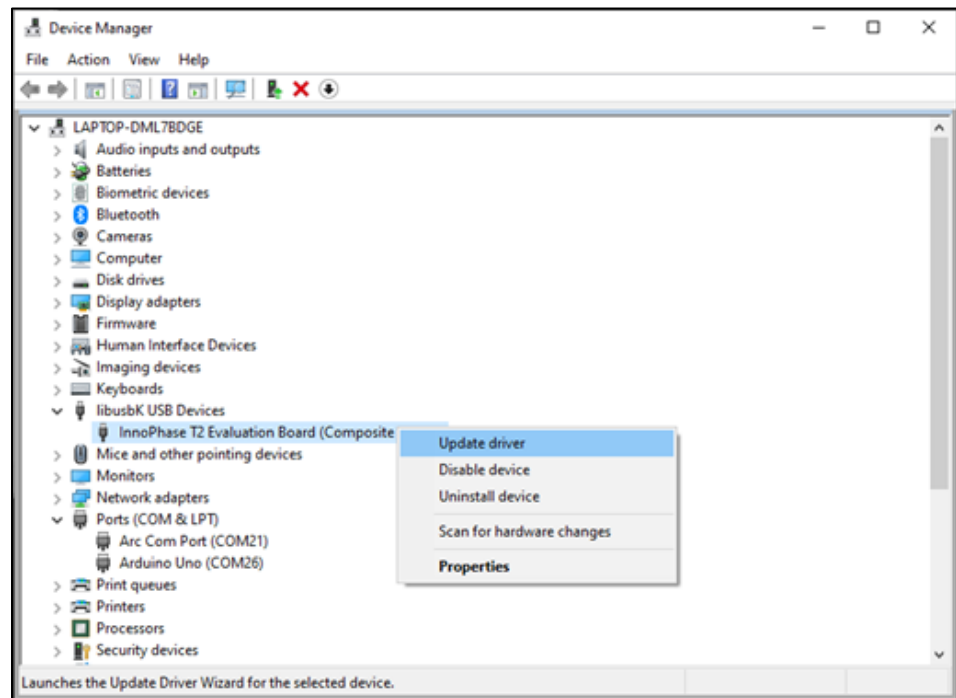

Figure 10: Device Manager

2. On the new window, select `Browse` computer for drivers, click on `Let me pick from a list of available drivers on my computer` option and click on `Next`.
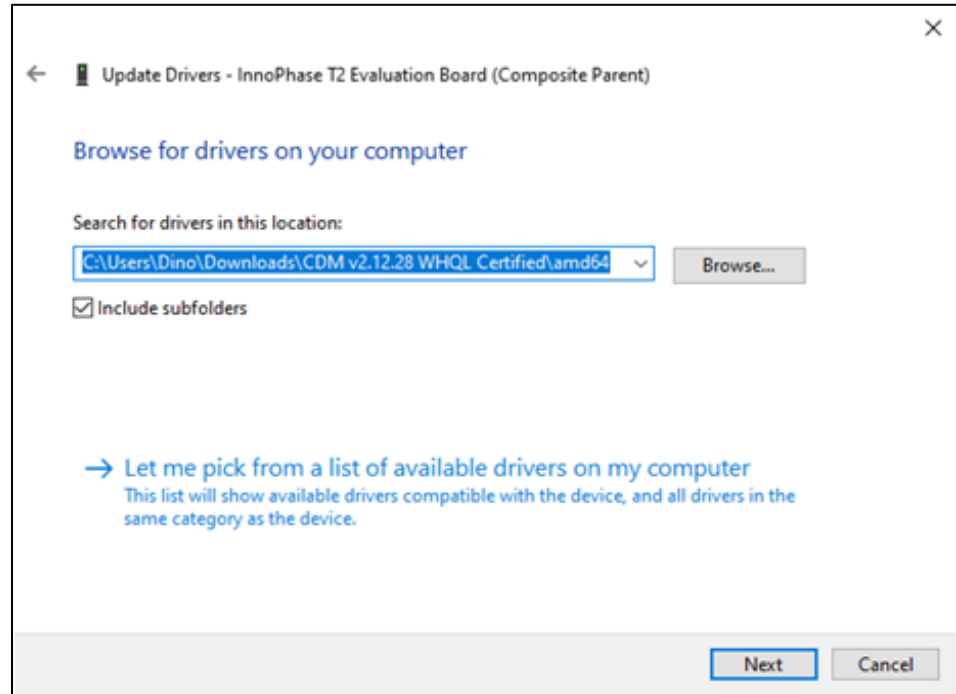

Figure 11: Update Devices

3. Select `USB Composite Device` and click `Next` to reinstall the COM ports.
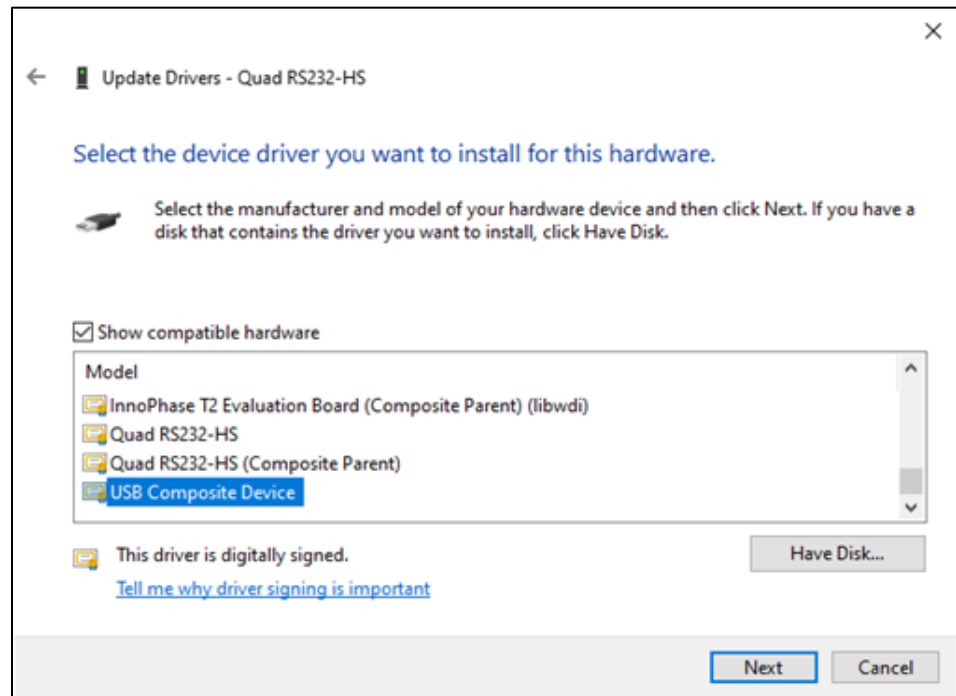

Figure 12: Select the device driver

7.2   Shield Mode

This mode will make the EVB-A board to act as a Wi-Fi/BLE5 shield. To enable this mode, flip the switch U3 towards V33_ARD. This also pulls up the INA pin of the multi-port SPDT device MAX4761ETX, which then routes the GPIOs from the INP101x module to the shield headers JP1, JP2, JP3 and JP4. In the shield mode a suitable firmware (such as Serial to Wi-Fi application available in the SDK) should be pre-flashed in the INP101x.

7.2.1   EVB-A as Wi-Fi Shield with STM32 Nucleo Board

A comprehensive set of host application packages are available to download via the InnoPhase website to demonstrate the use of EVB-A as a Wi-Fi/BLE5 shield board.
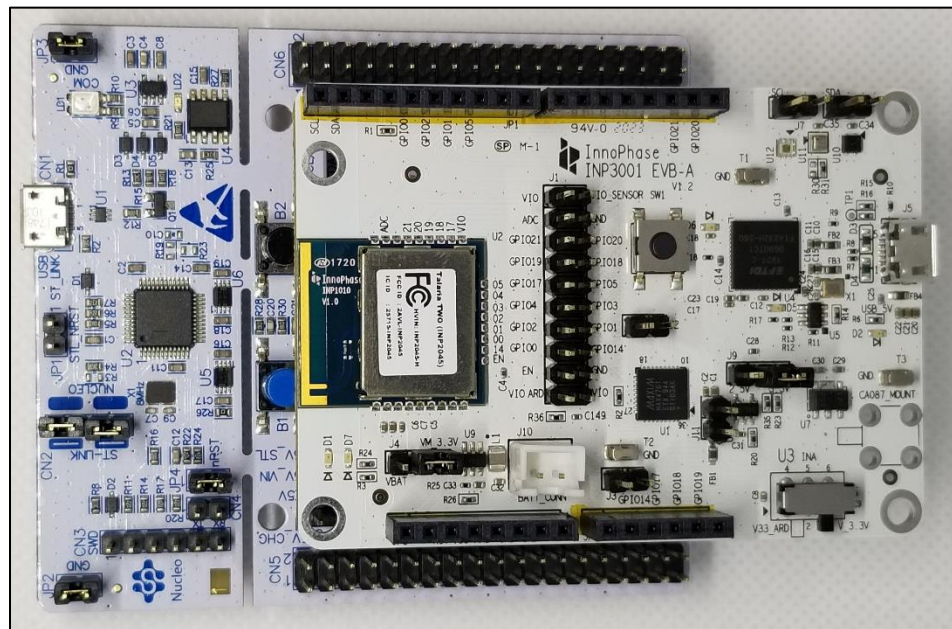

Figure 13: INP3010 EVB-A as Wi-Fi Shield

8 Power Measurement

The power consumption of the INP101x module is measured by either connecting a DMM on the jumper J4 or supplying power directly on J4 using specialty power supplies like Otti Arc from Qiotech. Figure 14 shows the connection setup to measure current consumption using Otti Arc.

Figure 14: Current measurement setup using Otti Arc

9 Using Battery as Power Source

Header J4 will switch between VBat and Vm_3.3V. Figure 15 shows VBat connection.
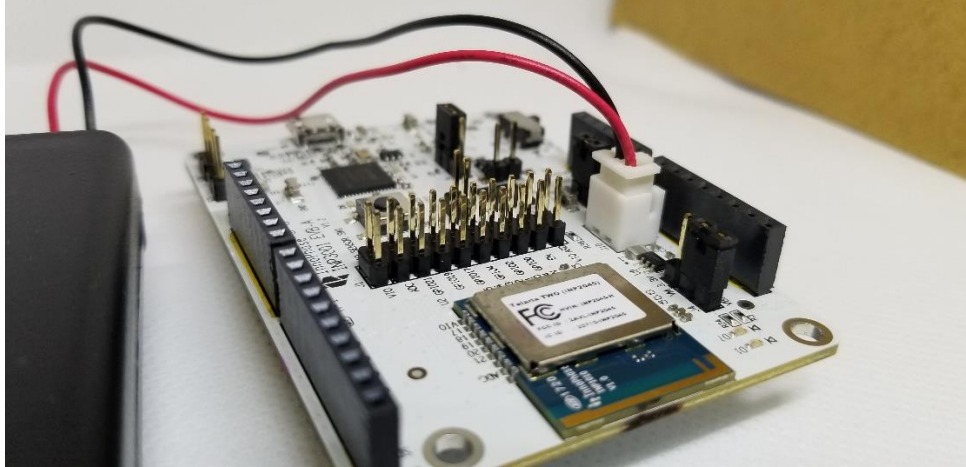

Figure 15: J10 Battery connection

Note: when using a battery as a power source there will be an additional current draw from LED (D7 or D12 depending on board version). If attempting to measure an accurate module current draw from the battery connection, the LED series resistor must be removed to disconnect the LED.

10 Support

1. Sales Support: Contact an InnoPhase sales representative via email – sales@innophaseinc.com
2. Technical Support:
    a. Visit: https://innophaseinc.com/contact/
    b. Also Visit: https://innophaseinc.com/talaria-two-modules
    c. Contact: support@innophaseinc.com

InnoPhase is working diligently to provide outstanding support to all customers.