

# LT1790

# Micropower SOT-23 Low Dropout Reference Family

# **DESCRIPTION**

The [LT®1790](http://www.linear.com/LT1790) is a family of SOT-23 micropower low dropout series references that combine high accuracy and low drift with low power dissipation and small package size. These micropower references use curvature compensation to obtain a low temperature coefficient and trimmed precision thin-film resistors to achieve high output accuracy. In addition, each LT1790 is post-package trimmed to greatly reduce the temperature coefficient and increase the output accuracy. Output accuracy is further assured by excellent line and load regulation. Special care has been taken to minimize thermally induced hysteresis.

The LT1790s are ideally suited for battery-operated systems because of their small size, low supply current and reduced dropout voltage. These references provide supply current and power dissipation advantages over shunt references that must idle the entire load current to operate. Since the LT1790 can also sink current, it can operate as a micropower negative voltage reference with the same performance as a positive reference.

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### **FEATURES**

- $\blacksquare$  **High Accuracy: A Grade—0.05% Max**
	- **B Grade—0.1% Max**
- **n** Low Drift:
	- **A Grade—10ppm/°C Max**
	- **B Grade—25ppm/°C Max**
- Low Thermal Hysteresis 40ppm (Typical) –40°C to 85°C
- Low Supply Current: 60µA Max
- Sinks and Sources Current
- **Low Dropout Voltage**
- Guaranteed Operational –40°C to 125°C
- Wide Supply Range to 18V
- Available Output Voltage Options: 1.25V, 2.048V, 2.5V, 3V, 3.3V, 4.096V and 5V
- Low Profile (1mm) ThinSOT<sup>™</sup> Package

# **APPLICATIONS**

- $\blacksquare$  Handheld Instruments
- Negative Voltage References
- Industrial Control Systems
- Data Acquisition Systems
- Battery-Operated Equipment

### TYPICAL APPLICATION



#### **Positive Connection for LT1790-2.5 Distribution for LT1790-2.5 Distribution for LT1790-2.5**



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#### ABSOLUTE MAXIMUM RATINGS PIN CONFIGURATION **(Note 1)**





# ORDER INFORMATION **<http://www.linear.com/product/LT1790#orderinfo>**



# ORDER INFORMATION



Consult LTC Marketing for parts specified with wider operating temperature ranges. \*The temperature grade is identified by a label on the shipping container. For more information on lead free part marking, go to: <http://www.linear.com/leadfree/>

For more information on tape and reel specifications, go to: <http://www.linear.com/tapeandreel/>. Some packages are available in 500 unit reels through designated sales channels with #TRMPBF suffix.



# AVAILABLE OPTIONS



#### 1.25V ELECTRICAL CHARACTERISTICS The  $\bullet$  denotes the specifications which apply over the specified **temperature range, otherwise specifications are at TA = 25°C. CL = 1µF and VIN = 2.6V, unless otherwise noted.**





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#### **2.048V ELECTRICAL CHARACTERISTICS** The  $\bullet$  denotes the specifications which apply over the **specified temperature range, otherwise specifications are at TA = 25°C. CL = 1µF and VIN = 2.8V, unless otherwise noted.**





### **2.048V ELECTRICAL CHARACTERISTICS** The  $\bullet$  denotes the specifications which apply over the

**specified temperature range, otherwise specifications are at TA = 25°C. CL = 1µF and VIN = 2.8V, unless otherwise noted.**



### 2.5V ELECTRICAL CHARACTERISTICS The  $\bullet$  denotes the specifications which apply over the specified

temperature range, otherwise specifications are at T<sub>A</sub> = 25°C. C<sub>L</sub> = 1µF and V<sub>IN</sub> = 3V, unless otherwise noted.





**STATE AREA** 

### **3V ELECTRICAL CHARACTERISTICS** The  $\bullet$  denotes the specifications which apply over the specified

**temperature range, otherwise specifications are at TA = 25°C. CL = 1µF and VIN = 3.5V, unless otherwise noted.**



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#### **3.3V ELECTRICAL CHARACTERISTICS** The  $\bullet$  denotes the specifications which apply over the specified

**temperature range, otherwise specifications are at TA = 25°C. CL = 1µF and VIN = 3.8V, unless otherwise noted.**





### 4.096V ELECTRICAL CHARACTERISTICS The  $\bullet$  denotes the specifications which apply over the

**specified temperature range, otherwise specifications are at TA = 25°C. CL = 1µF and VIN = 4.6V, unless otherwise noted.**





## **5V ELECTRICAL CHARACTERISTICS** The  $\bullet$  denotes the specifications which apply over the specified

**temperature range, otherwise specifications are at TA = 25°C. CL = 1µF and VIN = 5.5V, unless otherwise noted.**



**Note 1:** Stresses beyond those listed under Absolute Maximum Ratings may cause permanent damage to the device. Exposure to any Absolute Maximum Rating condition for extended periods may affect device reliability and lifetime.

**Note 2:** The LT1790 is guaranteed functional over the operating temperature range of –40°C to 125°C. The LT1790-1.25 at 125°C is typically less than 2% above the nominal voltage. The other voltage options are typically less than 0.25% above their nominal voltage.

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**Note 3:** If the part is stored outside of the specified temperature range, the output voltage may shift due to hysteresis.

**Note 4:** ESD (Electrostatic Discharge) sensitive device. Extensive use of ESD protection devices are used internal to the LT1790, however, high electrostatic discharge can damage or degrade the device. Use proper ESD handling precautions.

**Note 5:** Temperature coefficient is measured by dividing the change in output voltage by the specified temperature range. Incremental slope is also measured at 25°C.

**Note 6:** Load regulation is measured on a pulse basis from no load to the specified load current. Output changes due to die temperature change must be taken into account separately.

**Note 7:** Excludes load regulation errors.



# ELECTRICAL CHARACTERISTICS

**Note 8:** Peak-to-peak noise is measured with a single pole highpass filter at 0.1Hz and a 2-pole lowpass filter at 10Hz. The unit is enclosed in a still air environment to eliminate thermocouple effects on the leads. The test time is 10 seconds. Integrated RMS noise is measured from 10Hz to 1kHz with the HP3561A analyzer.

**Note 9:** Long-term drift typically has a logarithmic characteristic and therefore changes after 1000 hours tend to be smaller than before that time. Long-term drift is affected by differential stress between the IC and the board material created during board assembly. See the Applications Information section.

**Note 10:** Hysteresis in the output voltage is created by package stress that differs depending on whether the IC was previously at a higher or lower temperature. Output voltage is always measured at 25°C, but the IC is cycled to 85°C or –40°C before a successive measurements. Hysteresis is roughly proportional to the square of the temperature change. Hysteresis is not a problem for operational temperature excursions where the instrument might be stored at high or low temperature. See the Applications Information section.

# 1.25V TYPICAL PERFORMANCE CHARACTERISTICS

**Each of the voltage options have similar performance curves. For the 3V, 3.3V and the 4.096V options, the curves can be estimated based on the 2.5V and 5V curves.**



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# 2.5V TYPICAL PERFORMANCE CHARACTERISTICS

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INPUT VOLTAGE (V)

17902.5 G07

FREQUENCY (Hz)

17902.5 G08

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17902.5 G09

FREQUENCY (Hz)

# 2.5V TYPICAL PERFORMANCE CHARACTERISTICS

**Each of the voltage options have similar performance curves. For the 3V, 3.3V and the 4.096V options, the curves can be estimated based on the 2.5V and 5V curves.**







**Output Noise 0.1Hz to 10Hz COULD 100 CO** 









### 5V TYPICAL PERFORMANCE CHARACTERISTICS

**Each of the voltage options have similar performance curves. For the 3V, 3.3V and the 4.096V options, the curves can be estimated based on the 2.5V and 5V curves.**



1790fc



17905 G07

17905 G08

# 5V TYPICAL PERFORMANCE CHARACTERISTICS

**Each of the voltage options have similar performance curves. For the 3V, 3.3V and the 4.096V options, the curves can be estimated based on the 2.5V and 5V curves.**







**Output Noise 0.1Hz to 10Hz COVERTY SUMPLE SETTED SETTED SPECTREM** 





**Integrated Noise 10Hz to 1kHz**



#### **Bypass and Load Capacitors**

The LT1790 voltage references should have an input bypass capacitor of 0.1µF or larger, however the bypassing of other local devices may serve as the required component. These references also require an output capacitor for stability. The optimum output capacitance for most applications is 1µF, although larger values work as well. This capacitor affects the turn-on and settling time for the output to reach its final value.

All LT1790 voltages perform virtually the same, so the LT1790-2.5 is used as an example.



Figure 1 shows the turn-on time for the LT1790-2.5 with a 1µF input bypass and 1µF load capacitor. Figure 2 shows the output response to a 0.5V transient on  $V_{IN}$  with the same capacitors.

The test circuit of Figure 3 is used to measure the stability of various load currents. With  $R_1 = 1k$ , the 1V step produces a current step of 1mA. Figure  $\overline{4}$  shows the response to a ±0.5mA load. Figure 5 is the output response to a sourcing step from 4mA to 5mA, and Figure 6 is the output response of a sinking step from –4mA to –5mA.



**Figure 1. Turn-On Characteristics of LT1790-2.5 Figure 2. Output Response to 0.5V Ripple on VIN**



**Figure 3. Response Time Test Circuit**



**Figure 4. LT1790-2.5 Sourcing and Sinking 0.5mA Figure 5. LT1790-2.5 Sourcing 4mA to 5mA**





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#### **Positive or Negative Operation**

Series operation is ideal for extending battery life. If an LT1790 is operated in series mode it does not require an external current setting resistor. The specifications guarantee that the LT1790 family operates to 18V. When the circuitry being regulated does not demand current, the series connected LT1790 consumes only a few hundred µW, yet the same connection can sink or source 5mA of load current when demanded. A typical series connection is shown on the front page of this data sheet.

The circuit in Figure 7 shows the connection for  $a - 2.5V$ reference, although any LT1790 voltage option can be configured this way to make a negative reference. The LT1790 can be used as very stable negative references, however, they require a positive voltage applied to Pin 4 to bias internal circuitry. This voltage must be current limited with R1 to keep the output PNP transistor from turning on and driving the grounded output. C1 provides stability during load transients. This connection maintains nearly the same accuracy and temperature coefficient of the positive connected LT1790.

#### **Long-Term Drift**

**Long-term drift cannot be extrapolated from accelerated high temperature testing. This erroneous technique gives drift numbers that are widely optimistic. The only way long-term drift can be determined is to measure it over the time interval of interest.** The LT1790S6 drift data was taken on over 100 parts that were soldered into PC boards similar to a *real world* application. The boards were then placed into a constant temperature oven with  $T_A = 30^{\circ}$ C, their outputs scanned regularly and measured with an 8.5 digit DVM. Long-term drift curves are shown in the Typical Performance Characteristics section.



**Figure 6. LT1790-2.5 Sinking –4mA to –5mA**



**Figure 7. Using the LT1790-2.5 to Build a –2.5V Reference**



#### **Hysteresis**

Hysteresis data shown in Figures 8 and 9 represent the worst-case data taken on parts from 0°C to 70°C and from –40°C to 85°C. Units were cycled several times over these temperature ranges and the largest change is shown. As expected, the parts cycled over the higher temperature range have higher hysteresis than those cycled over the lower range.

In addition to thermal hysteresis, the thermal shock associated with high temperature soldering may cause the output to shift. For traditional PbSn solder temperatures, the output shift of the LT1790 is typically just 150ppm (0.015%).



**Figure 8. Worst-Case 0°C to 70°C Hysteresis on 79 Units**



**Figure 9. Worst-Case –40°C to 85°C Hysteresis on 80 Units**

For lead-free solder, IR reflow temperatures are much higher, often 240°C to 260°C at the peak. As a result, the packaging materials have been optimized to reduce  $V_{\text{OUT}}$  shift as possible during high temperature reflow. In addition, care should be taken when using lead-free solder to minimize the peak temperature and dwell time as much as is practical. A typical lead-free reflow profile is shown in Figure 10. LT1790 units were heated using a similar profile, with a peak temperature of 250°C. These parts were run through the heating process 3 times to show the cumulative effect of these heat cycles. Figure



**Figure 10. Lead-Free Reflow Profile**







11 shows the shift after 1 cycle, while Figure 12 shows shift after 3 cycles. In the worst case, shifts are typically 150ppm, but may be as high as 290ppm. Shifts in output voltage are proportional to temperature and dwell time.

In general, the output shift can be reduced or fully recovered by a long (12-24 hour) bake of the completed PC Board assembly at high temperature (100°C to 150C°) after soldering to remove mechanical stress that has been induced by thermal shock. Once the PC Boards have cooled to room temperature, they may continue to shift for up to 3 times the bake time. This should be taken into account before any calibration is performed.



**Delta Output Voltage (ppm)**

#### **Higher Input Voltage**

The circuit in Figure 13 shows an easy way to increase the input voltage range of the LT1790. The Zener diode can be anywhere from 6V to 18V. For equal power sharing between R1 and the Zener (at 30V), the 18V option is better. The circuit can tolerate much higher voltages for short periods and is suitable for transient protection.



**Figure 13. Extended Supply Range Reference**

Assuming 80µA max supply current for the LT1790, a 25µA load, 120mV max dropout and a 4V to 30V input specification, the largest that R1 can be is  $(4V - 3.3V - 120mV)/$ (80µA + 25µA) = 5.5k. Furthermore, assuming 220mW of dissipation in the 18V SOT-23 Zener, this gives a max current of  $(220mW)/(18V) = 12.2mA$ . So the smallest that R1 should be is  $(30V - 18V)/12.2mA = 1k$ , rated at  $150mW$ .

With R1 = 1k, and assuming a 450mV worst-case dropout, the LT1790 can deliver a minimum current of (4V  $-3.3V-450mV)/(1k) = 250\mu A$ . In Figure 13, R1 and C1 provide filtering of the Zener noise when the Zener is in its noisy V-I knee.

There are other variations for higher voltage operation that use a pass transistor shown in Figures 14 and 15. These circuits allow the input voltage to be as high as 160V while maintaining low supply current.



**Figure 14. Extended Supply Range Reference**



**Figure 15. Extended Supply Range Reference**



#### **More Output Current**

The circuit in Figure 16 is a compact, high output current, low dropout precision supply. The circuit uses the SOT-23 LT1782 and the ThinSOT LT1790. Resistive divider R1 and R2 set a voltage 22mV below  $V_S$ . For under 1mA of output current, the LT1790 supplies the load. Above 1mA of load current, the (+) input of the LT1782 is pulled below the 22mV divider reference and the output FET turns on to supply the load current. Capacitor C1 stops oscillations in the transition region. The no load standing current is only 120µA, yet the output can deliver over 300mA.

#### **Noise**

An estimate of the total integrated noise from 10Hz to 1kHz can be made by multiplying the flat band spot noise by √BW. For example, from the Typical Performance curves, the LT1790-1.25 noise spectrum shows the average spot noise to be about 450nV/ $\sqrt{Hz}$ . The square root of the

bandwidth is  $\sqrt{990}$  = 31.4. The total noise 10Hz to 1kHz noise is  $(450nV)(31.4) = 14.1\mu V$ . This agrees well with the measured noise.

This estimate may not be as good with higher voltage options, there are several reasons for this. Higher voltage options have higher noise and they have higher variability due to process variations. 10Hz to 1kHz noise may vary by 2dB on the LT1790-5 and 1dB on the LT1790-2.5.

Measured noise may also vary because of peaking in the noise spectrum. This effect can be seen in the range of 1kHz to 10kHz with all voltage options sourcing different load currents. From the Typical Performance curves the 10Hz to 1kHz noise spectrum of the LT1790-5 is shown to be 3µV/√Hz at low frequency. The estimated noise is  $(3µV)(31.4) = 93.4µV$ . The actual integrated 10Hz to 1kHz noise measures 118.3µV. The peaking shown causes this larger number. Peaking is a function of output capacitor as well as load current and process variations.



**Figure 16. Compact, High Output Current, Low Dropout, Precision 2.5V Supply**

## SIMPLIFIED SCHEMATIC



### PACKAGE DESCRIPTION

**Please refer to <http://www.linear.com/product/LT1790#packaging> for the most recent package drawings.**

**S6 Package 6-Lead Plastic TSOT-23**

(Reference LTC DWG # 05-08-1636)



4. DIMENSIONS ARE EXCLUSIVE OF MOLD FLASH AND METAL BURR

- 5. MOLD FLASH SHALL NOT EXCEED 0.254mm
- 6. JEDEC PACKAGE REFERENCE IS MO-193



### REVISION HISTORY **(Revision history begins at Rev C)**





