

LT3465/LT3465A

1.2MHz/2.4MHz White LED Drivers with Built-in Schottky in ThinSOT

The LT[®]3465/LT3465A are step-up DC/DC converters

designed to drive up to six LEDs in series from a Li-Ion cell.

Series connection of the LEDs provides identical LED

currents and eliminates the need for ballast resistors.

These devices integrate the Schottky diode required externally on competing devices. Additional features include

output voltage limiting when LEDs are disconnected, one-

pin shutdown and dimming control. The LT3465 has

The LT3465 switches at 1.2MHz, allowing the use of tiny

external components. The faster LT3465A switches at

2.4MHz. Constant frequency switching results in low input

noise and a small output capacitor. Just 0.22µF is required

The LT3465 and LT3465A are available in the low profile

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trademark of Linear Technology Corporation. All other trademarks are the property of their

(1mm) 6-lead SOT-23 (ThinSOT[™]) package.

DESCRIPTION

internal soft-start.

respective owners.

for 3-, 4- or 5-LED applications.

FEATURES

- Inherently Matched LED Current
- Drives Up to Six LEDs from a 3.6V Supply
- No External Schottky Diode Required
- 1.2MHz Switching Frequency (LT3465)
- 2.4MHz Switching Frequency Above AM Broadcast Band (LT3465A)
- V_{IN} Range: 2.7V to 16V
- $V_{OUT(MAX)} = 30V$
- Automatic Soft-Start (LT3465)
- **Open LED Protection**
- High Efficiency: 81% (LT3465) 79% (LT3465A) Tvpical
- Requires Only 0.22µF Output Capacitor
- Low Profile (1mm) SOT-23

APPLICATIONS

- Cellular Phones
- PDAs, Handheld Computers
- **Digital Cameras**
- **MP3** Plavers
- **GPS** Receivers

TYPICAL APPLICATION

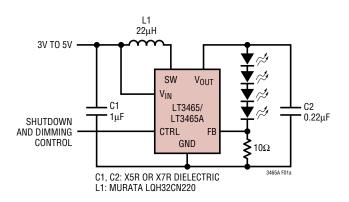


Figure 1. Li-Ion Powered Driver for Four White LEDs

82 V_{IN} = 3.6V 4 LEDs 80 78 76 EFFICIENCY (%) 74 72 70 68 66 64 I T3465 62 LT3465A 60 ٥ 5 10 15 20 LED CURRENT (mA)

Conversion Efficiency

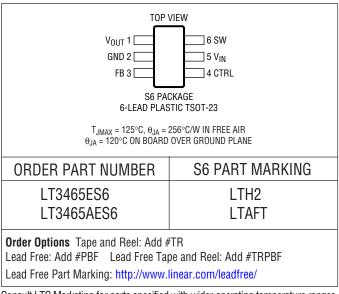
3465A E01b



ABSOLUTE MAXIMUM RATINGS

(Note 1)
Input Voltage (V _{IN}) 16V
SW Voltage 36V
FB Voltage 2V
CTRL Voltage 10V
Operating Temperature Range (Note 2) −40°C to 85°C
Maximum Junction Temperature 125°C
Storage Temperature Range65°C to 150°C
Lead Temperature (Soldering, 10 sec) 300°C

PACKAGE/ORDER INFORMATION



Consult LTC Marketing for parts specified with wider operating temperature ranges.

ELECTRICAL CHARACTERISTICS

ELECTRICAL CHARACTERISTICS The \bullet denotes the specifications which apply over the full operating temperature range, otherwise specifications are at T_A = 25°C. V_{IN} = 3V, V_{CTRL} = 3V, unless otherwise noted.

				LT3465			LT3465A		
PARAMETER	CONDITIONS		MIN	TYP	MAX	MIN	TYP	MAX	UNITS
Minimum Operating Voltage			2.7			2.7			V
Maximum Operating Voltage					16			16	V
Feedback Voltage	$0^{\circ}C \le T_A \le 85^{\circ}C$		188	200	212	188	200	212	mV
FB Pin Bias Current			10	35	100	10	35	100	nA
Supply Current	Not Switching		1.9	2.6	3.3	1.9	2.6	3.3	mA
	CTRL = 0V		2.0	3.2	5.0	2.0	3.2	5.0	μΑ
Switching Frequency			0.8	1.2	1.6	1.8	2.4	2.8	MHz
Maximum Duty Cycle		•	90	93		90	93		%
Switch Current Limit		•	225	340		225	340		mA
Switch V _{CESAT}	I _{SW} = 250mA			300			300		mV
Switch Leakage Current	V _{SW} = 5V			0.01	5		0.01	5	μA
V _{CTRL} for Full LED Current			1.8			1.8			V
V _{CTRL} to Enable Chip		•	150			150			mV
V _{CTRL} to Shut Down Chip		•			50			50	mV
CTRL Pin Bias Current			48	60	72	48	60	72	μA
	$T_A = 85^{\circ}C$ $T_A = -40^{\circ}C$		40 60	50 75	60 90	40 60	50 75	60 90	μΑ μΑ
Soft-Start Time				600	00				μs
Schottky Forward Drop	I _D = 150mA			0.7			0.7		V
Schottky Leakage Current	V _R = 30V				4			4	μA

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: The LT3465E/LT3465AE are guaranteed to meet performance

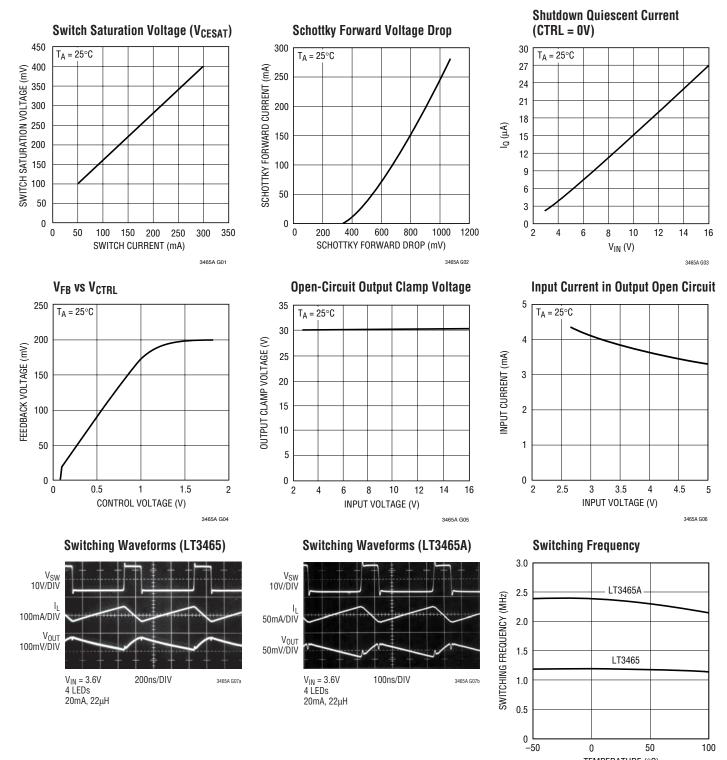
specifications from 0°C to 70°C. Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.





TYPICAL PERFORMANCE CHARACTERISTICS

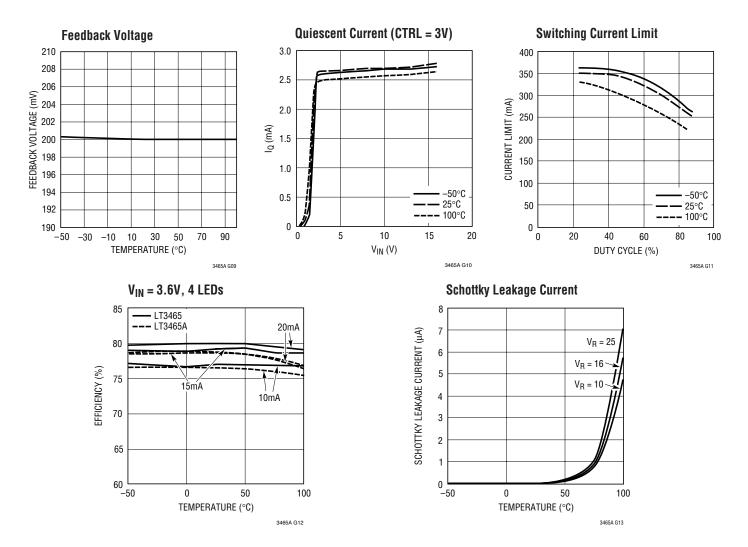
LINEAR



4365A G08

TEMPERATURE (°C)

TYPICAL PERFORMANCE CHARACTERISTICS





PIN FUNCTIONS

V_{OUT} (**Pin 1**): Output Pin. Connect to output capacitor and LEDs. Minimize trace between this pin and output capacitor to reduce EMI.

GND (Pin 2): Ground Pin. Connect directly to local ground plane.

FB (Pin 3): Feedback Pin. Reference voltage is 200mV. Connect LEDs and a resistor at this pin. LED current is determined by the resistance and CTRL pin voltage:

CTRL (Pin 4): Dimming Control and Shutdown Pin. Ground this pin to shut down the device. When V_{CTRL} is greater than about 1.8V, full-scale LED current is generated. When V_{CTRL} is less than 1V, LED current is reduced. Floating this pin places the device in shutdown mode.

 V_{IN} (Pin 5): Input Supply Pin. Must be locally bypassed with a 1µF X5R or X7R type ceramic capacitor.

SW (Pin 6): Switch Pin. Connect inductor here.

$$I_{LED} = \frac{1}{R_{FB}} \bullet \left(200\text{mV} - 26\text{mV} \bullet 1\text{n} \left(\frac{\exp\left(\frac{200\text{mV}}{26\text{mV}}\right)}{\exp\left(\frac{V_{CTRL}(\text{mV})}{5\text{mV} \bullet 26\text{mV}}\right)} + 1 \right) \right) \text{ for } V_{CTRL} > 150\text{mV}$$



BLOCK DIAGRAM

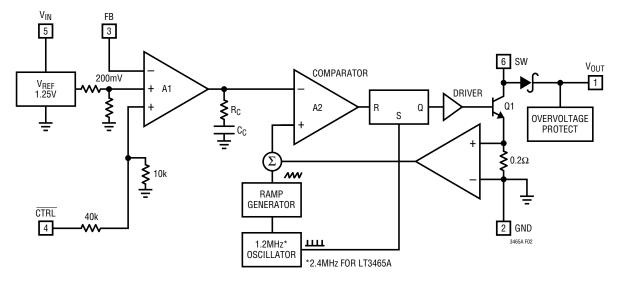


Figure 2. LT3465 Block Diagram



Operation

The LT3465 uses a constant frequency, current mode control scheme to provide excellent line and load regulation. Operation can be best understood by referring to the block diagram in Figure 2. At the start of each oscillator cycle, the SR latch is set, which turns on the power switch Q1. A voltage proportional to the switch current is added to a stabilizing ramp and the resulting sum is fed into the positive terminal of the PWM comparator A2. When this voltage exceeds the level at the negative input of A2, the SR latch is reset turning off the power switch. The level at the negative input of A2 is set by the error amplifier A1, and is simply an amplified version of the difference between the feedback voltage and the reference voltage of 200mV. In this manner, the error amplifier sets the correct peak current level to keep the output in regulation. If the error amplifier's output increases, more current is delivered to the output; if it decreases, less current is delivered. The CTRL pin voltage is used to adjust the reference voltage. The block diagram for the LT3465A (not shown) is identical except that the oscillator frequency is 2.4MHz.

Minimum Output Current

The LT3465 can drive a 3-LED string at 1.5mA LED current without pulse skipping. As current is further reduced, the device will begin skipping pulses. This will result in some low frequency ripple, although the LED current remains regulated on an average basis down to zero. The photo in Figure 3a details circuit operation driving three white LEDs at a 1.5mA load. Peak inductor current is less than 40mA and the regulator operates in discontinuous mode, meaning the inductor current reaches zero during the discharge phase. After the inductor current reaches zero, the SW pin exhibits ringing due to the LC tank circuit formed by the inductor in combination with switch and diode capacitance. This ringing is not harmful; far less spectral energy is contained in the ringing than in the switch transitions. The ringing can be damped by application of a 300Ω resistor across the inductor, although this will degrade efficiency. Because of the higher switching frequency, the LT3465A can drive a 3-LED string at 0.2mA LED current without pulse

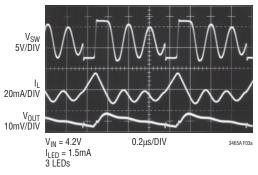


Figure 3a. Switching Waveforms (LT3465)

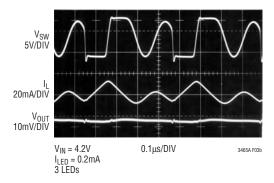


Figure 3b. Switching Waveforms (LT3465A)



skipping using a 1k resistor from FB to GND. The photo in Figure 3b details circuit operation driving three white LEDs at a 0.2mA load. Peak inductor current is less than 30mA.

Inductor Selection

A 22 μ H inductor is recommended for most LT3465 applications. Although small size and high efficiency are major concerns, the inductor should have low core losses at 1.2MHz and low DCR (copper wire resistance). Some inductors in this category with small size are listed in Table 1. The efficiency comparison of different inductors is shown in Figure 4a. A 22 μ H or 10 μ H inductor is recommended for most LT3465A applications. The inductor should have low core losses at 2.4MHz and low DCR. The efficiency comparison of different inductors is shown in figure 4b.

PART NUMBER	DCR (Ω)	CURRENT RATING (mA)	MANUFACTURER
LQH32CN220 LQH2MCN220	0.71 2.4	250 185	Murata 814-237-1431 www.murata.com
ELJPC220KF	4.0	160	Panasonic 714-373-7334 www.panasonic.com
CDRH3D16-220	0.53	350	Sumida 847-956-0666 www.sumida.com
LB2012B220M	1.7	75	Taiyo Yuden 408-573-4150 www.t-yuden.com
LEM2520-220	5.5	125	Taiyo Yuden 408-573-4150 www.t-yuden.com

Table 1. Recommended Inductors

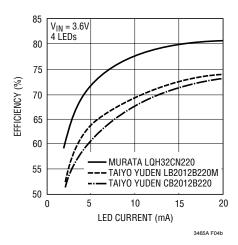
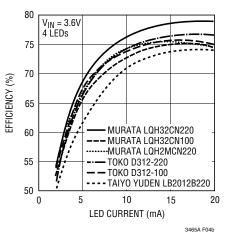
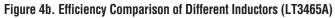


Figure 4a. Efficiency Comparison of Different Inductors (LT3465)





Capacitor Selection

The small size of ceramic capacitors makes them ideal for LT3465 and LT3465A applications. X5R and X7R types are recommended because they retain their capacitance over wider voltage and temperature ranges than other types such as Y5V or Z5U. A 1 μ F input capacitor and a 0.22 μ F output capacitor are sufficient for most LT3465 and LT3465A applications.

MANUFACTURER	PHONE	URL
Taiyo Yuden	408-573-4150	www.t-yuden.com
Murata	814-237-1431	www.murata.com
Kemet	408-986-0424	www.kemet.com



Soft-Start (LT3465)

The LT3465 has an internal soft-start circuit to limit the input current during circuit start-up. The circuit start-up waveforms are shown in Figure 5.

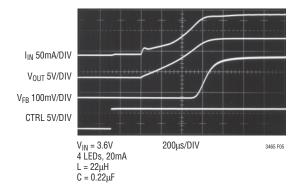


Figure 5. Start-Up Waveforms

Inrush Current

The LT3465 and LT3465A have a built-in Schottky diode. When supply voltage is applied to the V_{IN} pin, the voltage difference between V_{IN} and V_{OUT} generates inrush current flowing from input through the inductor and the Schottky diode to charge the output capacitor to V_{IN}. The maximum current the Schottky diode in the LT3465 and LT3465A can sustain is 1A. The selection of inductor and capacitor value should ensure the peak of the inrush current to be below 1A. The peak inrush current can be calculated as follows:

$$I_{P} = \frac{V_{IN} - 0.6}{L \cdot \omega} \cdot exp\left[-\frac{\alpha}{\omega} \cdot \arctan\left(\frac{\omega}{\alpha}\right)\right] \cdot sin\left[\arctan\left(\frac{\omega}{\alpha}\right)\right]$$
$$\alpha = \frac{r + 1.5}{2 \cdot L}$$
$$\omega = \sqrt{\frac{1}{L \cdot C} - \frac{\left(r + 1.5\right)^{2}}{4 \cdot L^{2}}}$$

where L is the inductance, r is the resistance of the inductor and C is the output capacitance. For low DCR $\,$

inductors, which is usually the case for this application, the peak inrush current can be simplified as follows:

$$I_{P} = \frac{V_{IN} - 0.6}{L \bullet \omega} \bullet \exp\left(-\frac{\alpha}{\omega} \bullet \frac{\pi}{2}\right)$$

Table 3 gives inrush peak currents for some component selections.

Table 3. Inrush Peak Current

V _{IN} (V)	r (Ω)	L (μΗ)	C (μ F)	I _P (A)
5	0.5	22	0.22	0.38
5	0.5	22	1	0.70
3.6	0.5	22	0.22	0.26
5	0.5	33	1	0.60

LED Current and Dimming Control

The LED current is controlled by the feedback resistor (R1 in Figure 1) and the feedback reference voltage.

$$I_{LED} = V_{FB}/R_{FB}$$

The CTRL pin controls the feedback reference voltage as shown in the Typical Performance Characteristics. For CTRL higher than 1.8V, the feedback reference is 200mV, which results in full LED current. CTRL pin can be used as dimming control when CTRL voltage is between 200mV to 1.5V. In order to have accurate LED current, precision resistors are preferred (1% is recommended). The formula and table for R_{FB} selection are shown below.

 $R_{FB} = 200 \text{mV/I}_{\text{LED-Full}}$ (1)

Table 4. R_{FB} Resistor Value Selection

FULL I _{LED} (mA)	R1 (Ω)
5	40.0
10	20.0
15	13.3
20	10.0

The filtered PWM signal can be considered to be an adjustable DC voltage. It can be used to adjust the CTRL voltage source in dimming control. The circuit is shown in Figure 6. The corner frequency of R1 and C1 should be



lower than the frequency of the PWM signal. R1 needs to be much smaller than the internal impedance in the CTRL pin, which is $50k\Omega$. A 5k resistor is suggested.

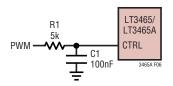
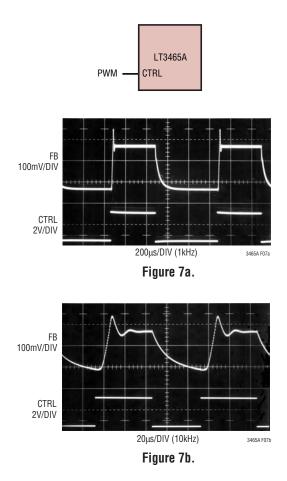


Figure 6. Dimming Control Using a Filtered PWM Signal

Dimming Using Direct PWM (LT3465A)

Unlike the LT3465, the LT3465A does not have internal soft-start. Although the input current is higher during start-up, the absence of soft-start allows the CTRL pin to be directly driven with a PWM signal for dimming. A zero

percent duty cycle sets the LED current to zero, while 100% duty cycle sets it to full current. Average LED current increases proportionally with the duty cycle of the PWM signal. With the PWM signal at the CTRL pin to turn the LT3465A on and off, the output capacitor is charged and discharged accordingly. This capacitor charging/ discharging affects the waveform at the FB pin. For low PWM frequencies the output capacitor charging/discharging time is a very small portion in a PWM period. The average FB voltage increases linearly with the PWM duty cycle. As the PWM frequency increases, the capacitor charging/discharging has a larger effect on the linearity of the PWM control. Waveforms for a 1kHz and 10kHz PWM CTRL signals are shown in Figures 7a and 7b respectively. The capacitor charging/discharging has a larger effect on the FB waveform in the 10kHz case than that in the 1kHz





case. The Average FB Voltage vs PWM Duty Cycle curves of different PWM frequencies with different output capacitors are shown in Figures 7c and 7d respectively. For PWM frequency lower than 1kHz, the curves are almost linear. For PWM frequency higher than 10kHz, the curves show strong nonlinearity. Since the cause of the nonlinearity is the output capacitor charging/discharging, the output capacitance and output voltage also affect the nonlinearity in the high PWM frequencies. Because smaller capacitance corresponds to shorter capacitor charging/discharging time, the smaller output capacitance has better linearity as shown in Figures 7c and 7d. Figures 7e and 7f show the output voltage's effect to the curves. The PWM signal should be at least 1.8V in magnitude; lower voltage will lower the feedback voltage as shown in Equation 1.

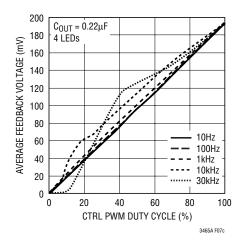


Figure 7c. V_{FB} vs CTRL PWM Duty Cycle

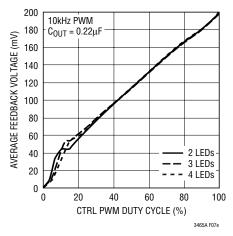


Figure 7e.V_{FB} vs CTRL PWM Duty Cycle

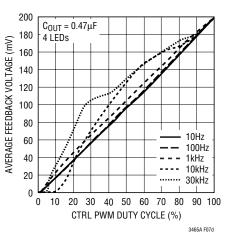


Figure 7d. V_{FB} vs CTRL PWM Duty Cycle

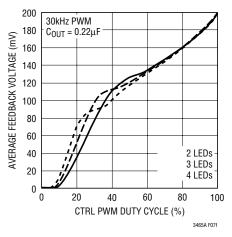


Figure 7f.V_{FB} vs CTRL PWM Duty Cycle



Open-Circuit Protection

The LT3465 and LT3465A have an internal open-circuit protection circuit. In the cases of output open circuit, when the LEDs are disconnected from the circuit or the LEDs fail, the V_{OUT} is clamped at 30V. The LT3465 and LT3465A will then switch at a very low frequency to minimize the input current. V_{OUT} and input current during output open circuit are shown in the Typical Performance Characteristics.

Board Layout Consideration

As with all switching regulators, careful attention must be paid to the PCB board layout and component placement. To maximize efficiency, switch rise and fall times are made as short as possible. To prevent electromagnetic interference (EMI) problems, proper layout of the high frequency switching path is essential. Place C_{OUT} next to the V_{OUT} and GND pins. Always use a ground plane under the switching regulator to minimize interplane coupling. In addition, the ground connection for the feedback

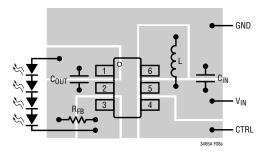


Figure 8. Recommended Component Placement.

resistor R1 should be tied directly to the GND pin and not shared with any other component, ensuring a clean, noisefree connection. Recommended component placement is shown in Figure 8.

Start-Up Input Current (LT3465A)

As previously mentioned, the LT3465A does not have an internal soft-start circuit. Inrush current can therefore rise to approximately 400mA as shown in Figure 9 when driving 4 LEDs. The LT3465 has an internal soft-start circuit and is recommended if inrush current must be minimized.

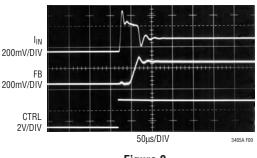
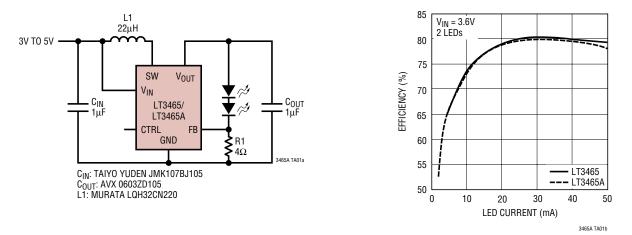


Figure 9.

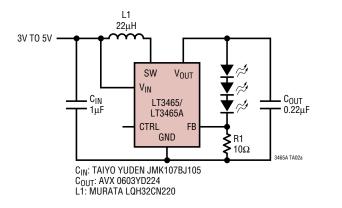


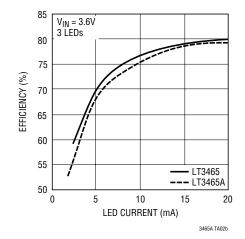
TYPICAL APPLICATIONS



Li-lon to Two White LEDs

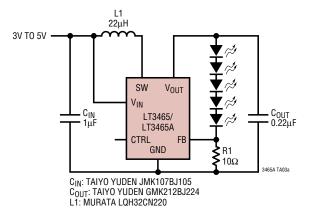
Li-lon to Three White LEDs



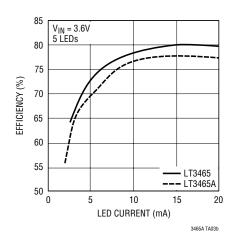




TYPICAL APPLICATION

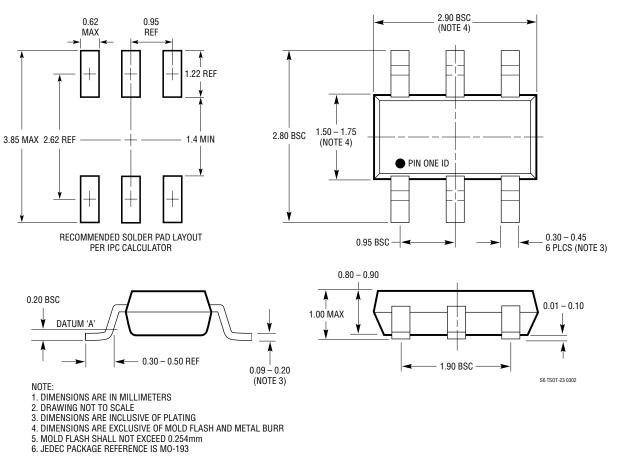


Li-lon to Five White LEDs





PACKAGE DESCRIPTION



S6 Package 6-Lead Plastic TSOT-23 (Reference LTC DWG # 05-08-1636)