## Power Amplifier 10.0 - 15.35 GHz

#### Features

- Fully Integrated Power Amplifier
- Wide Bandwidth 10.0 15.35 GHz
- 30 dB Small Signal Gain
- 40 dBm Third Order Intercept Point (OIP3)
- 31 dBm Output P1dB
- Integrated Power Detector
- Bias Voltage 5 V, 1.3 A
- Lead-Free 5 mm 24-lead QFN Package
- RoHS\* Compliant

### Description

The MAAP-110150 is a packaged linear power amplifier that operates over the range 10.0 - 15.35 GHz. The device typically provides 30 dB of gain and 40 dBm OIP3 with more than 31 dBm of output P1dB.

This power amplifier is assembled in a lead free, fully molded 5 mm QFN package and consists of a 3 stage power amplifier with integrated, on-chip peak power detector and envelope detector. The device includes on-chip ESD protection structures and DC by-pass capacitors to ease the implementation and volume assembly.

The device is well suited for use in the 10, 11, 13, 15 GHz cellular backhaul applications.

## Ordering Information<sup>1,2</sup>

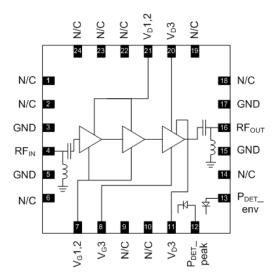
Part Number	Package
MAAP-110150	Bulk
MAAP-110150-TR0500	Tape and Reel
MAAP-110150-001SMB	Sample Board

1. Reference Application Note M513 for reel size information.

2. All sample boards include 5 loose parts.

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### Functional Schematic



## Pin Configuration<sup>3,4</sup>

Pin No.	Function	Pin No.	Function
1	No Connection	13	Envelope Power Detector
2	No Connection	14	No Connection
3	Ground	15	Ground
4	RF Input	16	RF Output
5	Ground	17	Ground
6	No Connection	18	No Connection
7	Gate Voltage 1,2	19	No Connection
8	Gate Voltage 3	20	Drain Voltage 3
9	No Connection	21	Drain Voltage 1,2
10	No Connection	22	No Connection
11	Drain Voltage 3	23	No Connection
12	Peak Power Detector	24	No Connection

MACOM recommends connecting unused package pins to ground.

4. The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

\* Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.

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## **Electrical Specifications:**

## Freq. = 10.0 - 15.35 GHz, $I_{DQ}$ = 1.3 A, $T_A$ = 25°C, $V_D$ = 5 V, $Z_0$ = 50 $\Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Gain	10.00 - 10.30 GHz 10.30 - 11.70 GHz 12.75 - 13.25 GHz 14.50 - 15.35 GHz	dB	 27.0 27.0 27.0	30.75 31.00 30.00 31.00	_
P1dB, @ 1 dB Compression	10.00 - 10.30 GHz 10.30 - 11.70 GHz 12.75 - 13.25 GHz 14.50 - 15.35 GHz	dBm	_	31.50 31.75 31.75 31.50	_
P <sub>SAT</sub>	10.00 - 10.30 GHz 10.30 - 11.70 GHz 12.75 - 13.25 GHz 14.50 - 15.35 GHz	dBm		34.25 34.50 34.00 33.50	_
OIP3	10.00 - 10.30 GHz 10.30 - 11.70 GHz 12.75 - 13.25 GHz 14.50 - 15.35 GHz	dBm	 38.0 38.0 35.5	40.50 41.00 40.00 37.50	_
Input Return Loss	—	dB		12	_
Output Return Loss	_	dB	_	12	—
PAE, @ 1 dB Compression	_	%	_	20	_
Quiescent Current	_	mA	_	1300	_

## Absolute Maximum Ratings<sup>5,6,7</sup>

Parameter	Rating
Drain Voltage ( $V_D$ 1,2,3)	7 V
Gate Voltage (V <sub>G</sub> 1,2,3)	-3 V
Drain to Gate Voltage (V <sub>D</sub> -V <sub>G</sub> )	10 V
Storage Temperature	-65°C to +150°C
Junction Temperature	+175°C

5. Exceeding any one or combination of these limits may cause permanent damage to this device.

6. MACOM does not recommend sustained operation near these survivability limits.

 Operating at nominal conditions with T<sub>J</sub> ≤ +150°C will ensure MTTF > 1 x 10<sup>6</sup> hours.

## Maximum Operating Ratings<sup>8,9</sup>

Parameter	Rating
P <sub>DISS</sub>	10 W
Operating Temperature	-40°C to +85°C
Junction Temperature	+150°C

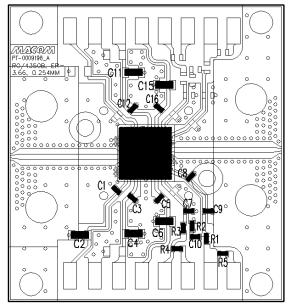
 Channel temperature directly affects device MTTF. Channel temperature should be kept as low as possible to maximize lifetime. Thermal resistance, O<sub>JC</sub> is 9.2 °C/W.

9. For saturated performance, it is recommended that the sum of  $(2V_{\text{DD}}$  + abs  $(V_{\text{GG}}))$  <15 V.

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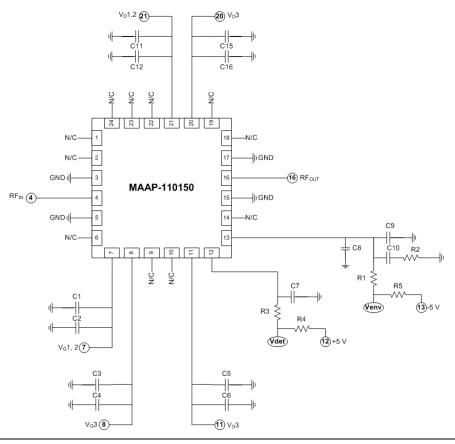
### **PCB** Layout



#### Parts List

Part	Value	Case Style
C1,C3,C5,C7,C8, C9,C10,C12,C16	100 nF	0402
C2,C4,C6,C11,C15	4.7 µF	0603
R1,R3	100 Ω	0402
R2	10 ΚΩ	0402
R4,R5	560 Ω	0402

## **Application Schematic**



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### Biasing

All gates should be pinched-off,  $V_G < -2 V$ , before applying the drain voltage,  $V_D = 5 V$  (do not exceed maximum specified  $V_{DG}$  value of 10 V). Then the gate voltages can be increased until the desired quiescent drain current is reached in each stage. The recommended quiescent bias is  $V_D = 5 V$ ,  $I_{D12} + I_{D3} = 1300 \text{ mA}$  (total). The performance in this datasheet has been measured with a fixed gate voltage and no drain current regulation under large signal operation. It is also possible to regulate the drain current dynamically, to limit the DC power dissipation under RF drive. To turn off the device, the turn on bias sequence should be followed in reverse.

### **Detector Operation**

MAAP-110150 includes a power and envelope detector. As per the application schematic, the power detector requires an external 5 V supply and the envelope detector requires -5 V. The output from the resistive voltage divider can be fed into a ADC or multimeter for the result.

### **Bias Arrangement**

Each DC pin ( $V_{D1,2}$ ,  $V_{D3}$  and  $V_{G1,2}$ ,  $V_{G3}$ ) needs to have bypass capacitance of 100 nF mounted as close to the packaged device as possible.

### **Handling Procedures**

Please observe the following precautions to avoid damage:

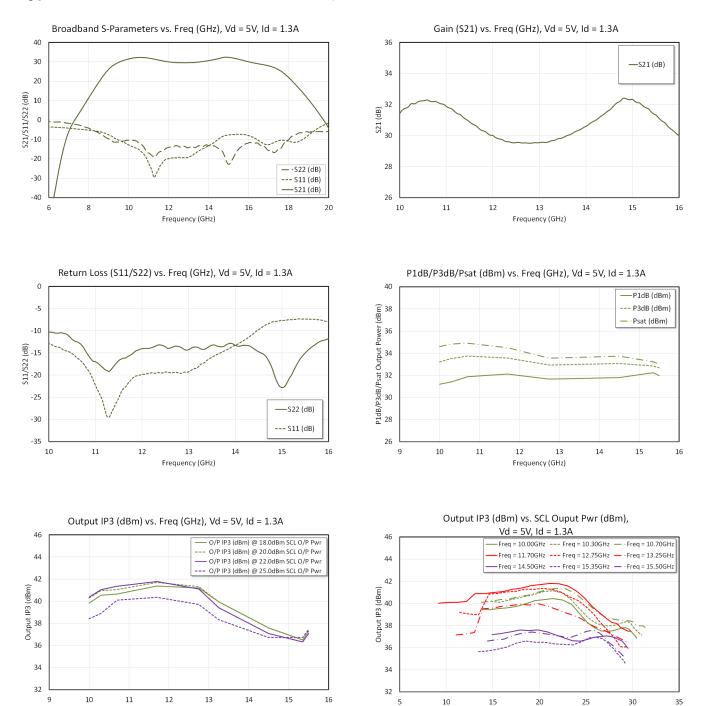
### **Static Sensitivity**

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these CDM class C1, HBM Class 0A devices.



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## Typical Performance Curves: $V_D$ = 5 V, $I_{DQ}$ = 1.3 A, $V_G$ = -1.05 ~ -0.85 V, $T_A$ = +25°C

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Frequency (GHz)

SCL Output Power (dBm)

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35

30

1 Efficiency (%)

15 pp

10 Jano

5

0

35

30

25 20 1 Efficiency (%)

15 pappe

10 over

5

0

35

30

- Added Efficiency (%)

ower

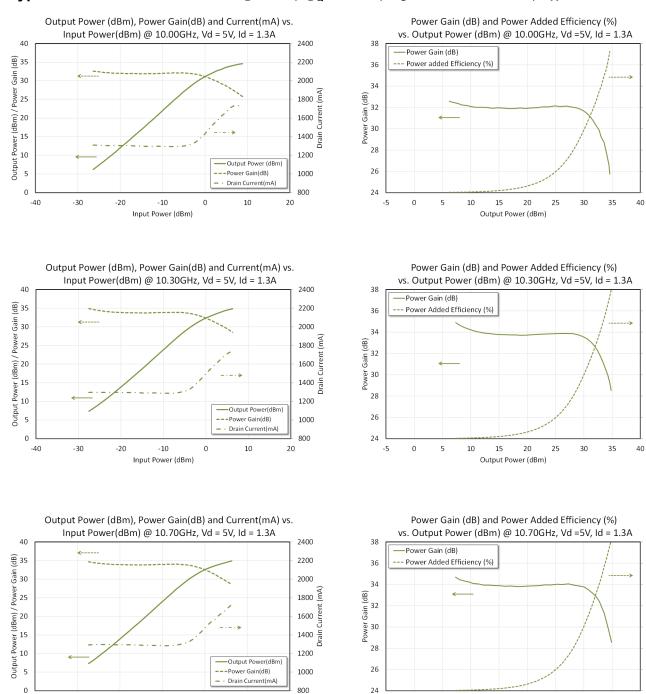
10

5

0

40

35



## Typical Performance Curves: $V_D = 5 V$ , $I_{DQ} = 1.3 A$ , $V_G = -1.05 \sim -0.85 V$ , $T_A = +25^{\circ}C$

6

-40

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-5

0

5

15

Output Power (dBm)

10

20

25

30

-30

-20

-10

Input Power (dBm)

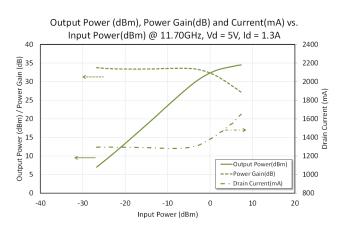
0

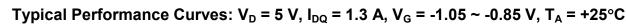
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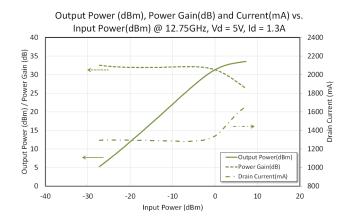


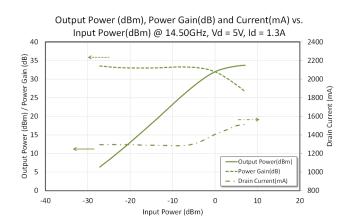
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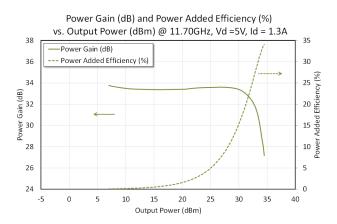
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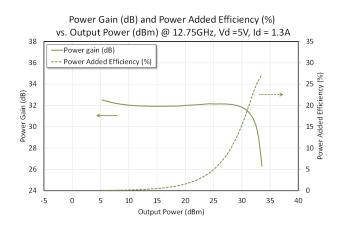


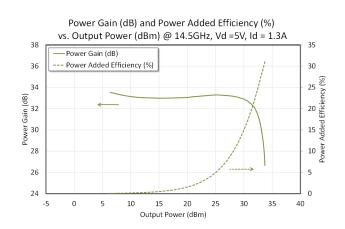












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#### Output Power (dBm), Power Gain(dB) and Current(mA) vs. Power Gain (dB) and Power Added Efficiency (%) Input Power(dBm) @ 15.35GHz, Vd = 5V, Id = 1.3A vs. Output Power (dBm) @ 15.35GHz, Vd =5V, Id = 1.3A 40 2400 38 35 -Power Gain (dB) କ୍<u></u> 35 2200 36 Power Added Efficiency (%) 30 08 Gain 25 25 d Efficiency (%) 2000 34 1800 E Power Power Power Gain (dB) 05 25 25 /(ugp)/ 1600 15 Tower Added E 15 1400 - 🗄 Output Power 28 10 1200 Output Power(dBr 26 5 5 ---Power Gain(dB) 1000 Drain Current(mA) 0 800 24 0 -40 -30 -10 0 10 20 -20 -5 0 5 10 15 20 25 30 35 40 Input Power (dBm) Output Power (dBm) Output Power (dBm), Power Gain(dB) and Current(mA) vs. Power Gain (dB) and Power Added Efficiency (%) Input Power(dBm) @ 15.50GHz, Vd = 5V, Id = 1.3A vs. Output Power (dBm) @ 15.5GHz, Vd =5V, Id = 1.3A 40 2400 38 35 –Power Gain (dB) କ୍<u></u>ପି 35 2200 -- Power Added Efficiency (% 36 30 08 Gain 25 20 25 Added Efficiency (%) 2000 34 Power Gain (dB) 05 25 25 Power Power 1800 E 1/(ugp) 1600 1400 . Ugi 15 Output Power 10 Mod 28 10 1200 Output Power(dBm 26 5 5 ---Power Gain(dB) 1000 drain Current(mA) 0 800 24 0 -40 -30 -10 0 10 20 -5 0 5 10 15 20 25 30 35 40 -20 Input Power (dBm) Output Power (dBm) Peak Detected Voltage (V) vs. Output Power (dBm), Env Detected Voltage (V) vs. Output Power (dBm), Vd = 5V, Id = 1.3A Vd = 5V, Id = 1.3A 5 4 Freq = 10.00GHz Freq = 10.00GHz Freq = 10.30GHz Freq = 10.70GHz Freg = 10.30GHzFreq = 10.70GHz 4 3 Peak Detected Voltage (V) -Freq = 11.70GHz -Freq = 12.75GHz -Freq = 11.70GHz -Freq = 12.75GHz Env Detected Voltage (V) Freq = 13.25GHz Freq = 14.50GHz Freq = 13.25GHz Freq = 14.50GHz 2 -Freq = 15.35GHz -Freq = 15.50GHz -Freq = 15.35GHz Freq = 15.50GHz 1 0 0 -1 0 5 10 15 20 25 30 35 40 0 5 10 15 20 25 30 35 40 Output Power (dBm) Output Power (dBm) MACOM Technology Solutions Inc. (MACOM) and its affiliates reserve the right to make changes to the product(s) or information contained herein without notice.

## Typical Performance Curves: $V_D = 5 V$ , $I_{DQ} = 1.3 A$ , $V_G = -1.05 \sim -0.85 V$ , $T_A = +25^{\circ}C$



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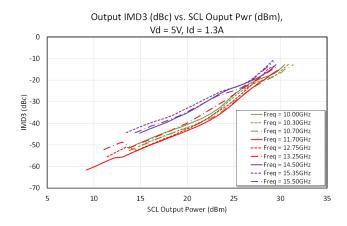
For further information and support please visit: https://www.macom.com/support

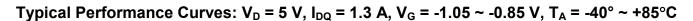


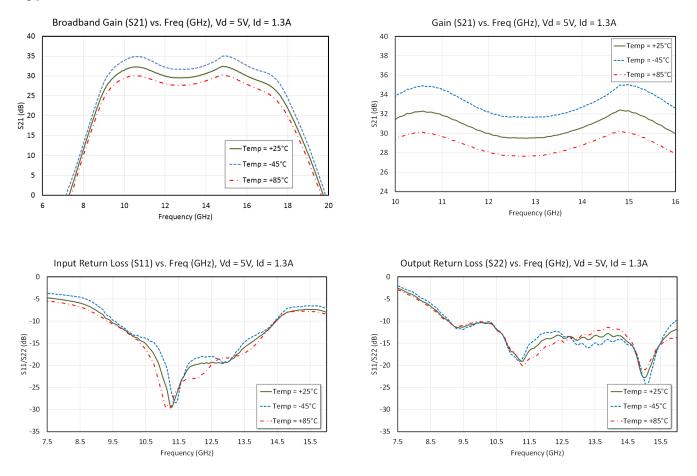
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## Typical Performance Curves: $V_D = 5 V$ , $I_{DQ} = 1.3 A$ , $V_G = -1.05 \sim -0.85 V$ , $T_A = +25^{\circ}C$





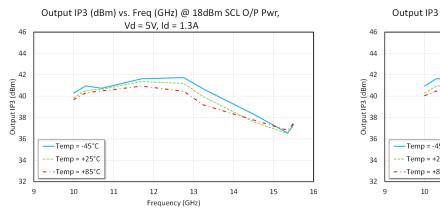


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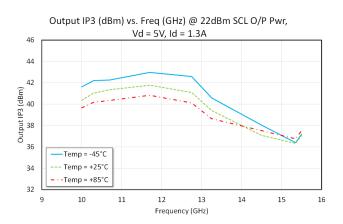


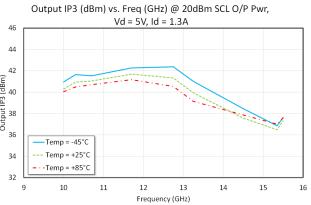
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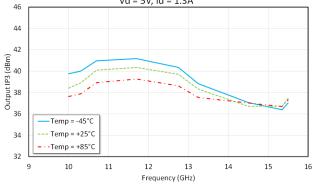


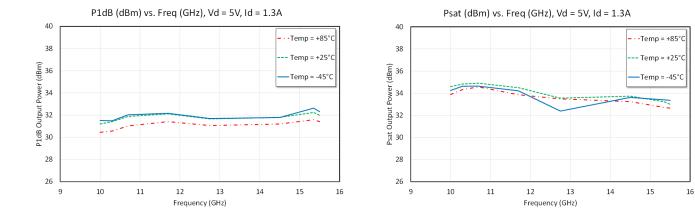
### Typical Performance Curves: $V_D = 5 V$ , $I_{DQ} = 1.3 A$ , $V_G = -1.05 \sim -0.85 V$ , $T_A = -40^\circ \sim +85^\circ C$





Output IP3 (dBm) vs. Freq (GHz) @ 25dBm SCL O/P Pwr, Vd = 5V, Id = 1.3A



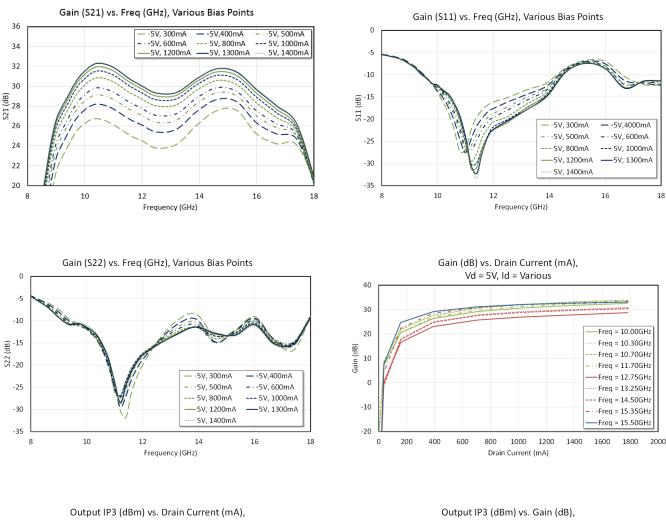


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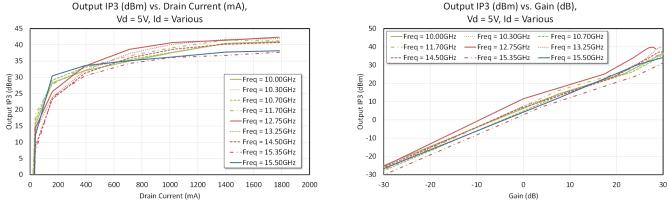


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## Typical Performance Curves: $V_D$ = 5 V, $I_{DQ}$ = Various, $V_G$ = -1.05 ~ -0.85 V, $T_A$ = +25°C



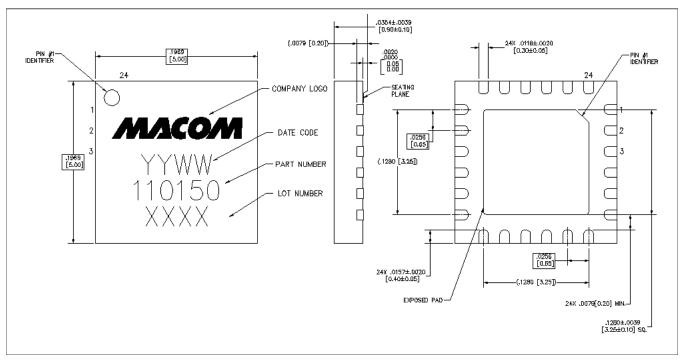
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### Lead-Free 5 mm 24-Lead PQFN



<sup>†</sup> Reference Application Note S2083 for lead-free solder reflow recommendations. Meets JEDEC moisture sensitivity level 3 requirements. Plating is NiPdAu

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